

FIG. 1A

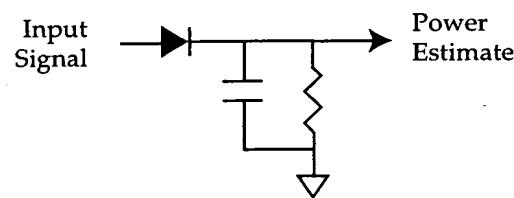


FIG. 1B

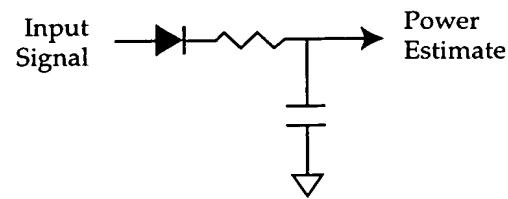


FIG. 1C

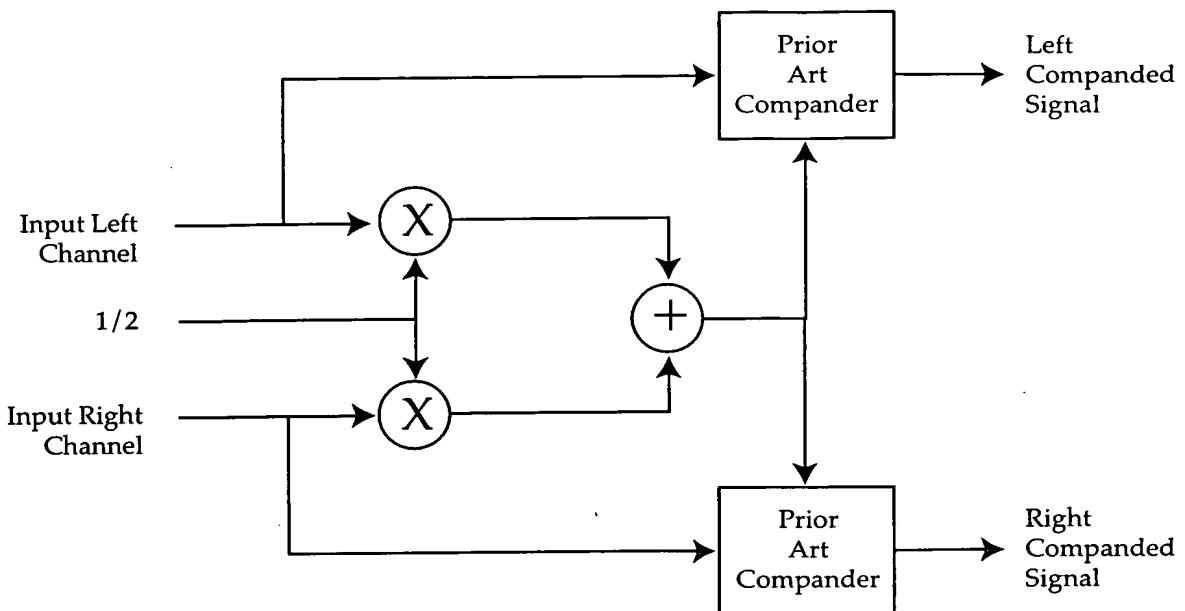


FIG. 1D

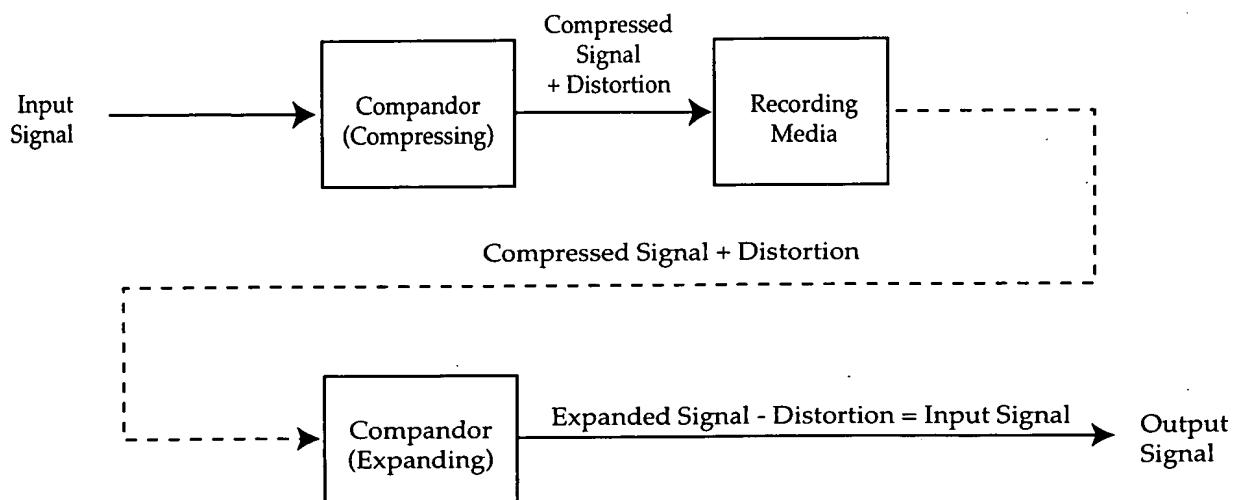


FIG. 1E

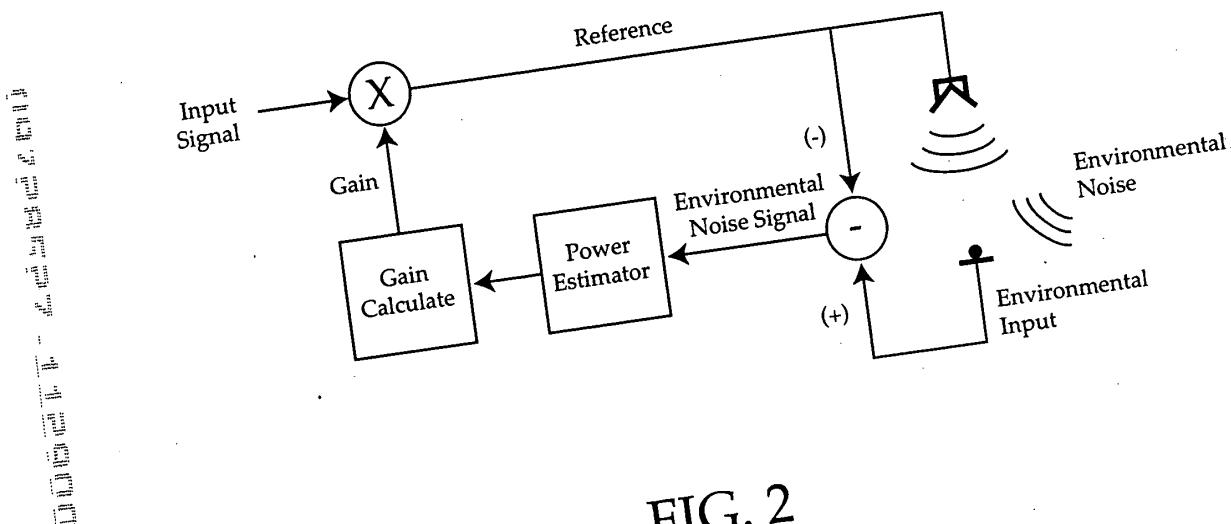


FIG. 2

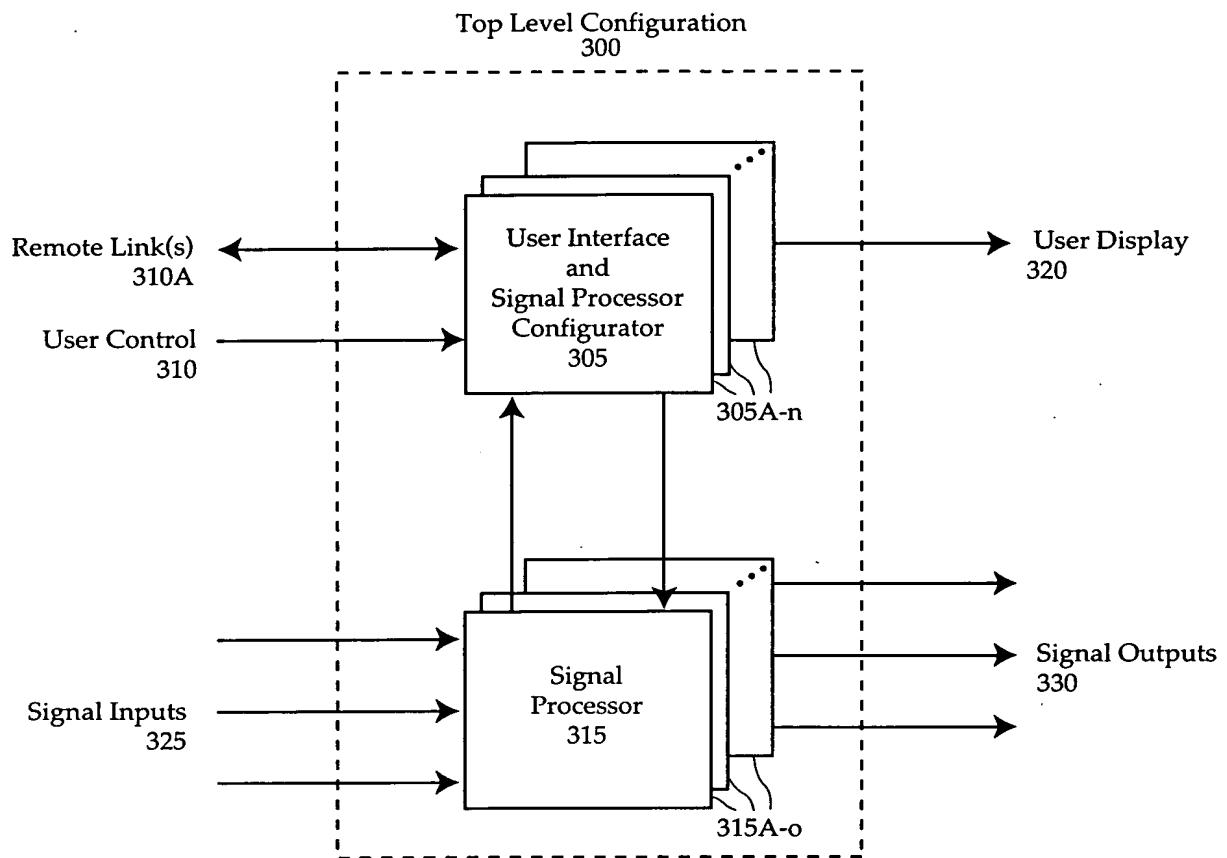


FIG. 3A

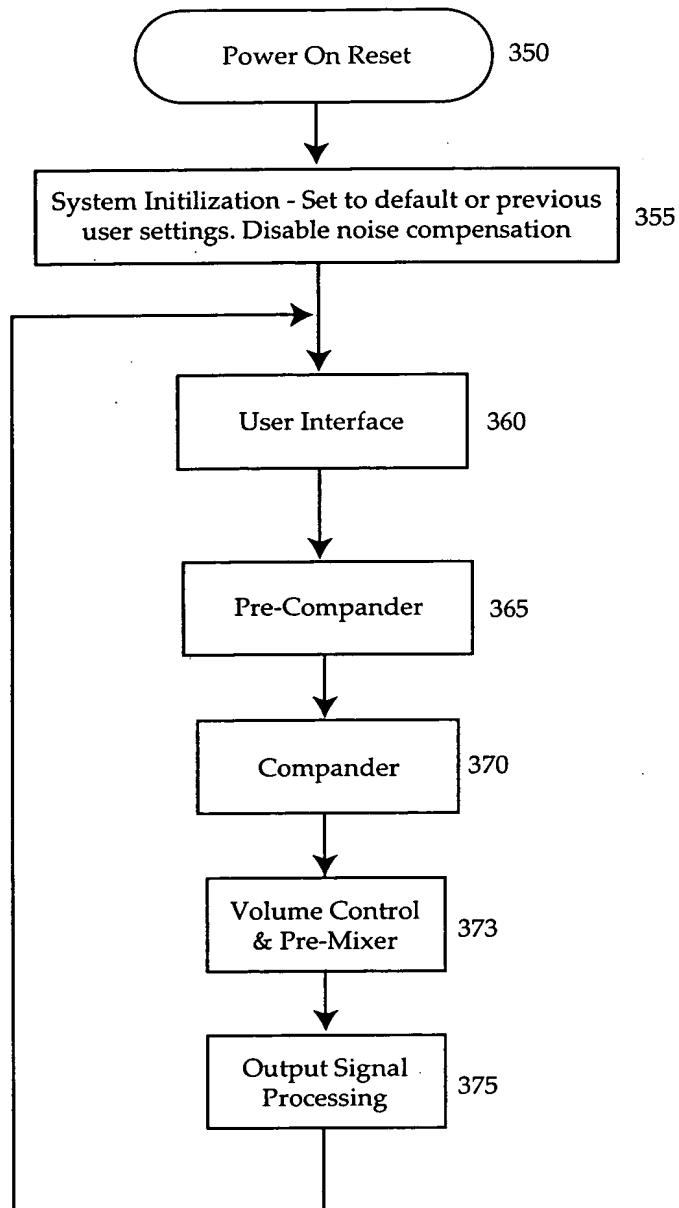


FIG. 3B

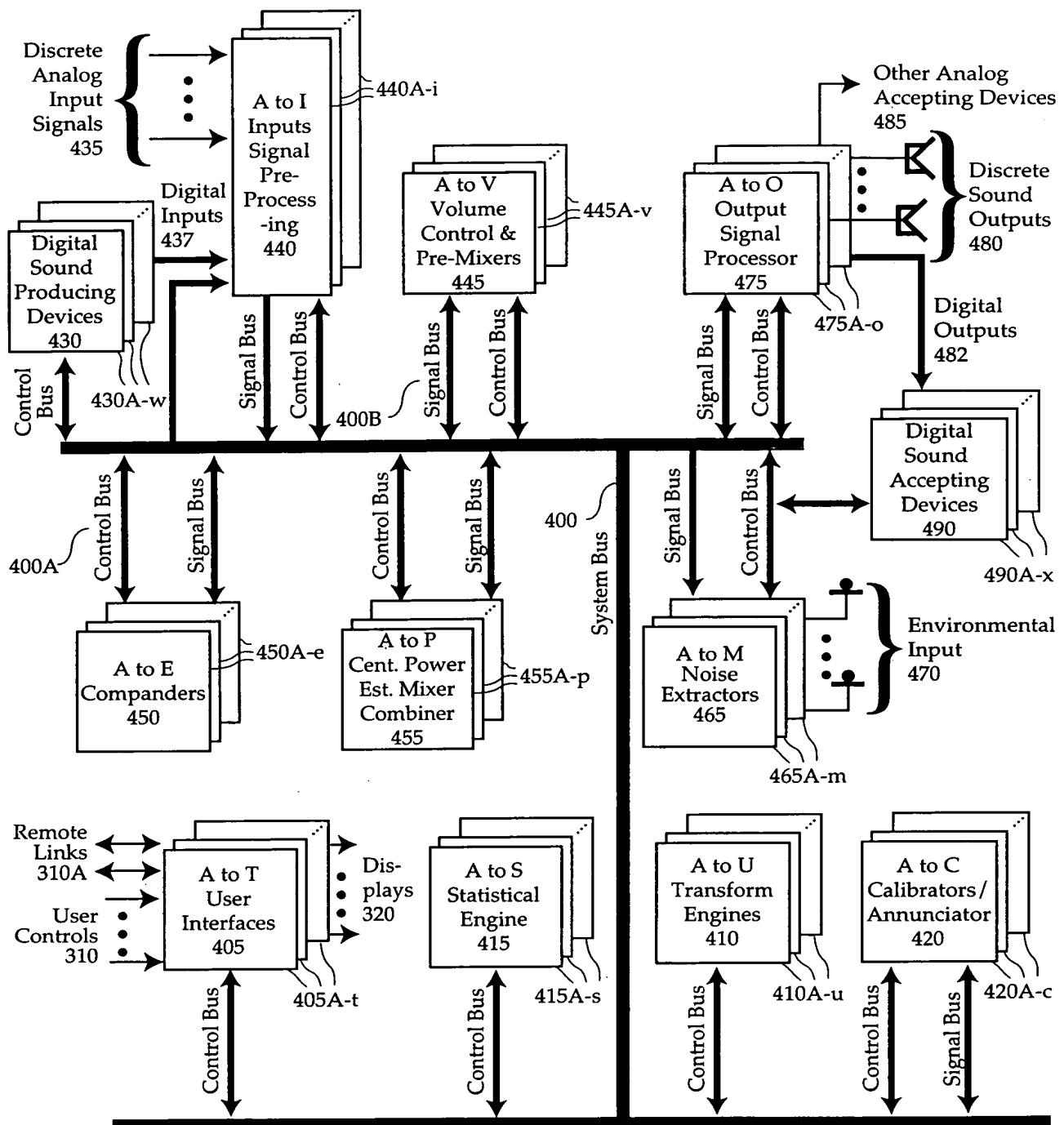


FIG. 4

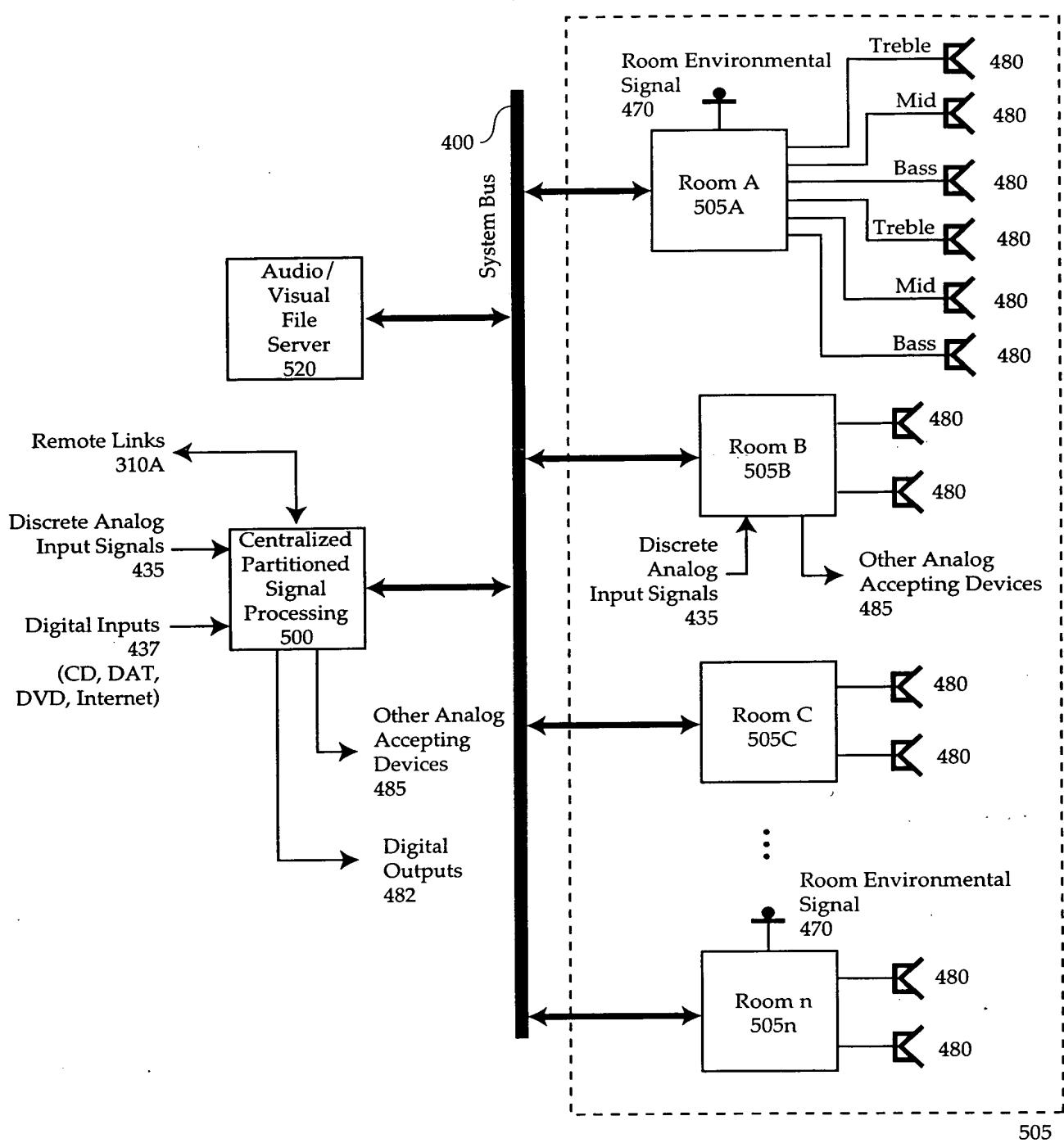


FIG. 5A

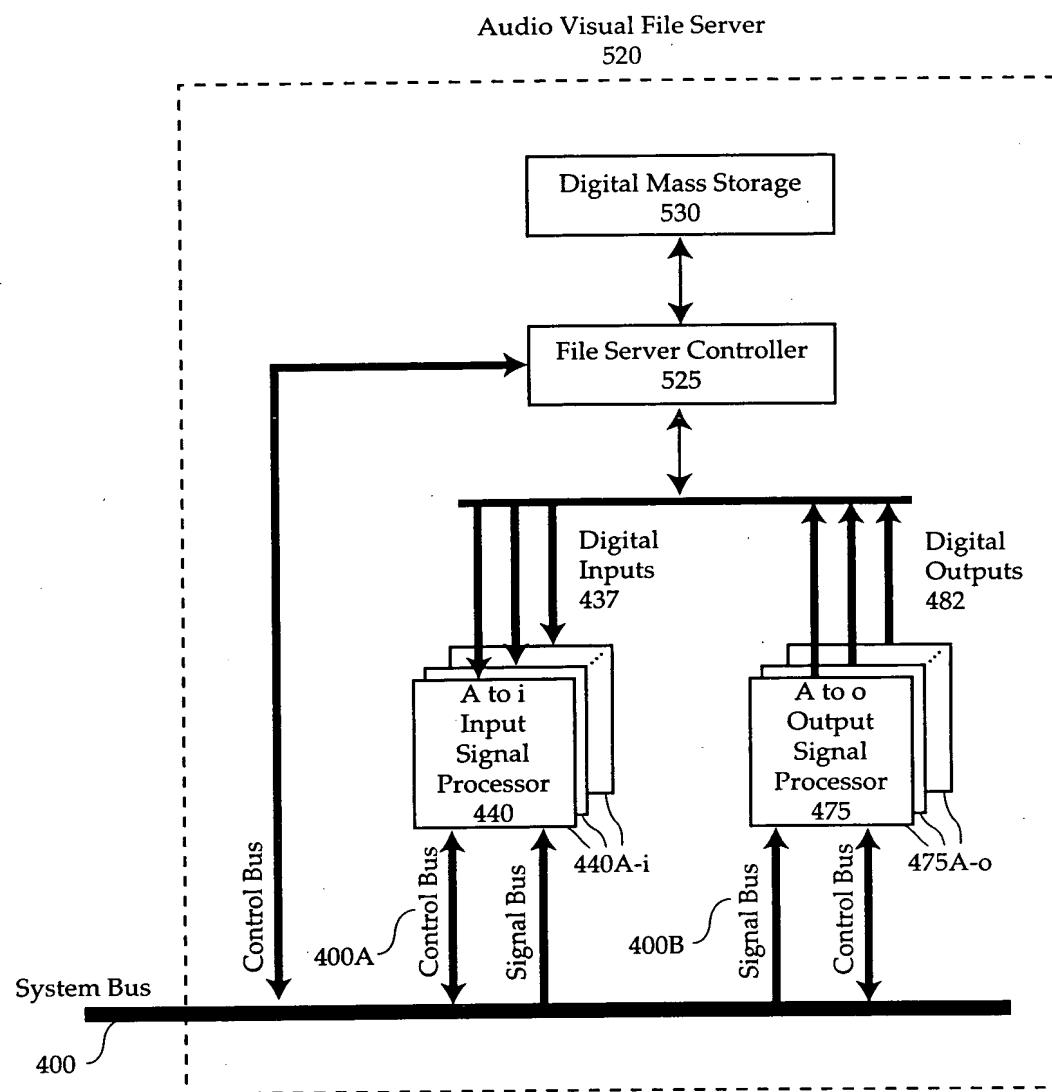


FIG. 5B

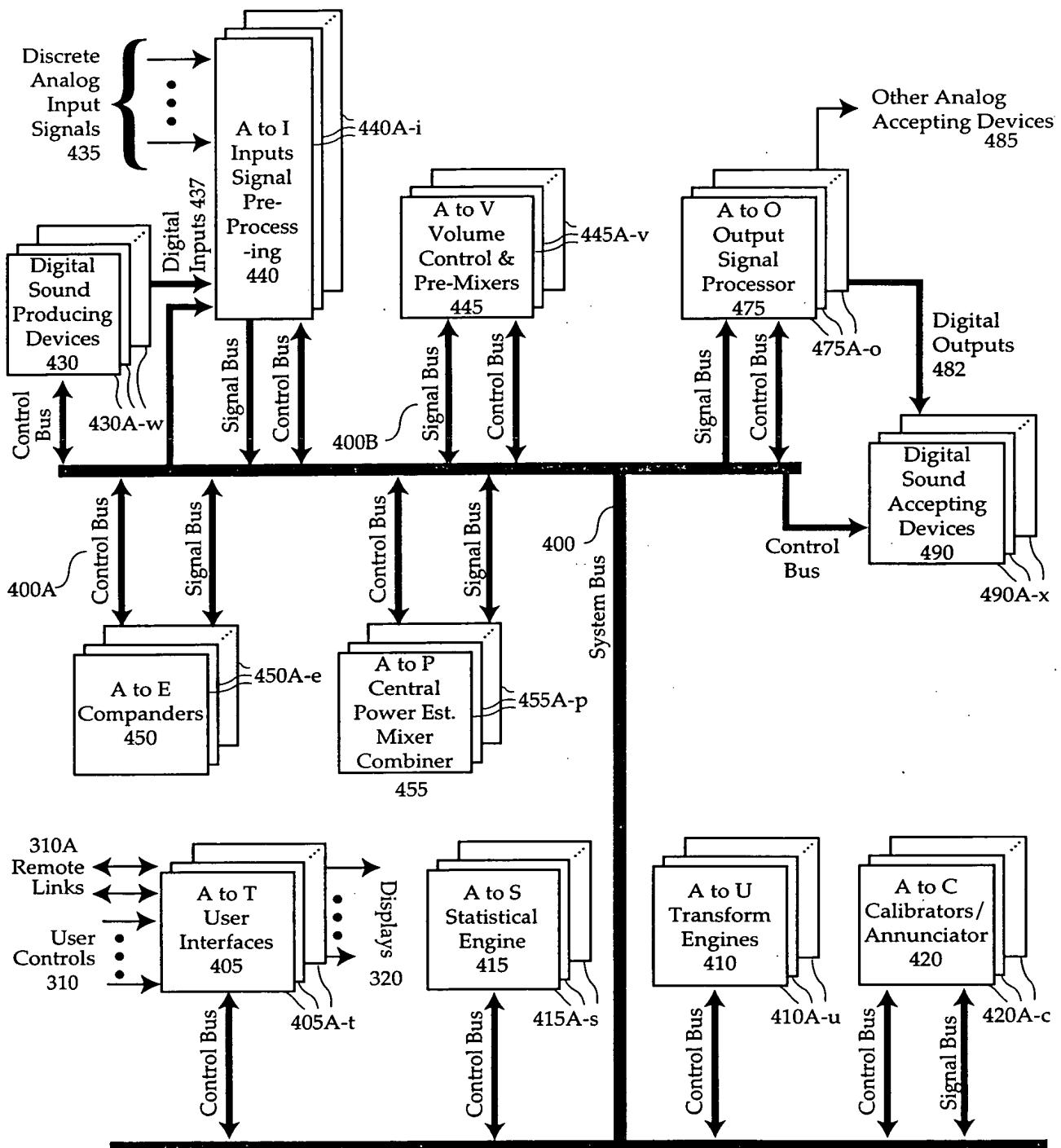


FIG. 5C

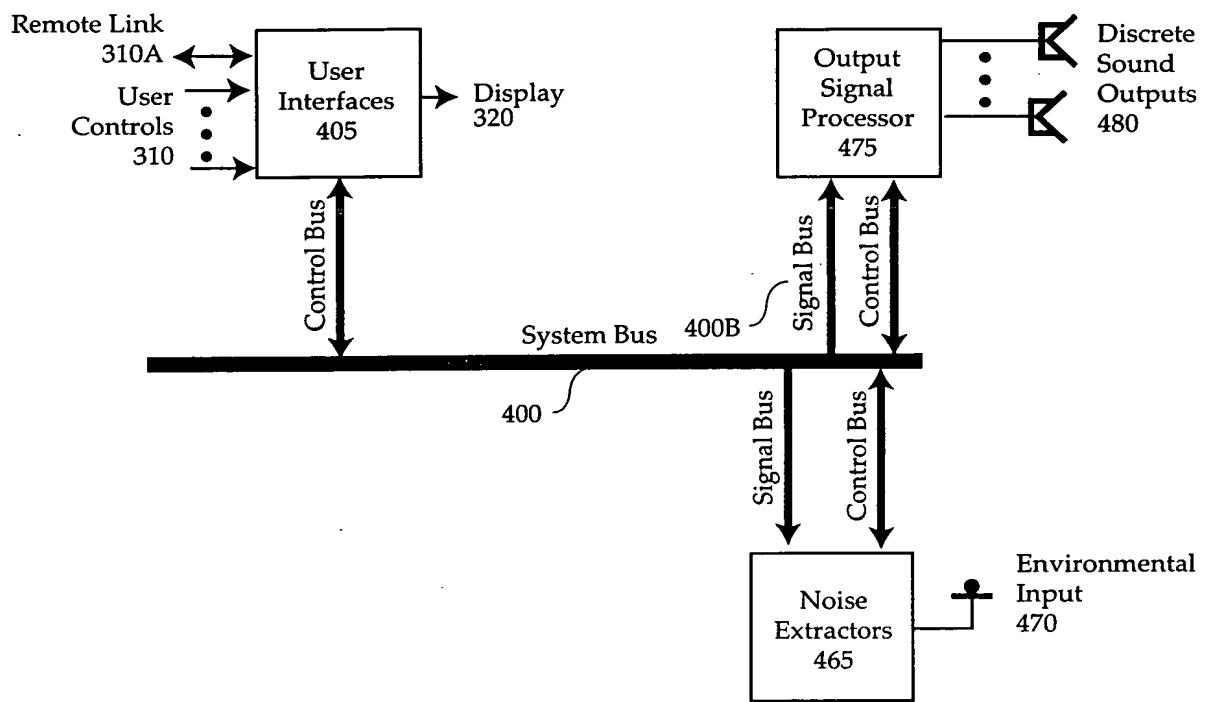


FIG. 5D

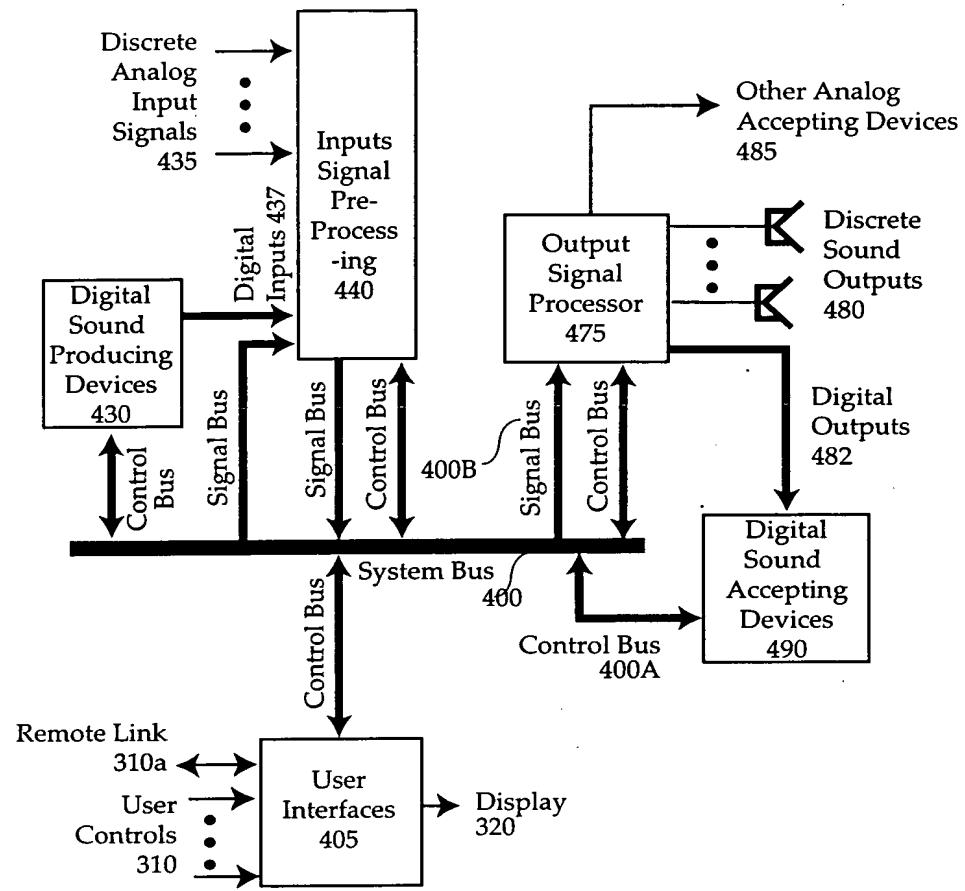


FIG. 5E

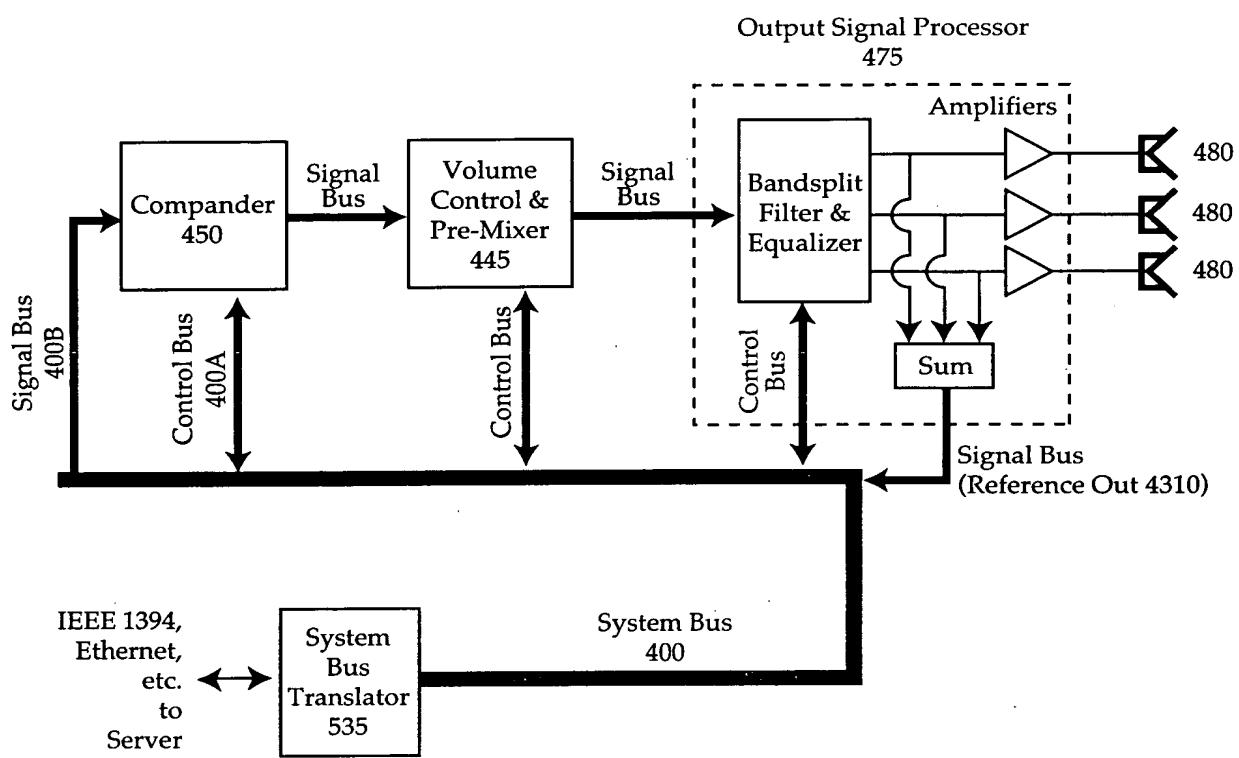


FIG. 5F

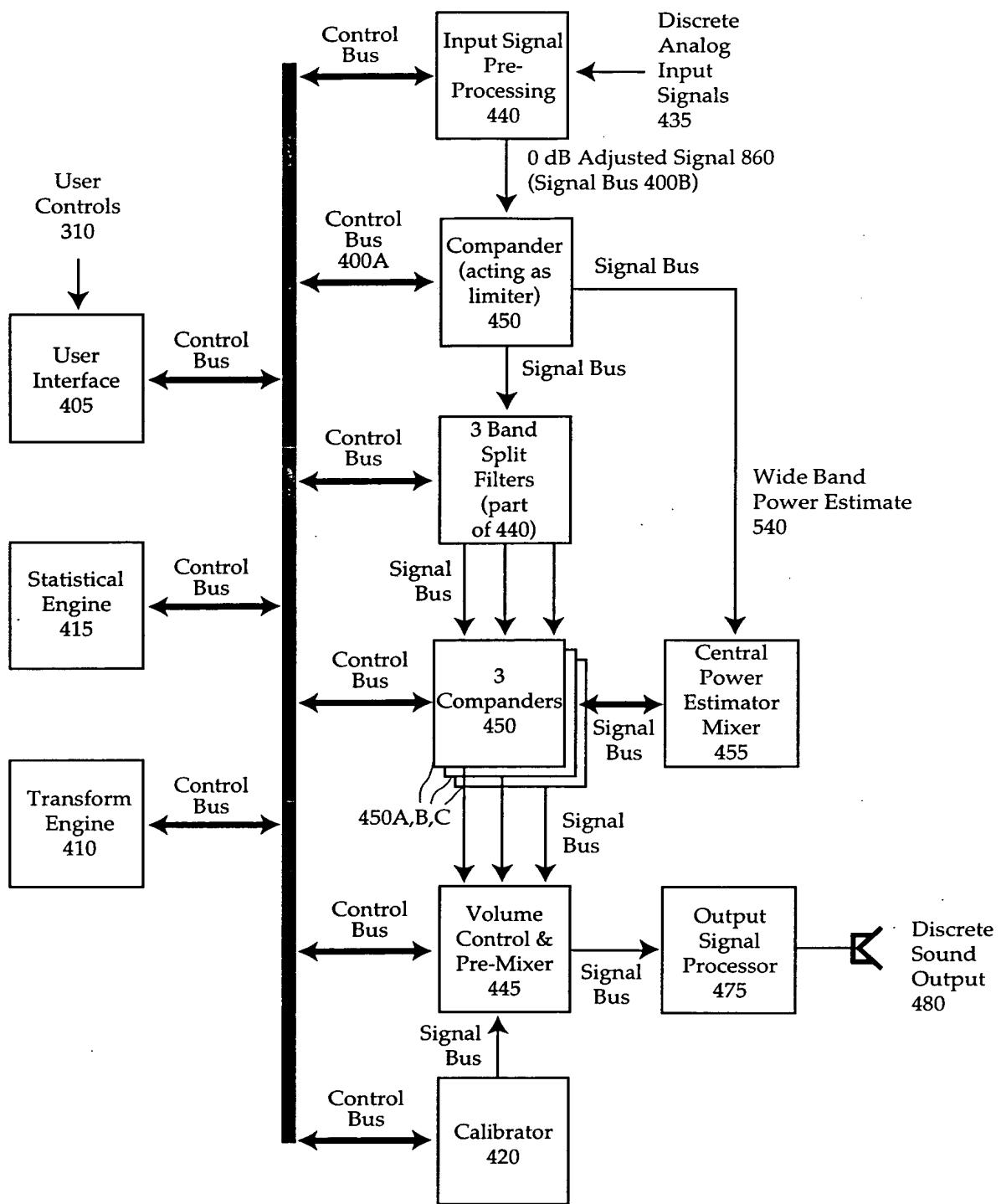


FIG. 5G

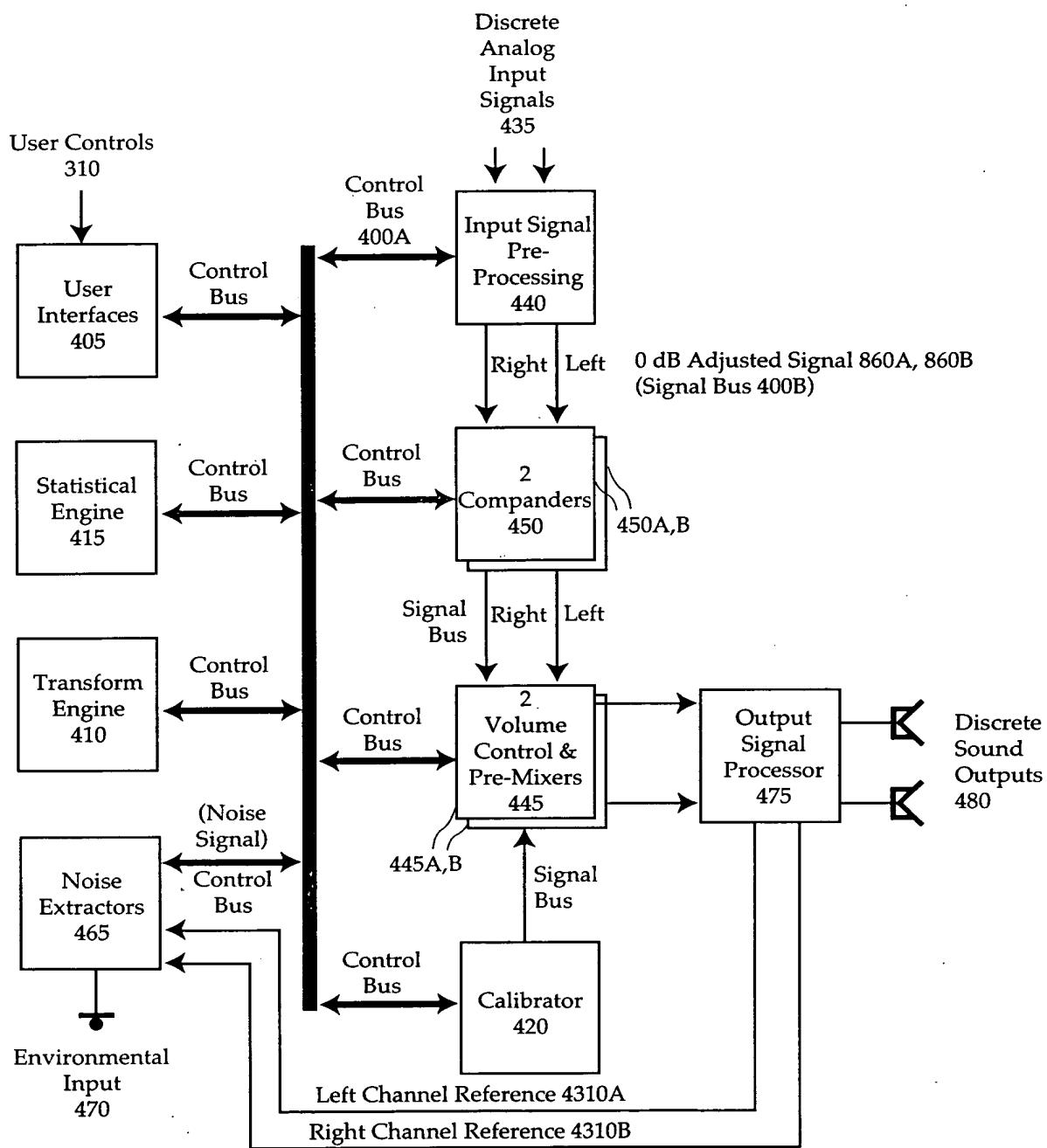


FIG. 5H

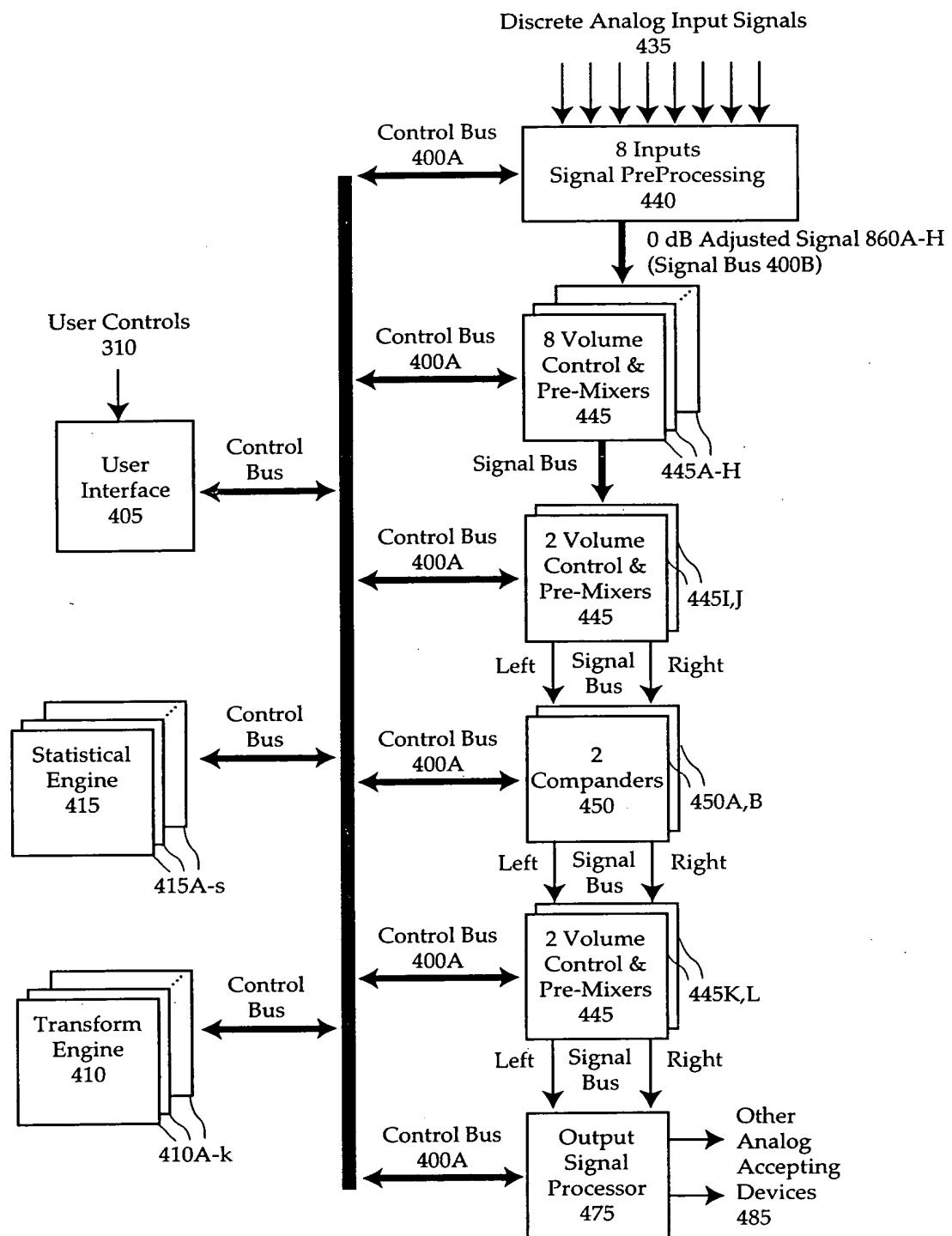


FIG. 5I

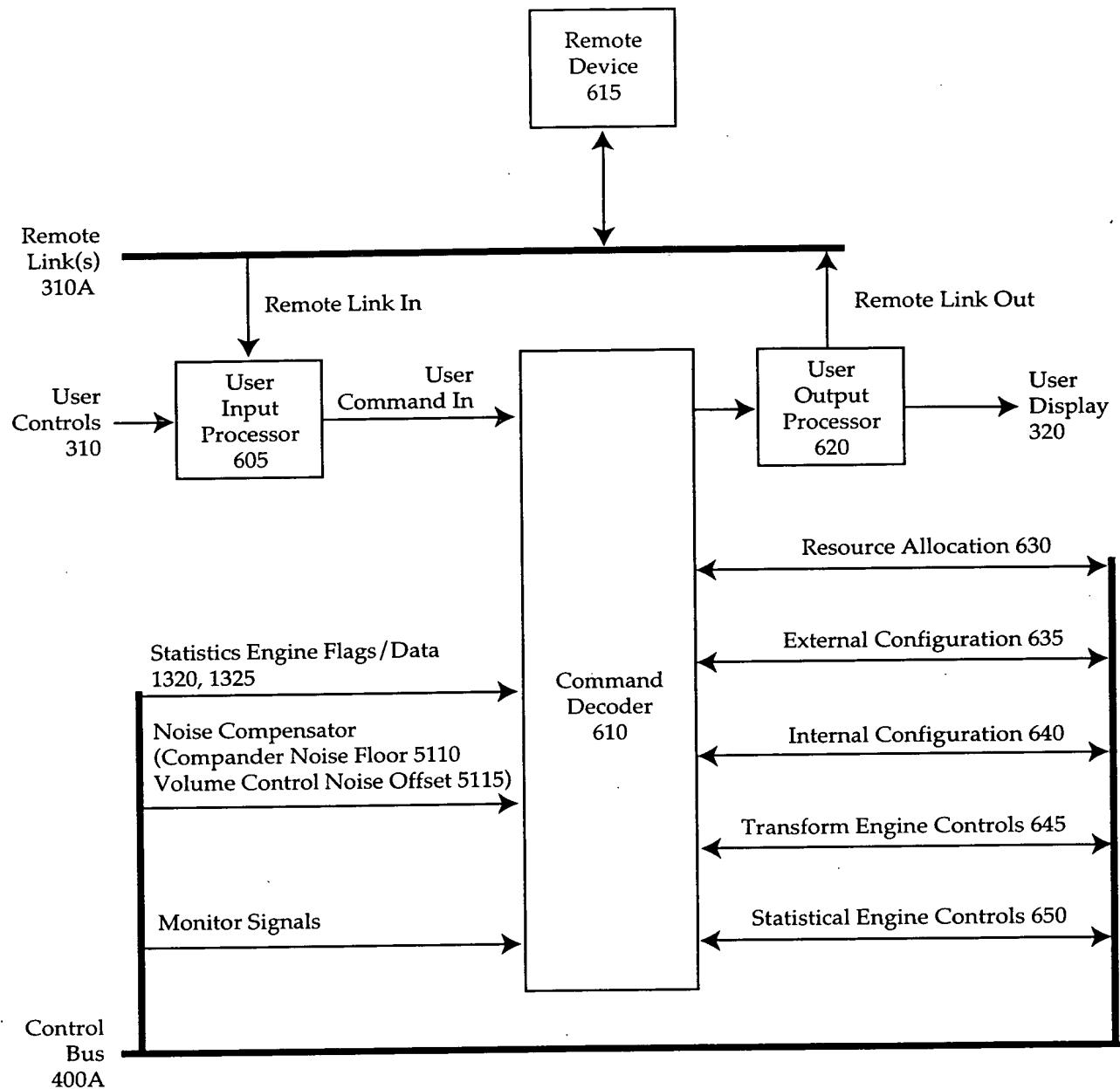


FIG. 6A

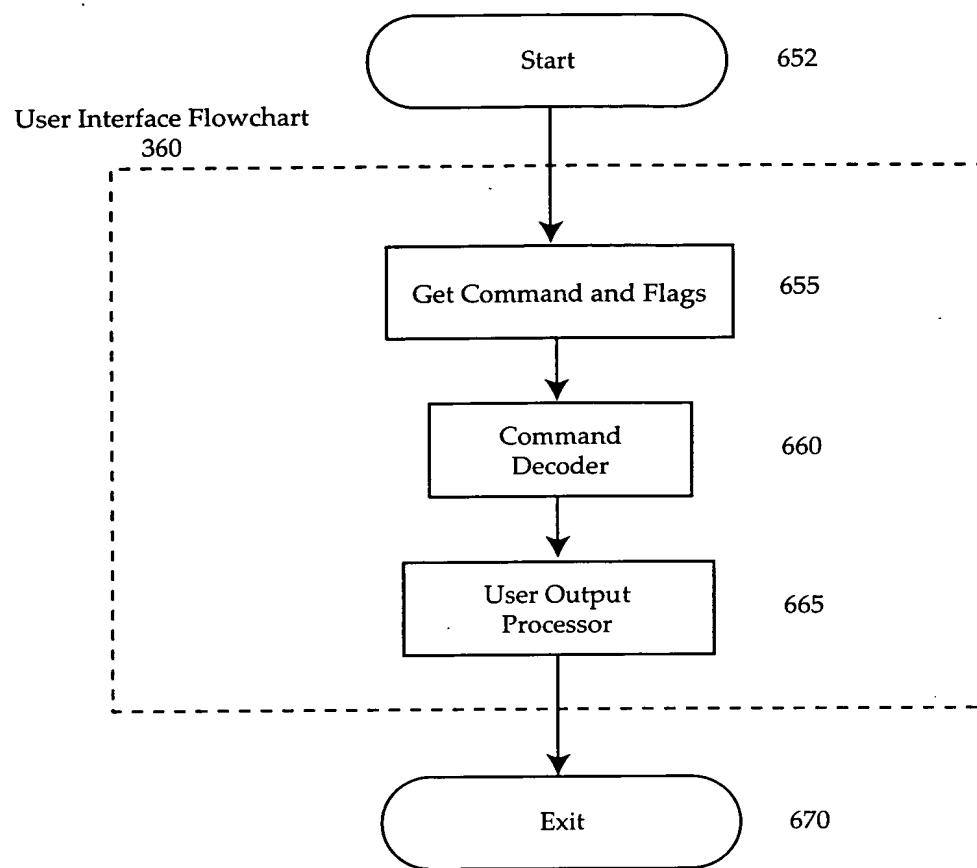


FIG. 6B

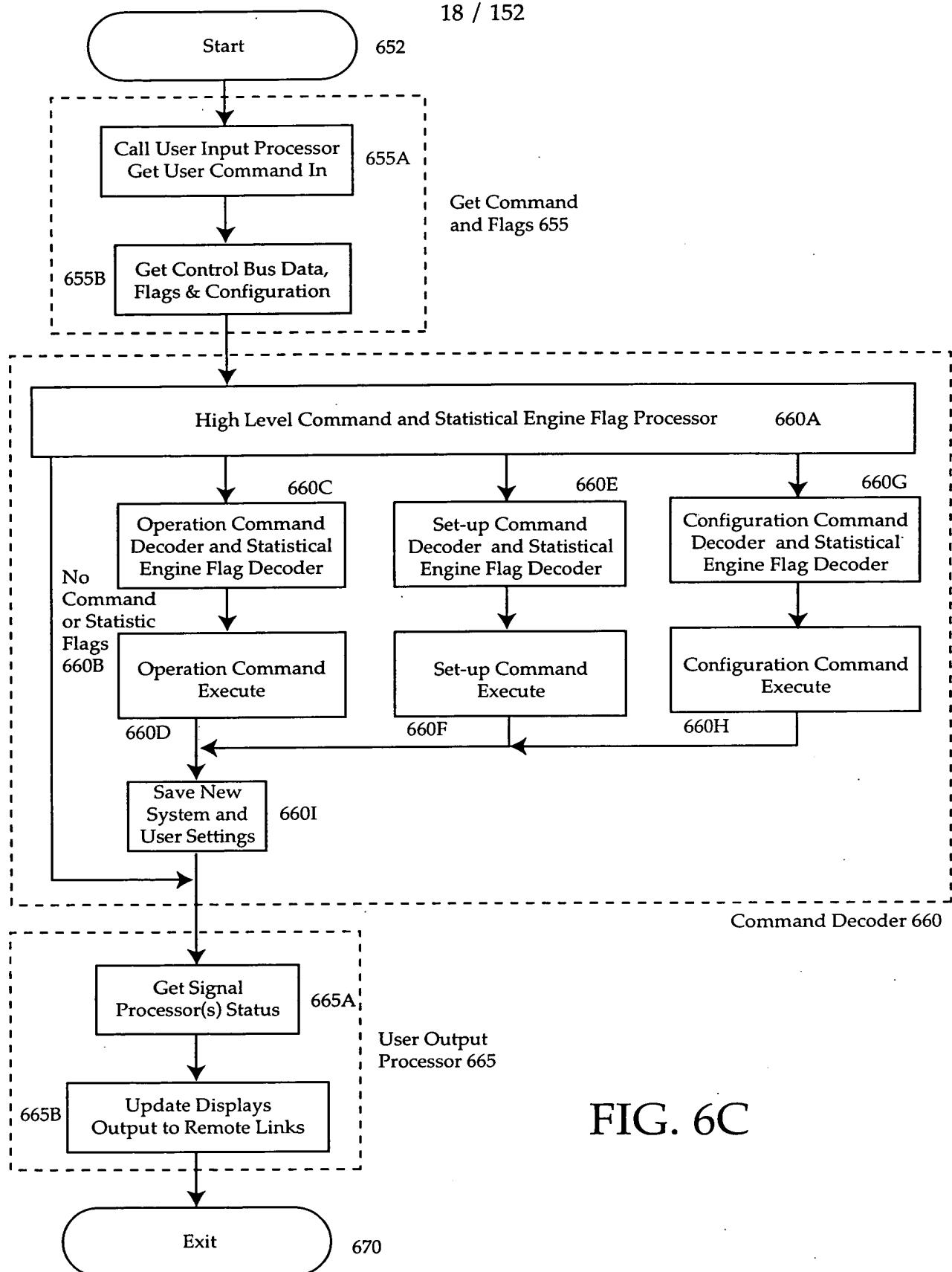


FIG. 6C

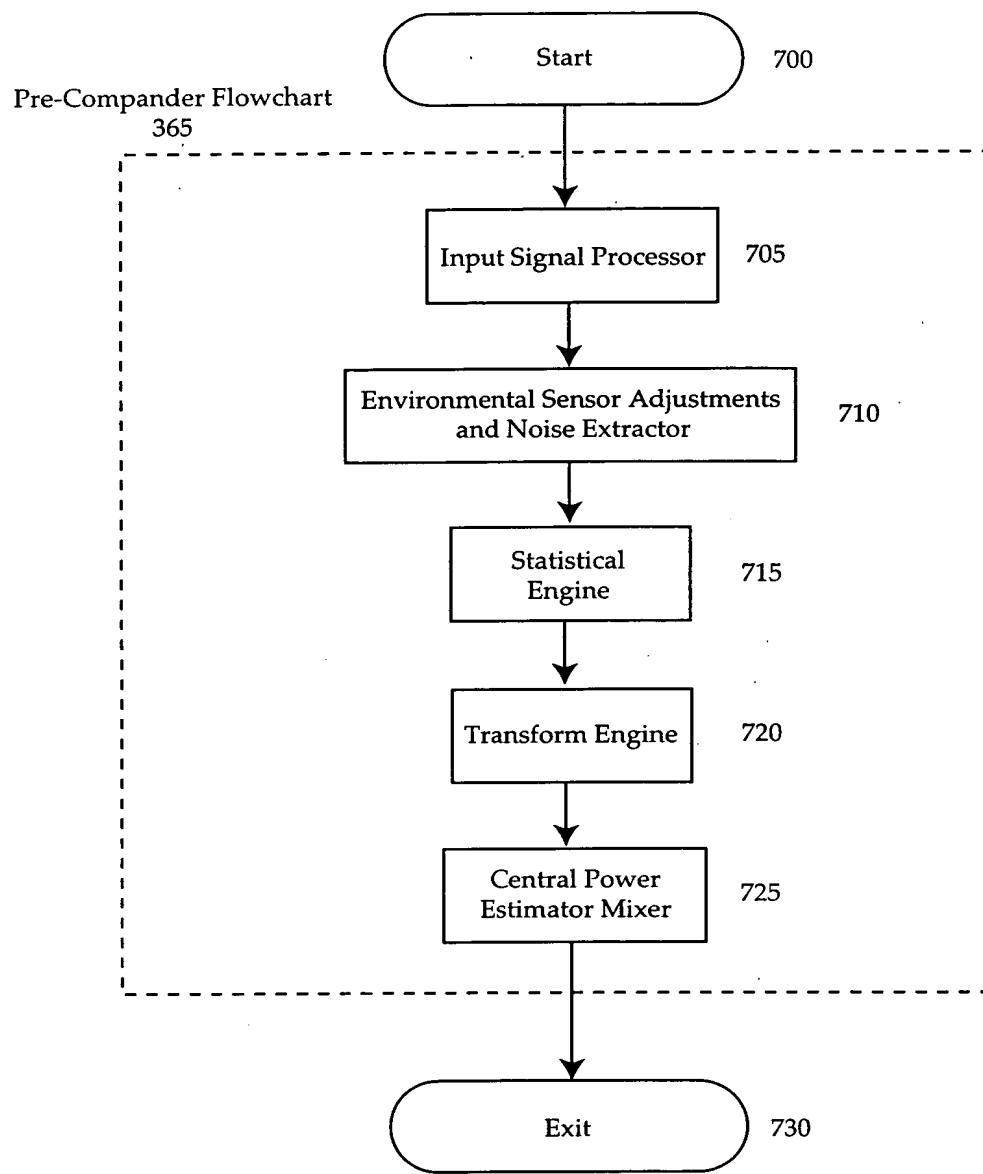


FIG. 7

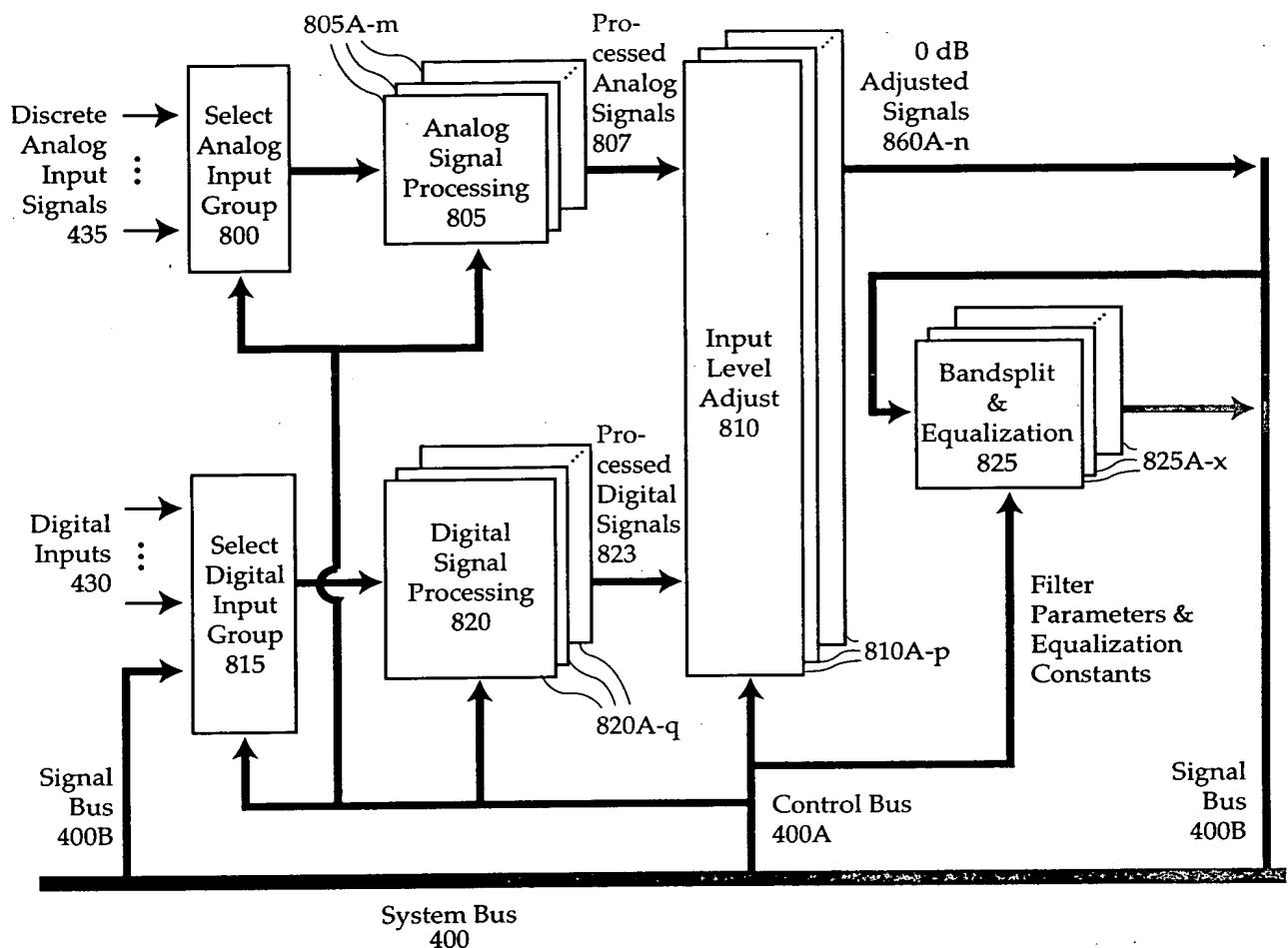


FIG. 8A

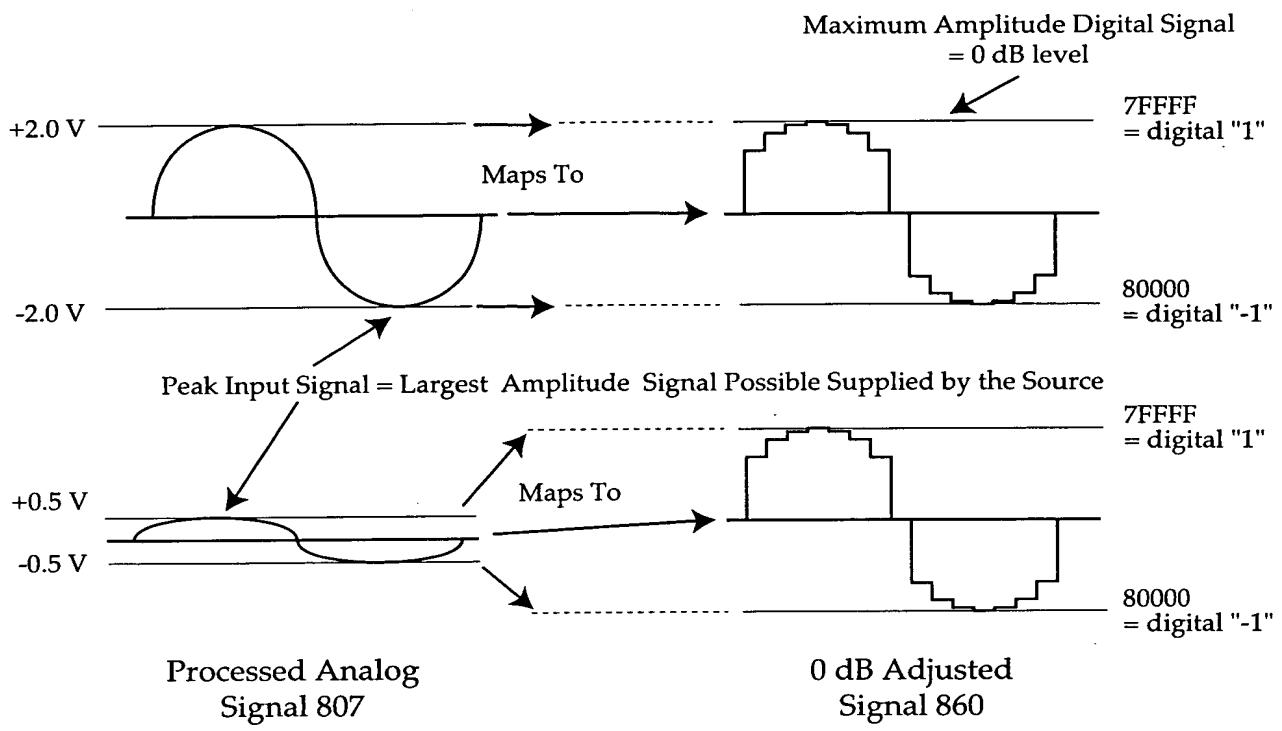


FIG. 8B

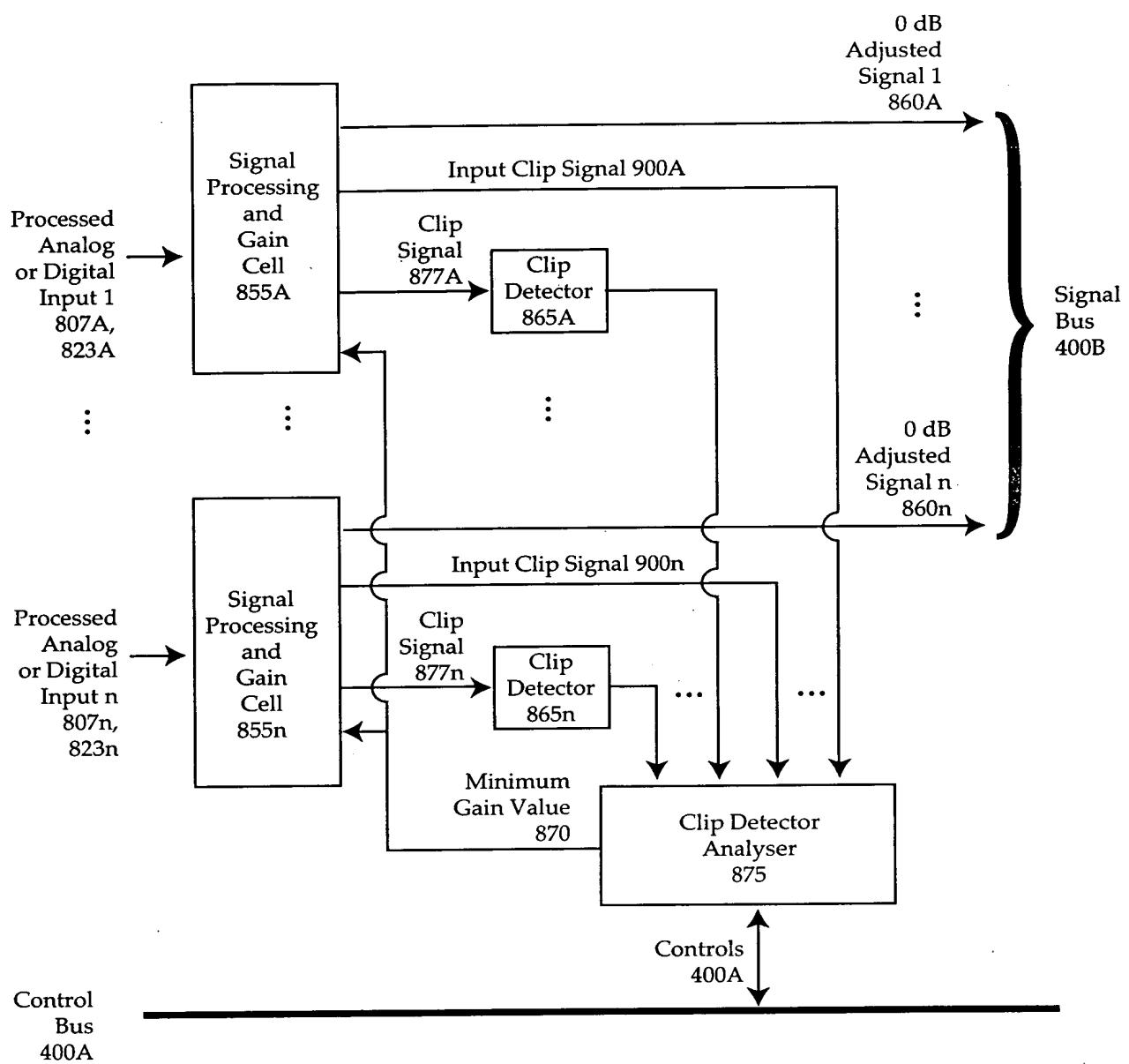


FIG. 8C

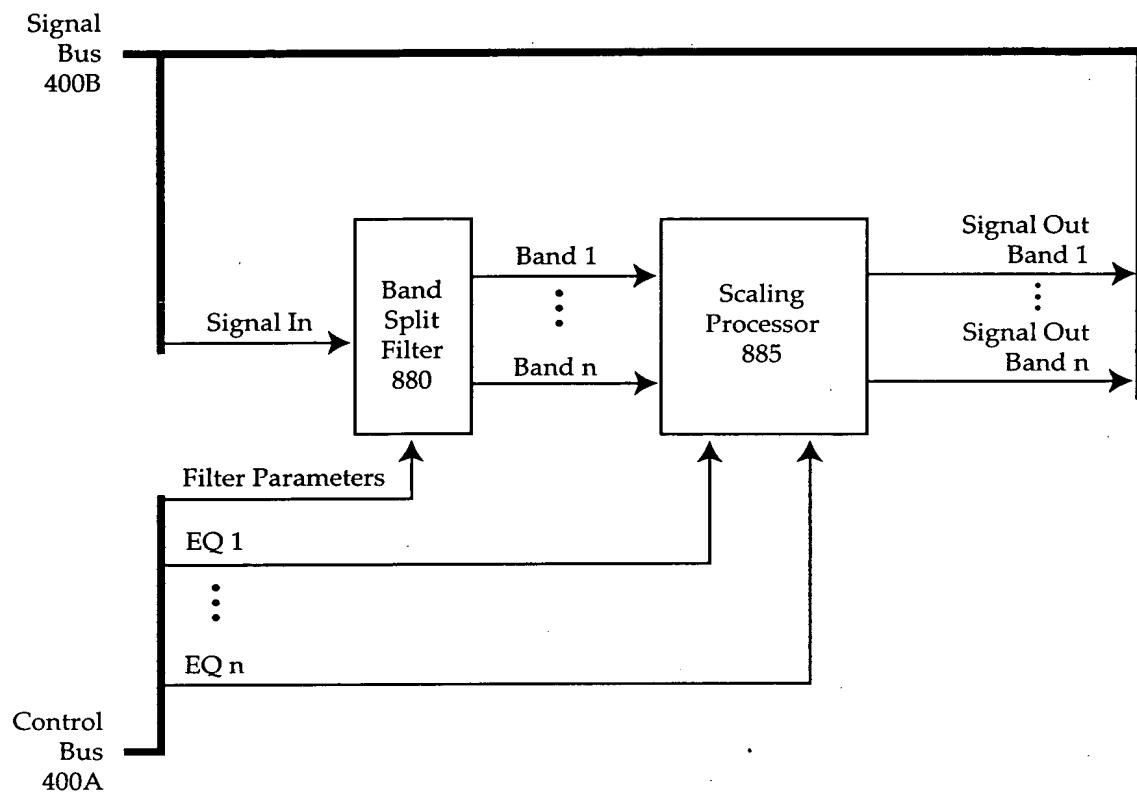


FIG. 8D

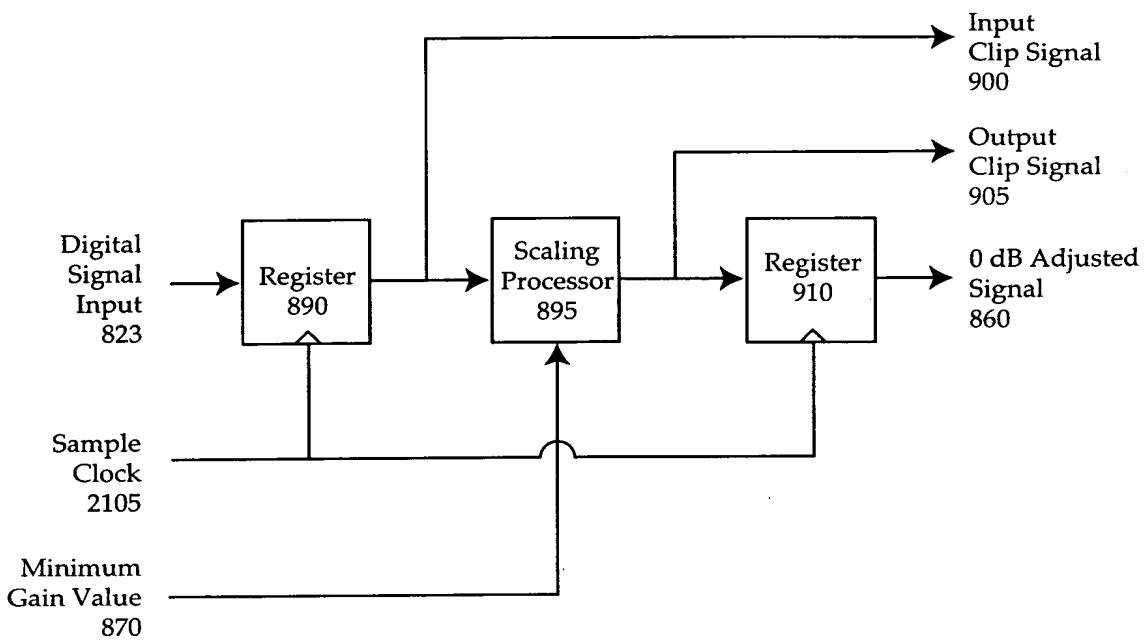


FIG. 8E

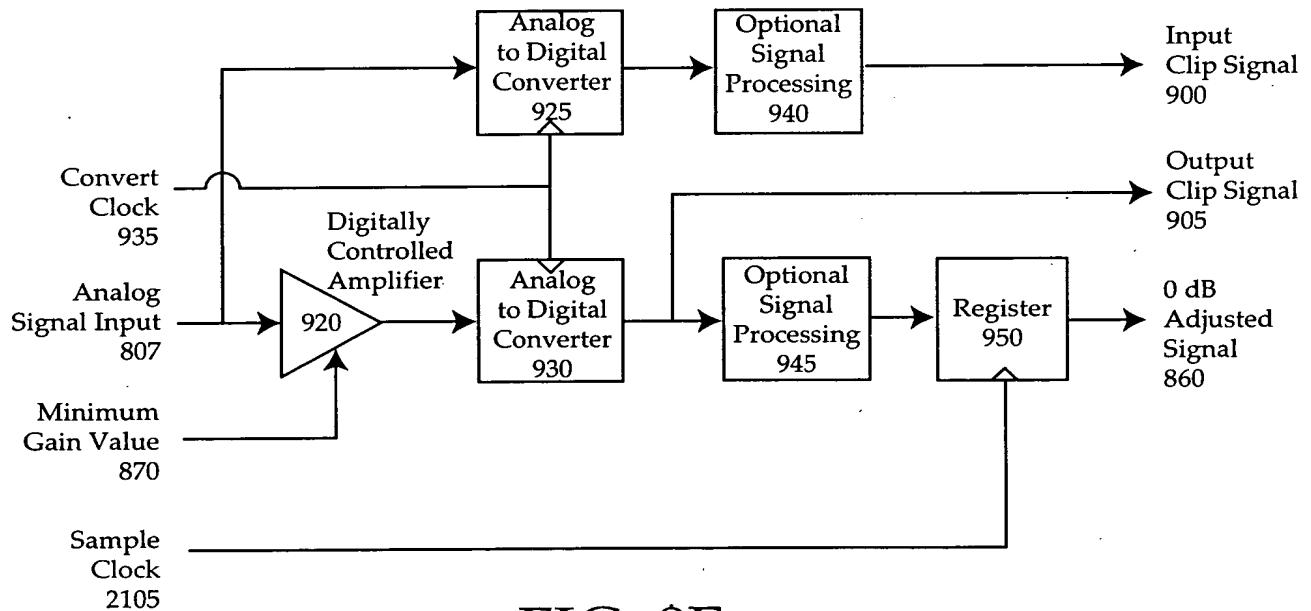


FIG. 8F

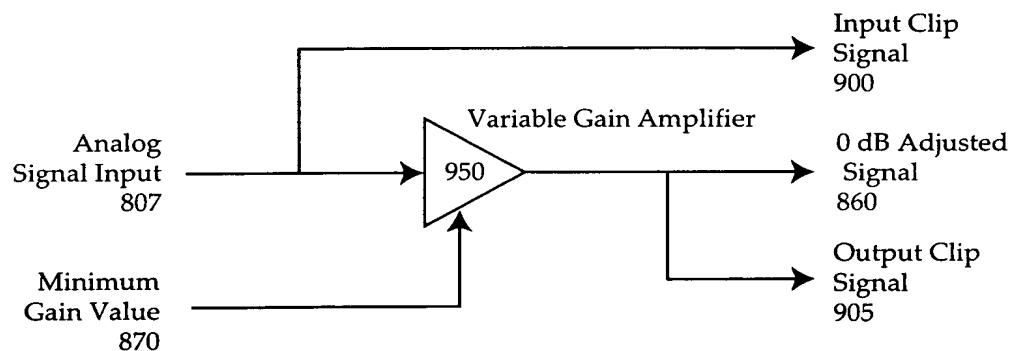


FIG. 8G

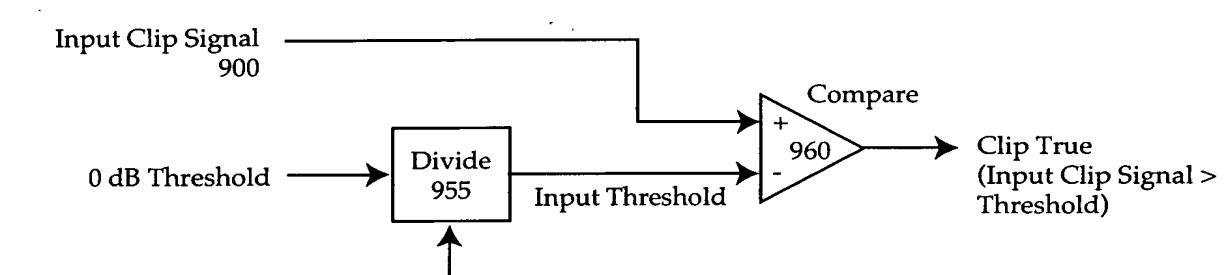


FIG. 9A

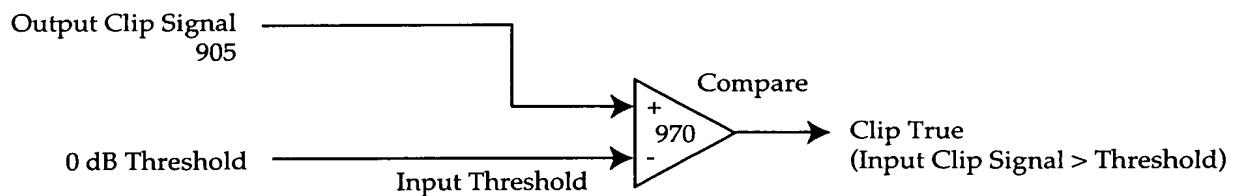


FIG. 9B

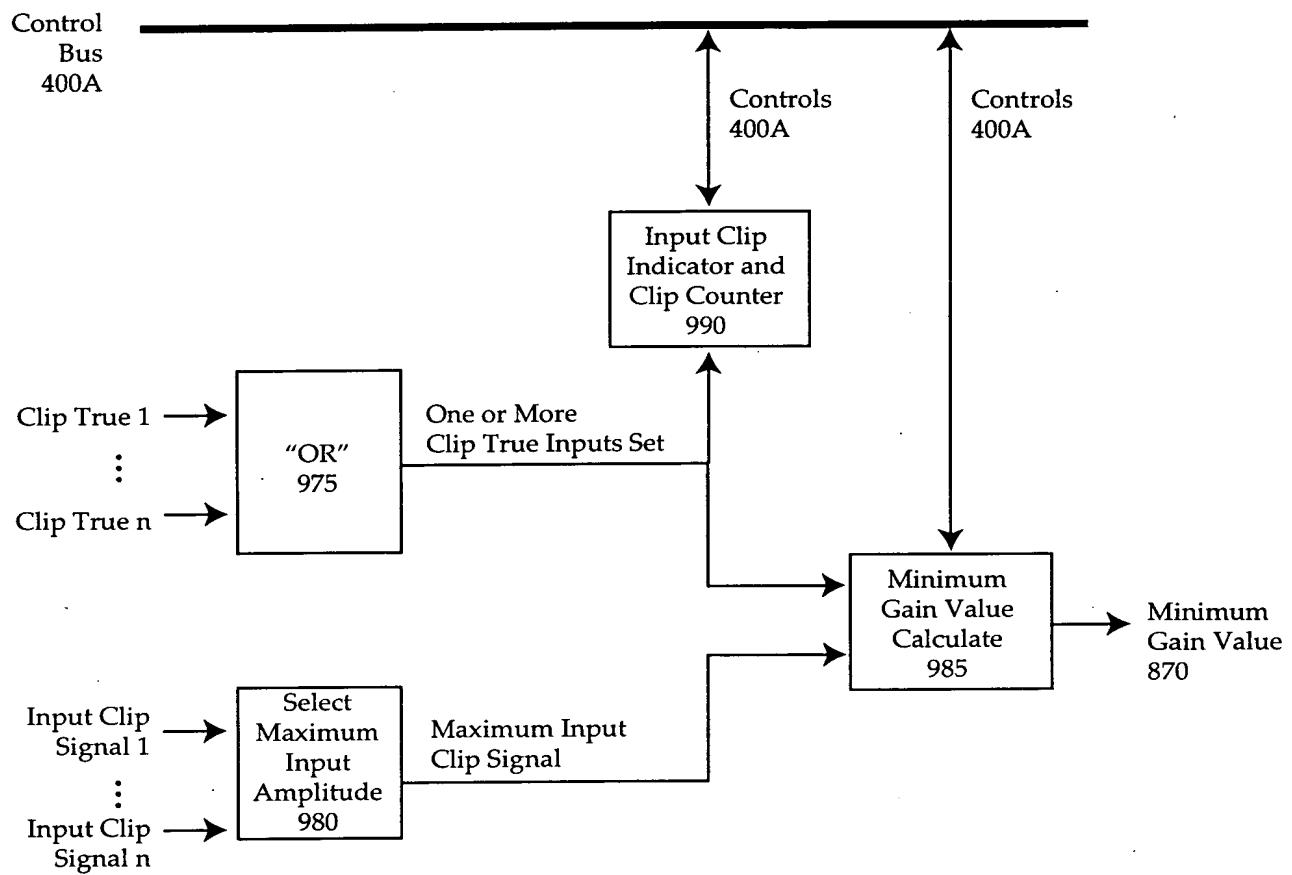


FIG. 9C

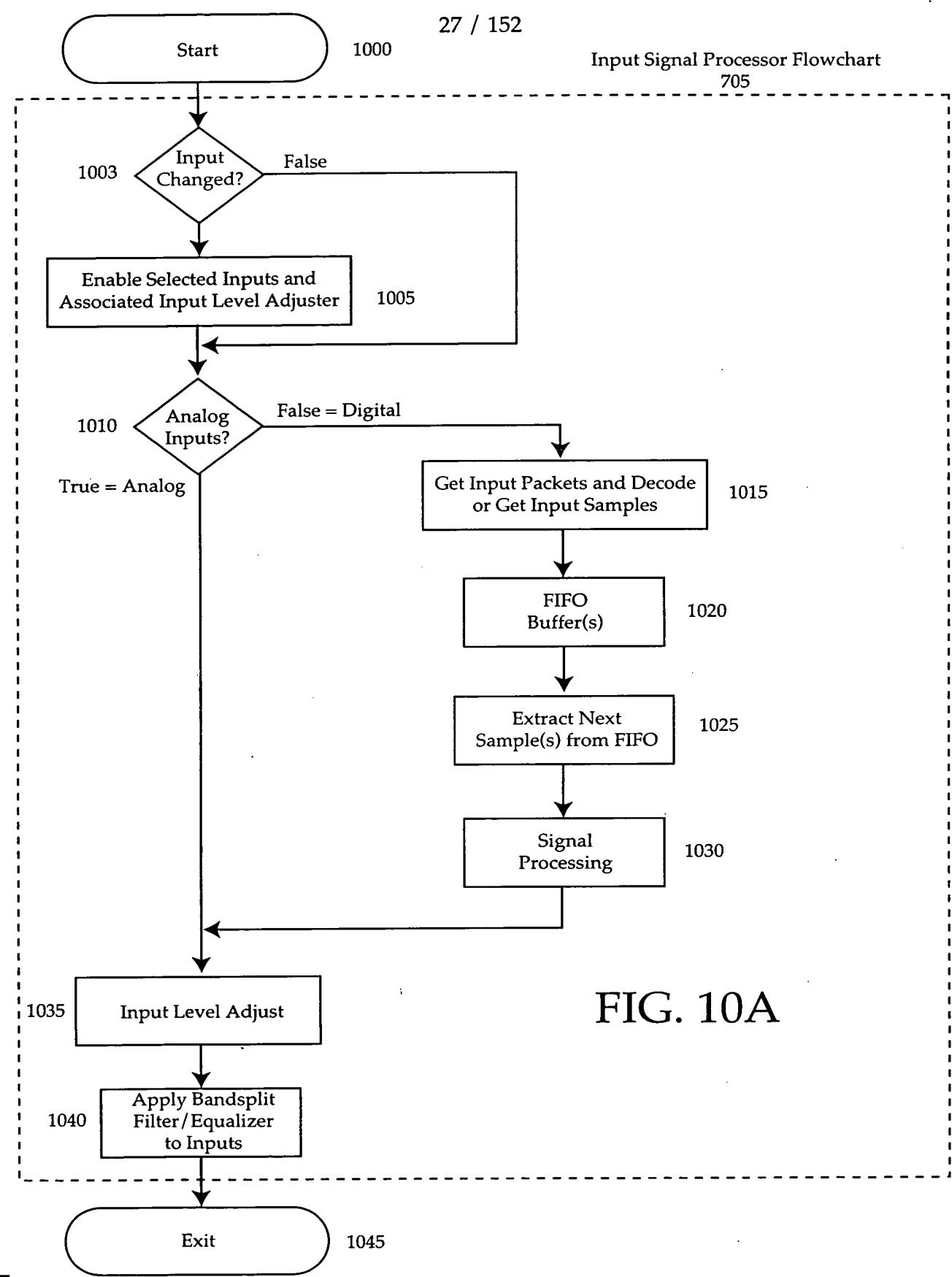


FIG. 10A

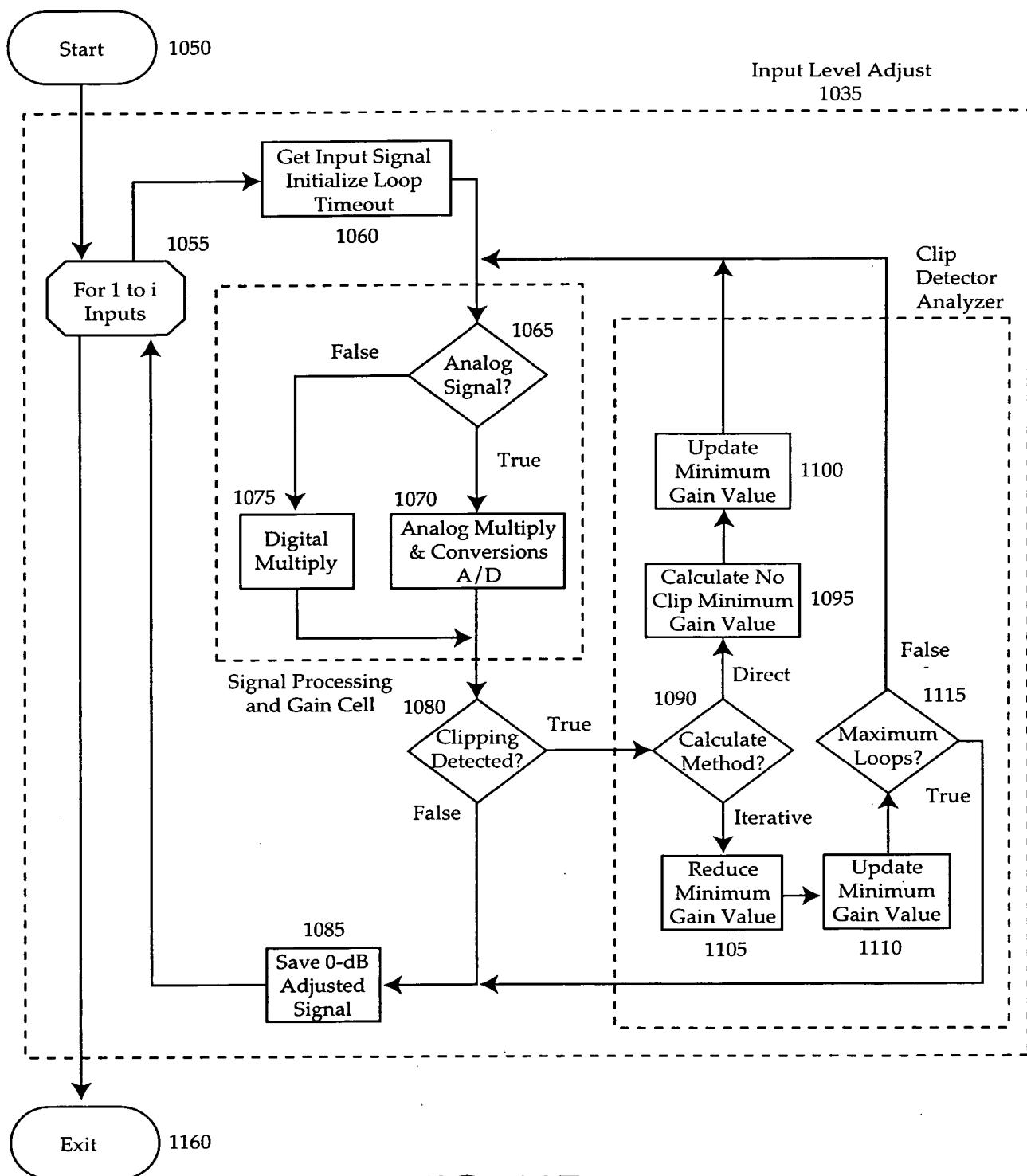


FIG. 10B

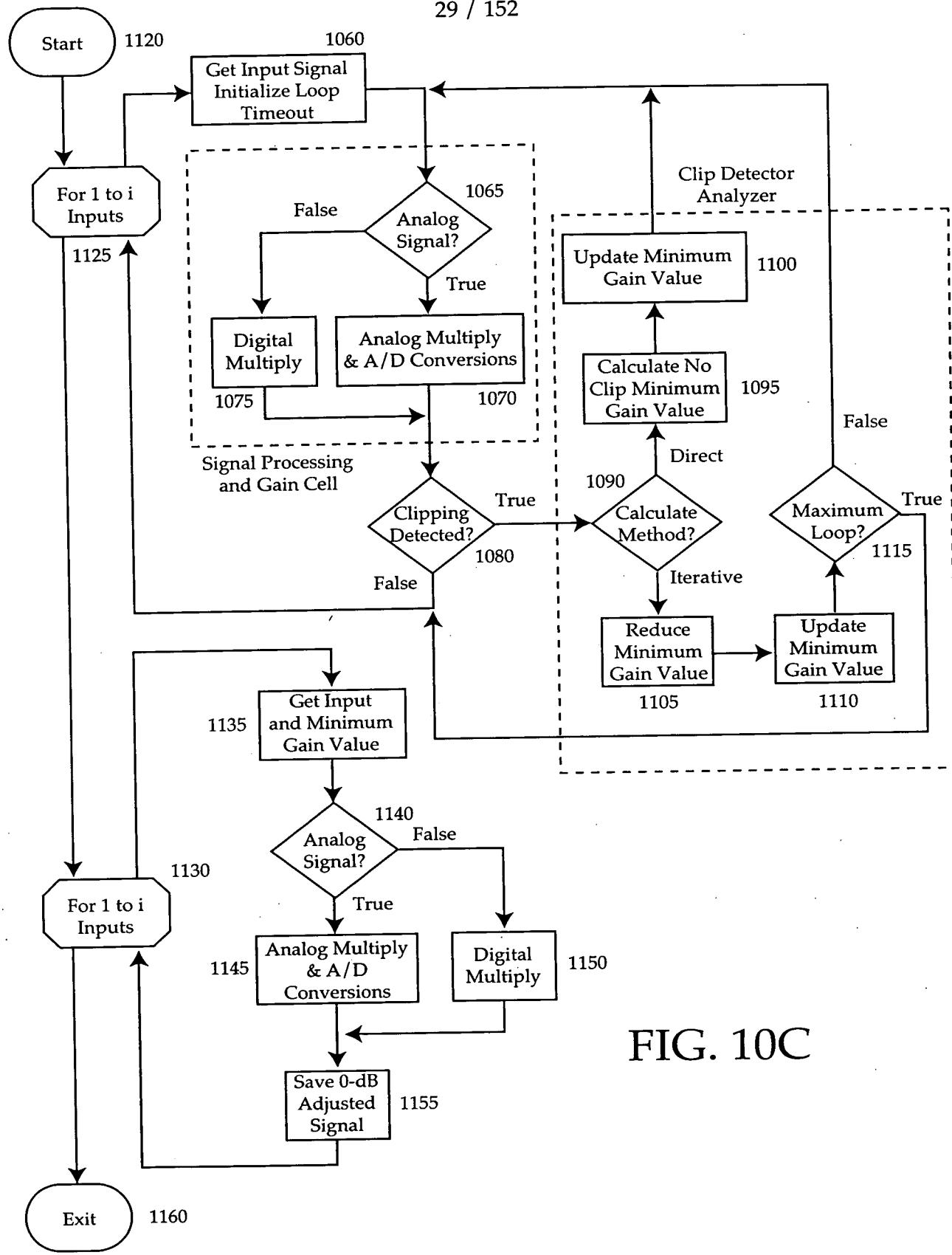


FIG. 10C

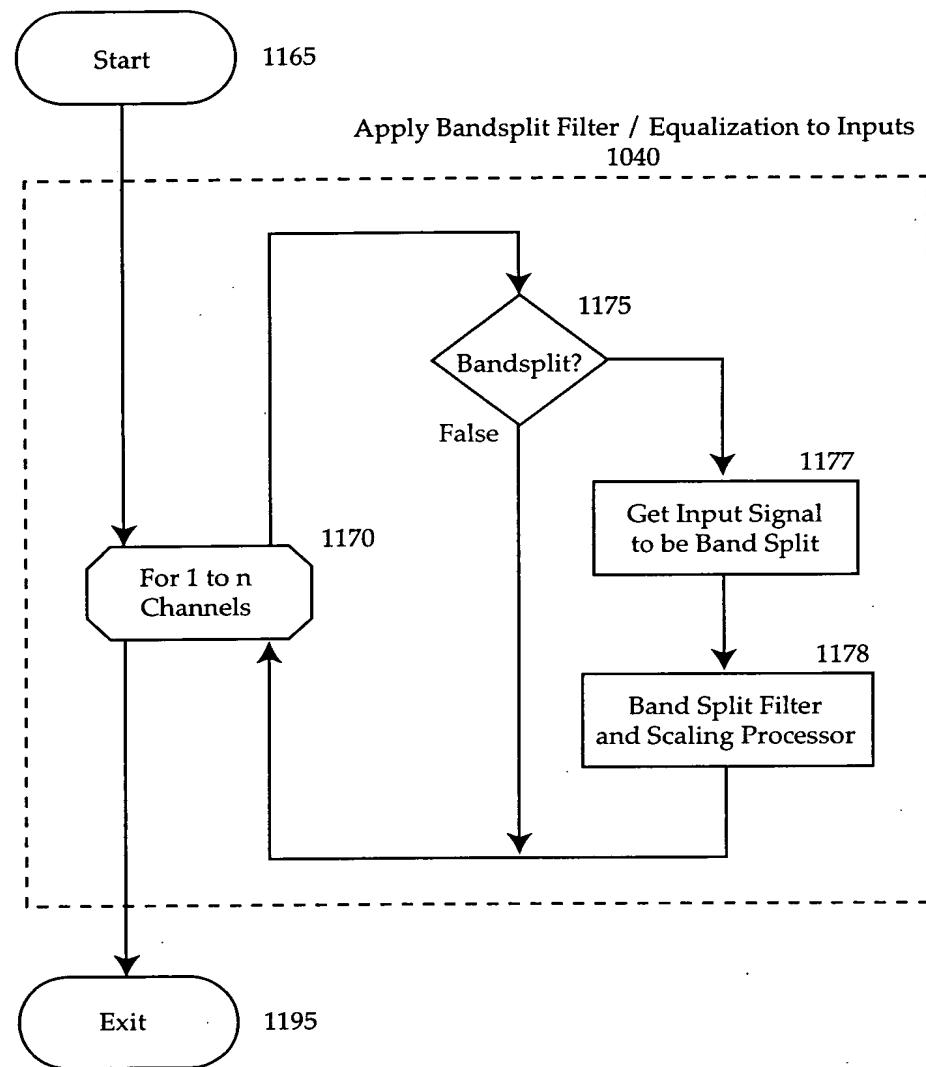


FIG. 10D

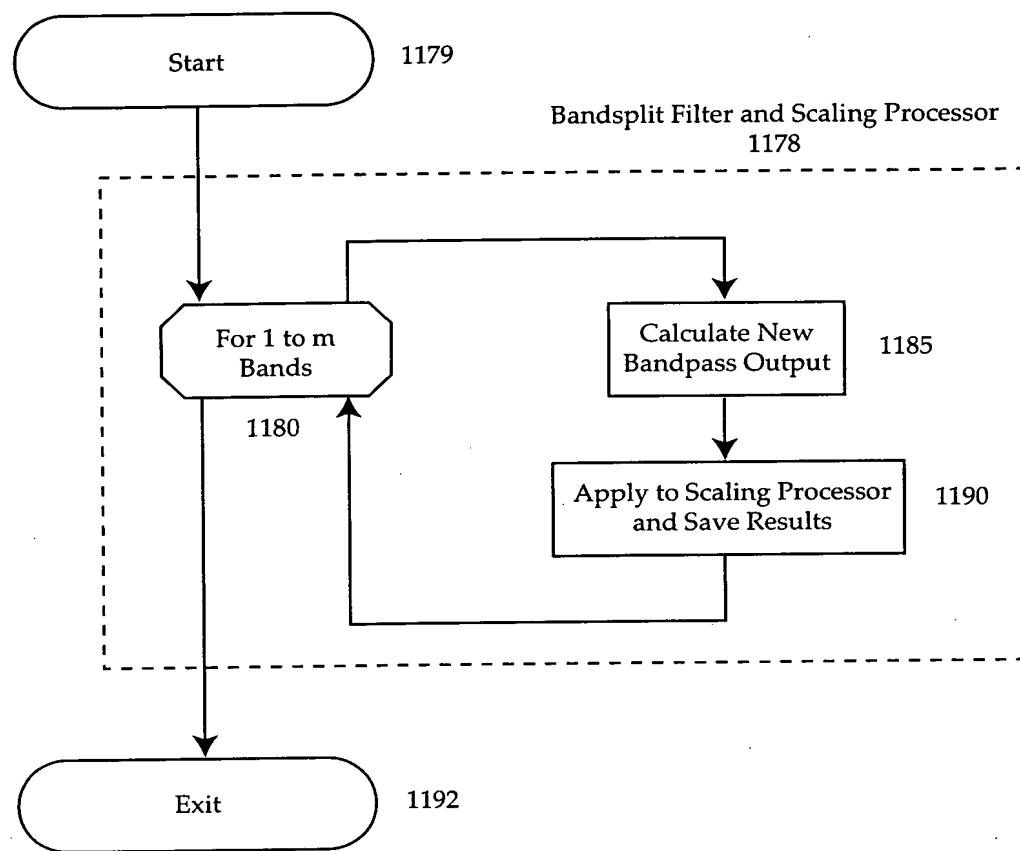


FIG. 10E

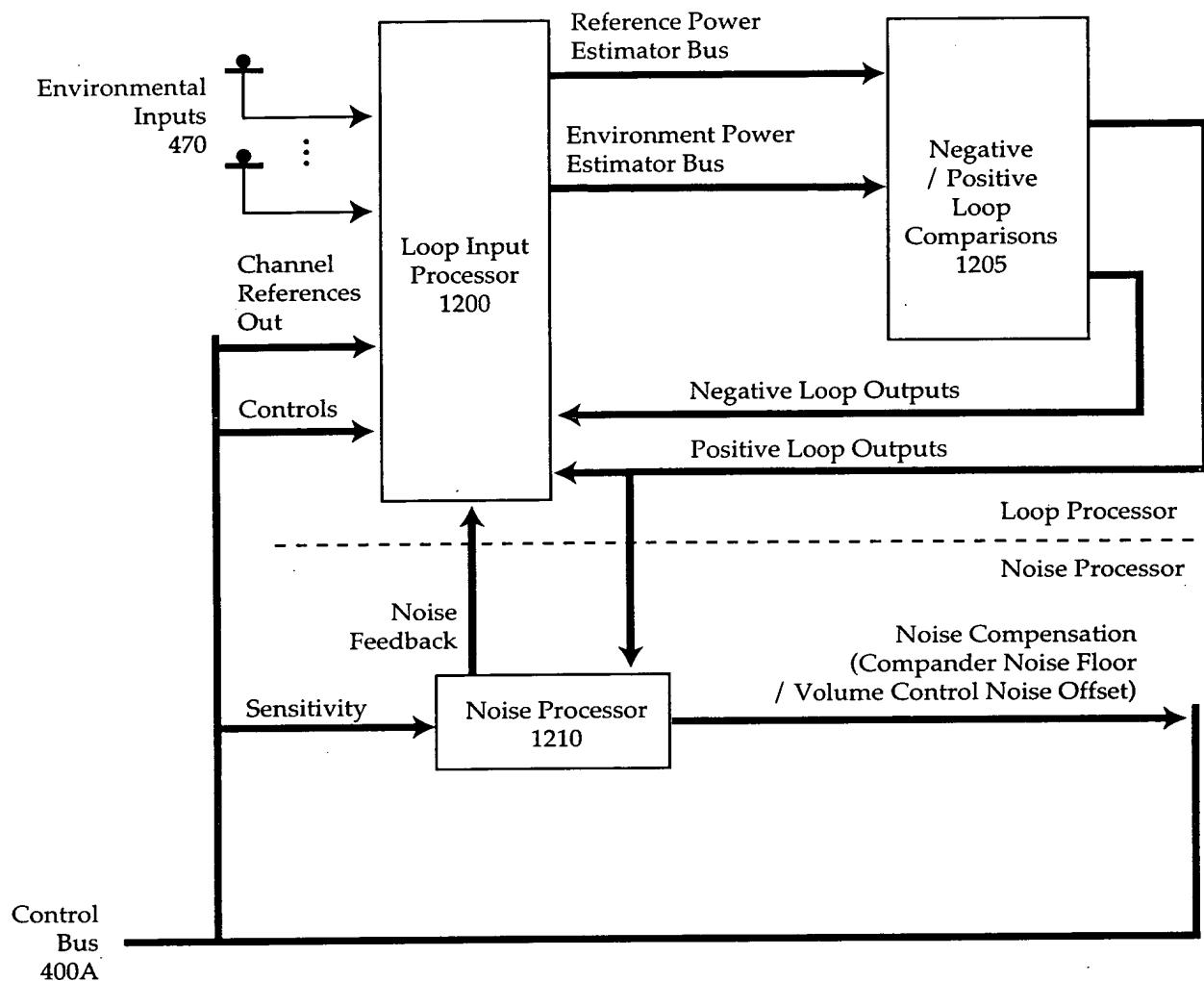


FIG. 11

Environmental Sensor Adjustments
& Noise Extractor Flowchart 710

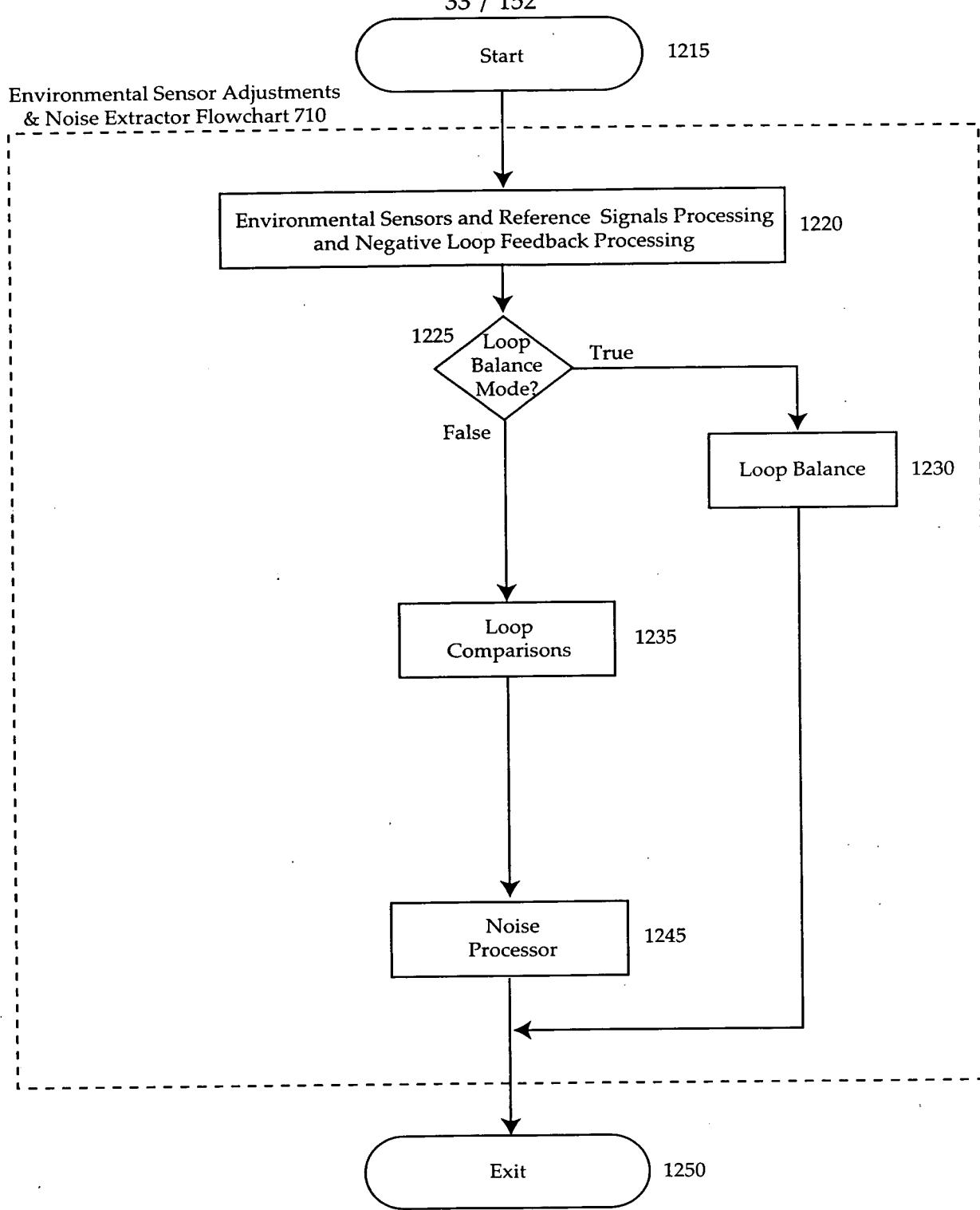


FIG. 12

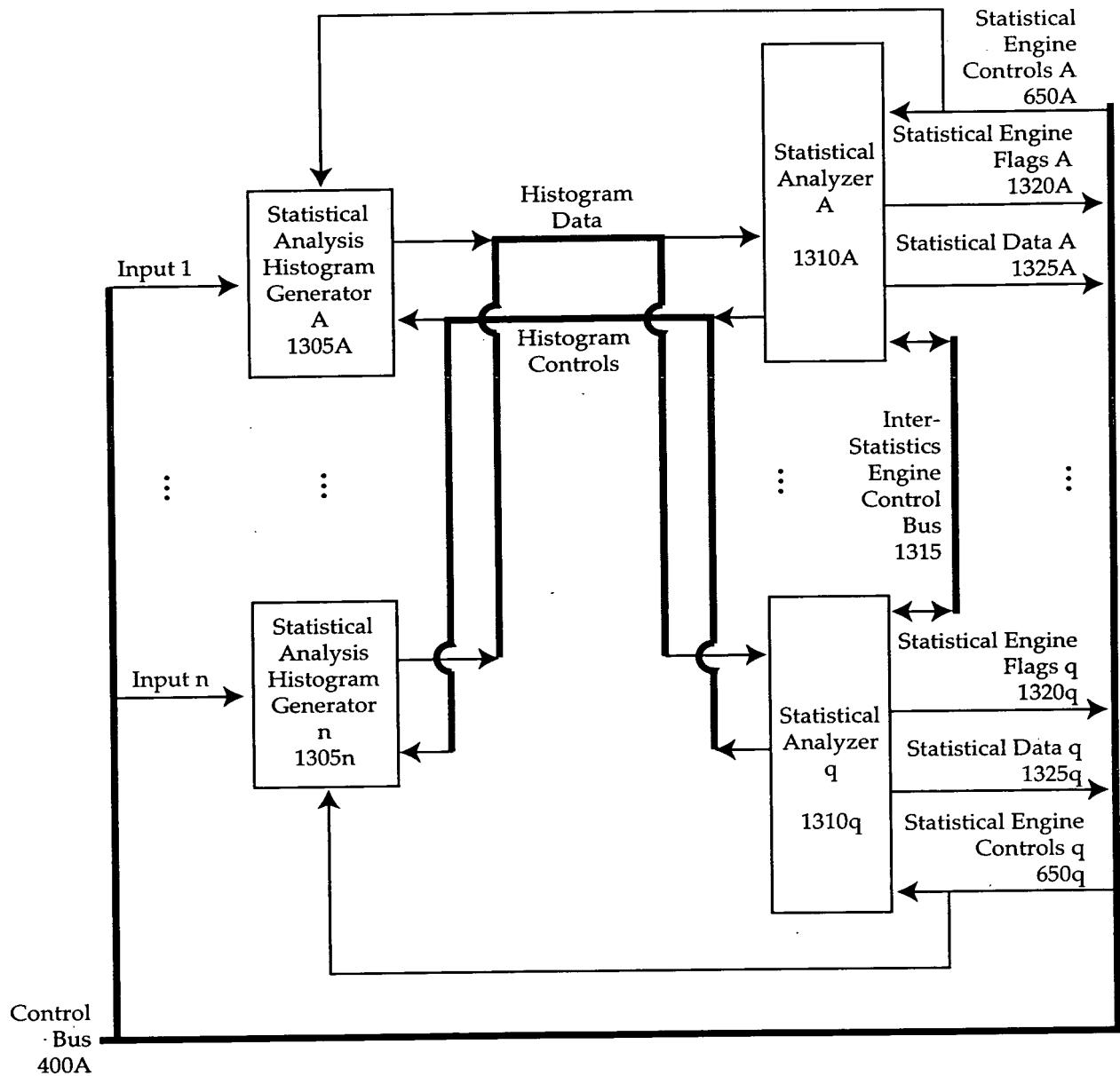


FIG. 13

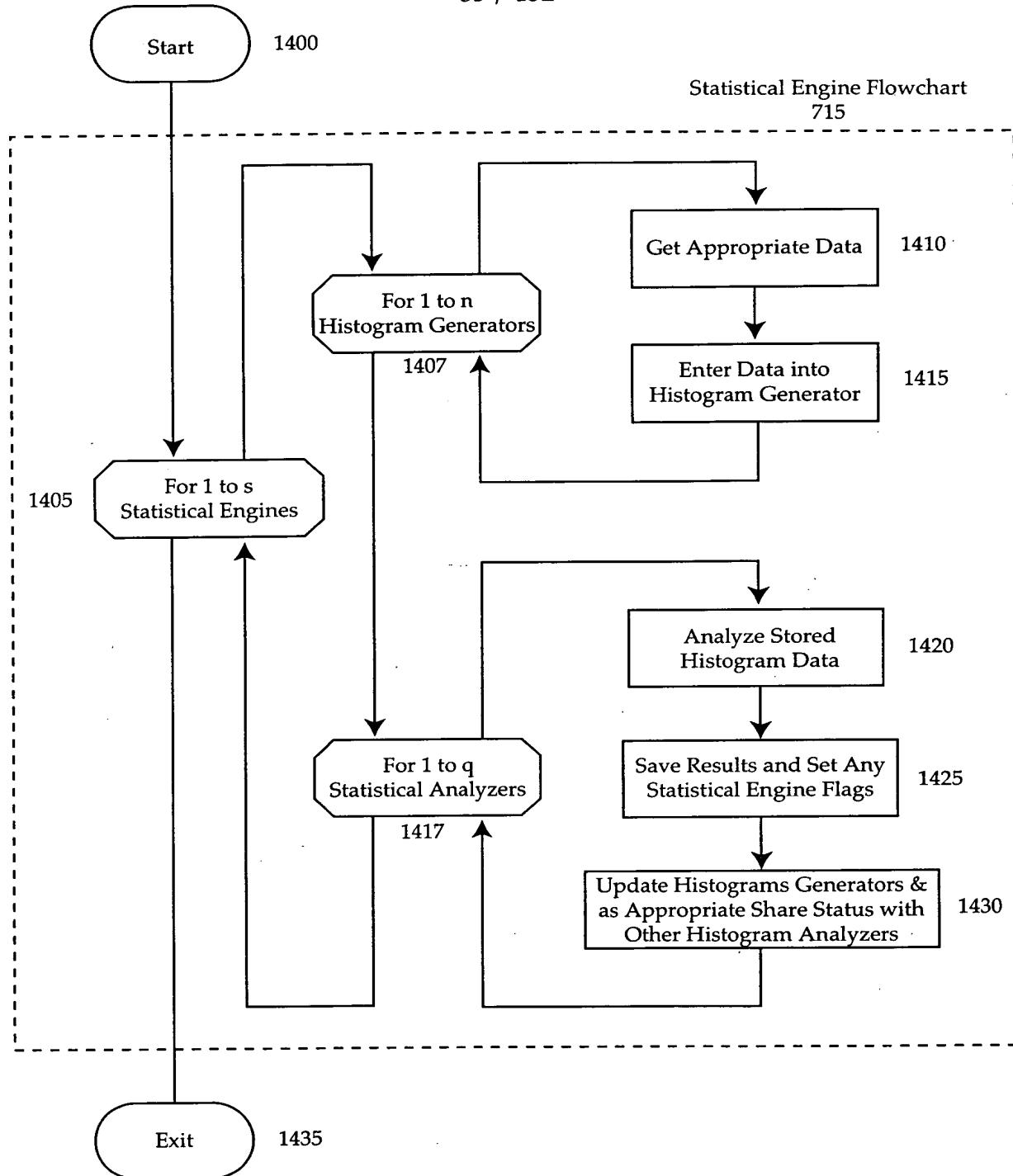


FIG. 14

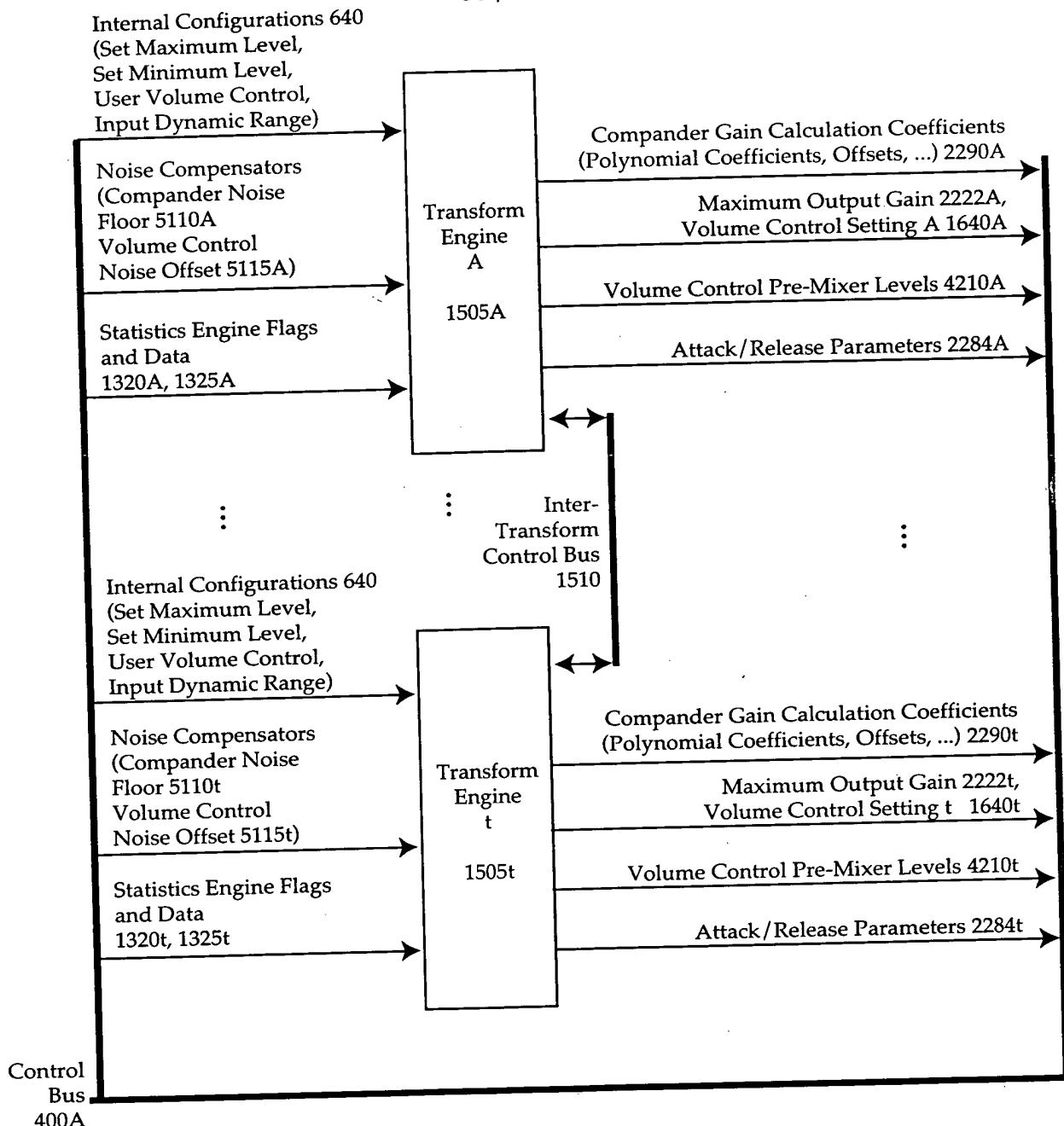


FIG. 15

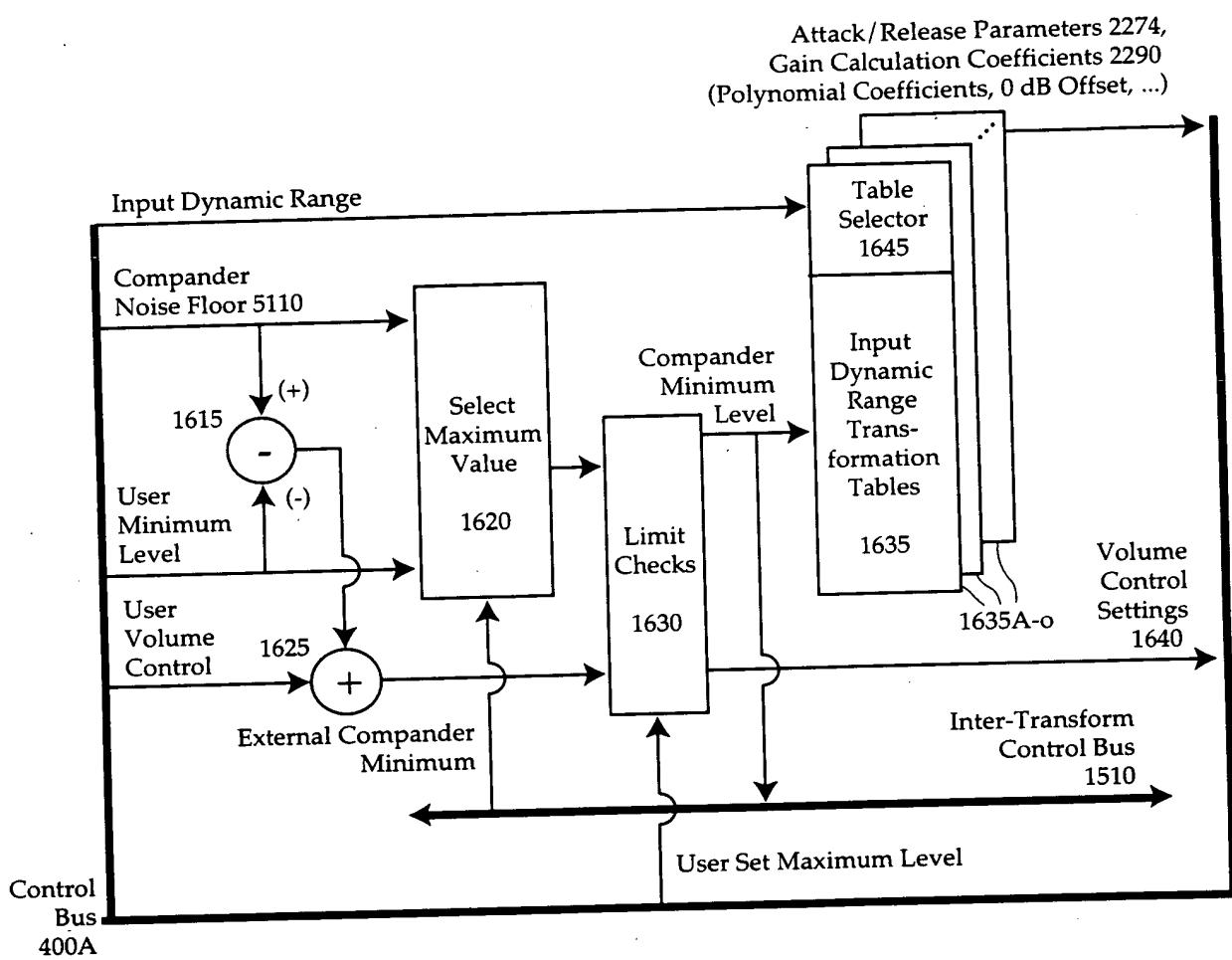


FIG. 16

Transform Engine Flowchart
720

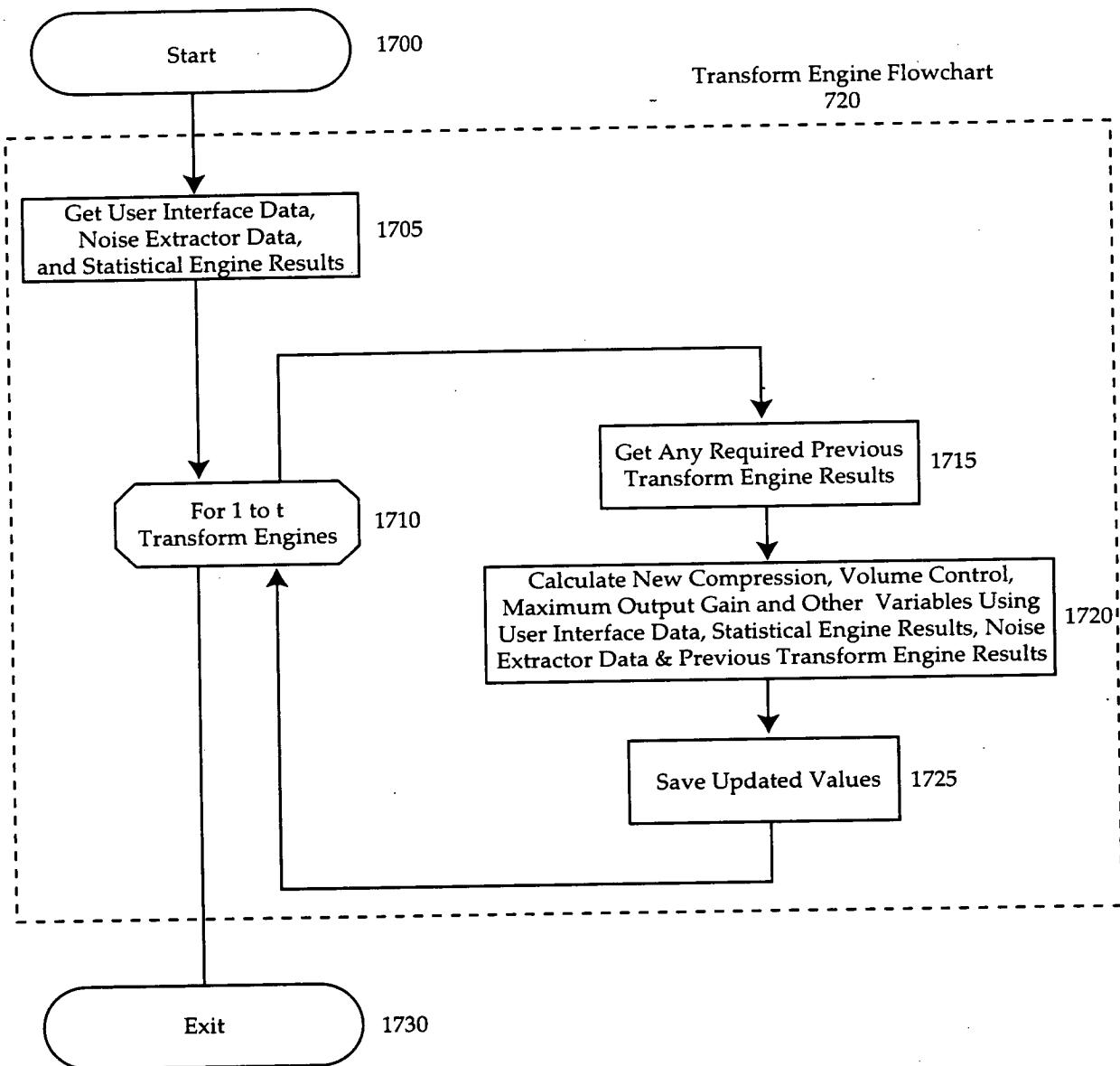


FIG. 17

Central Power Estimator Mixer Combiner
455

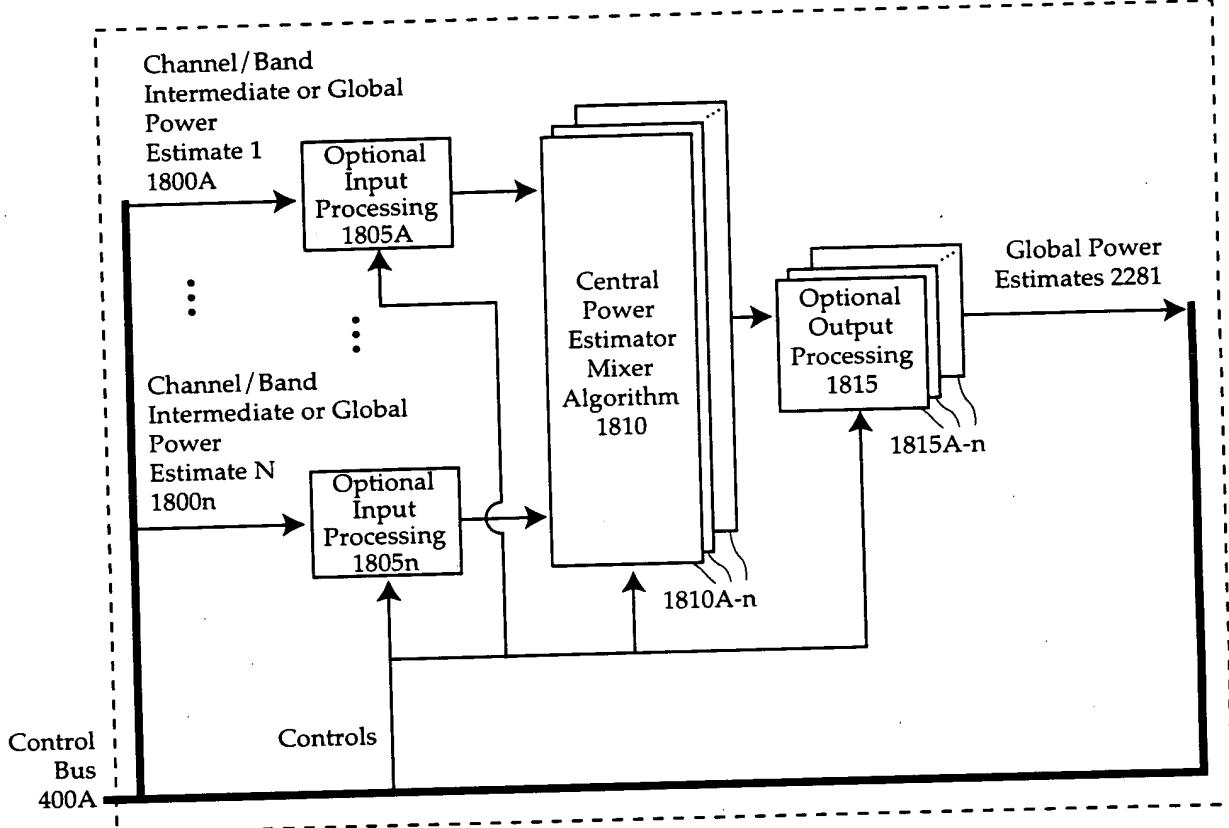


FIG. 18

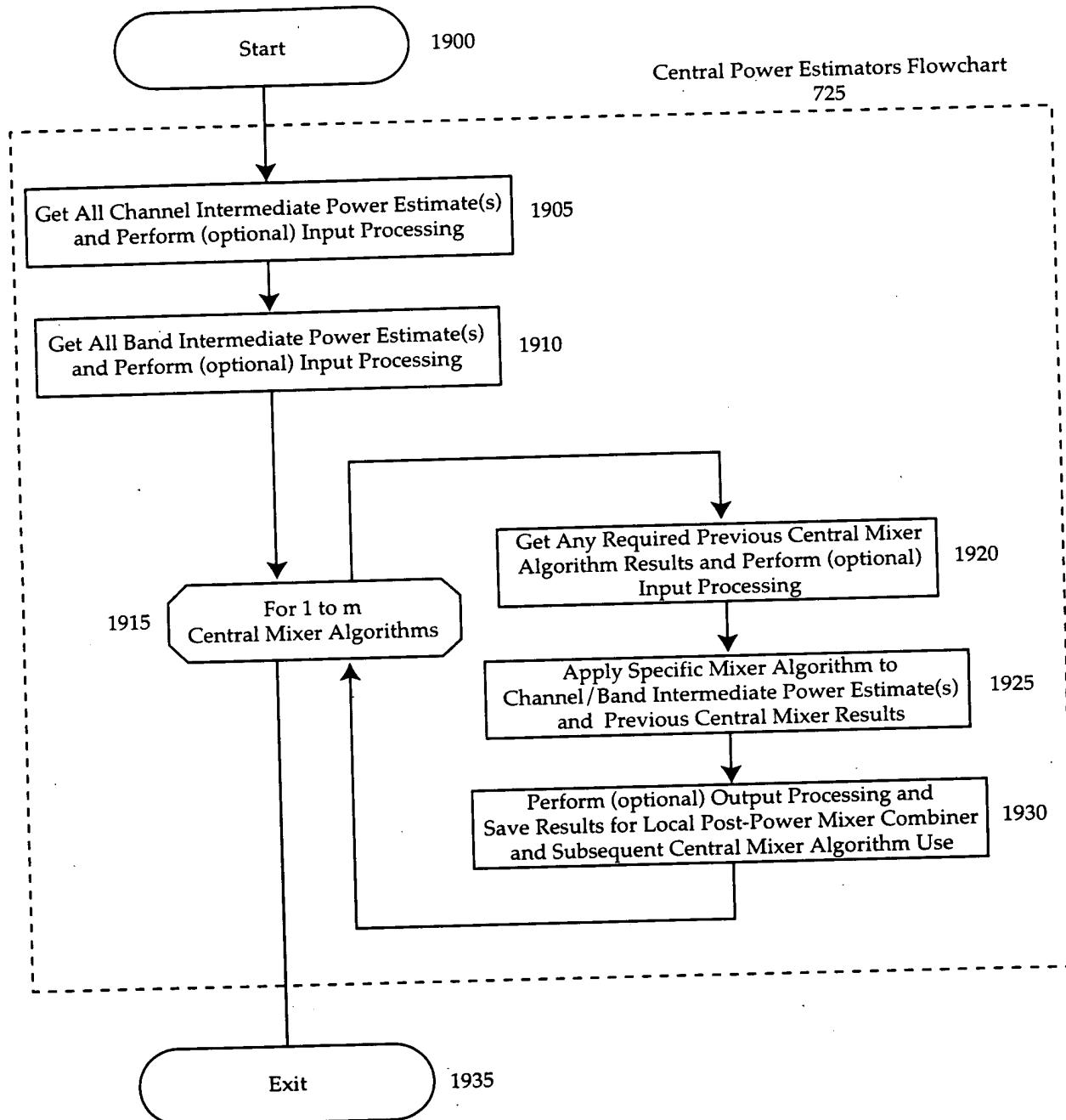


FIG. 19

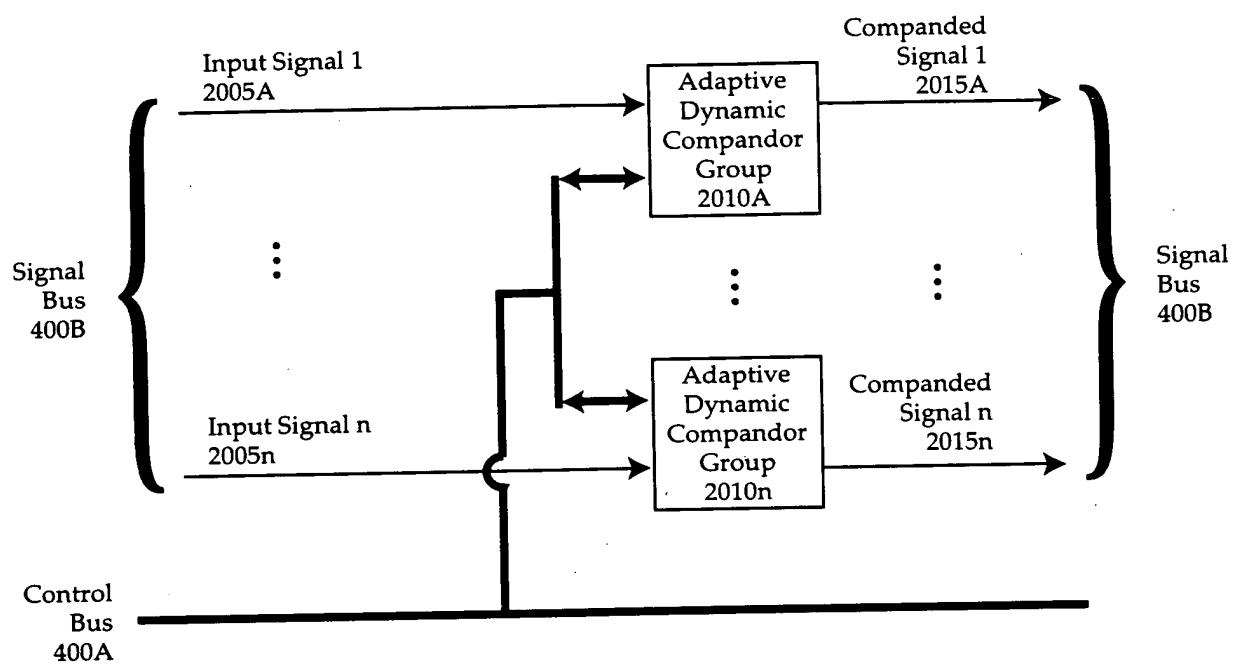


FIG. 20A

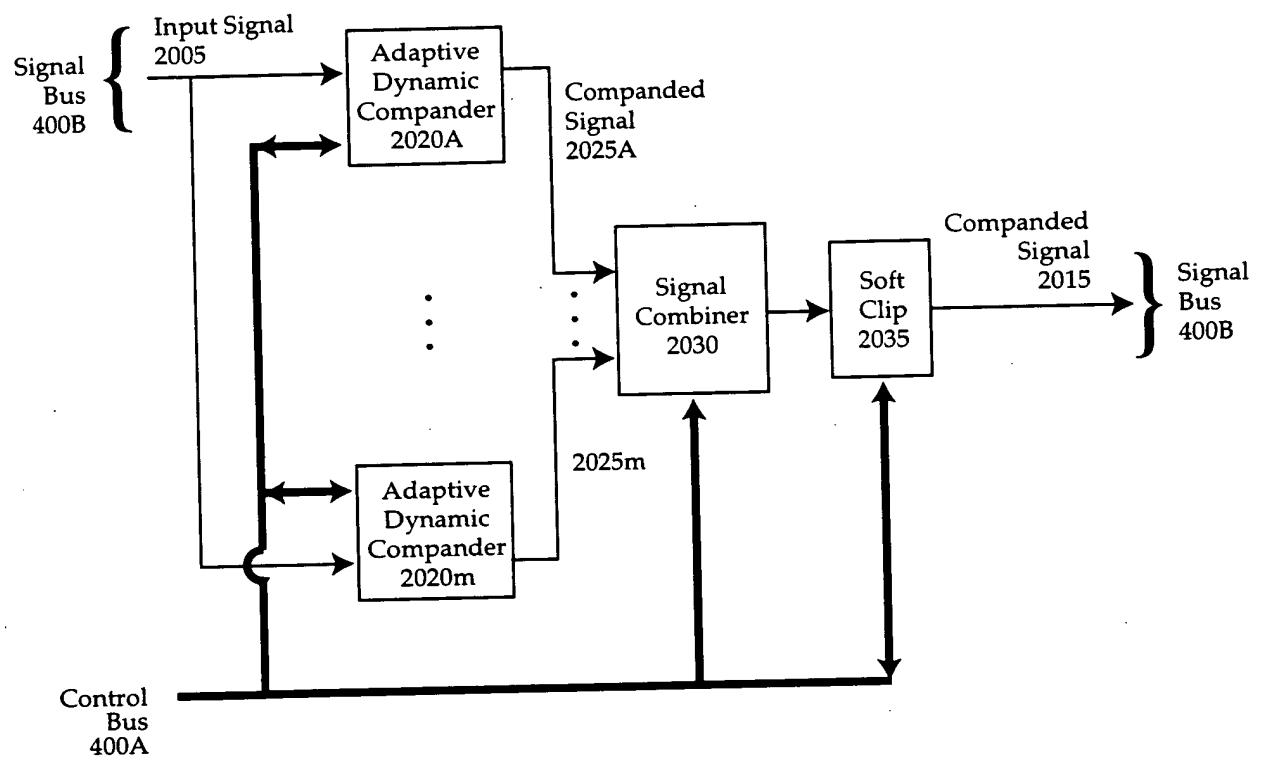


FIG. 20B

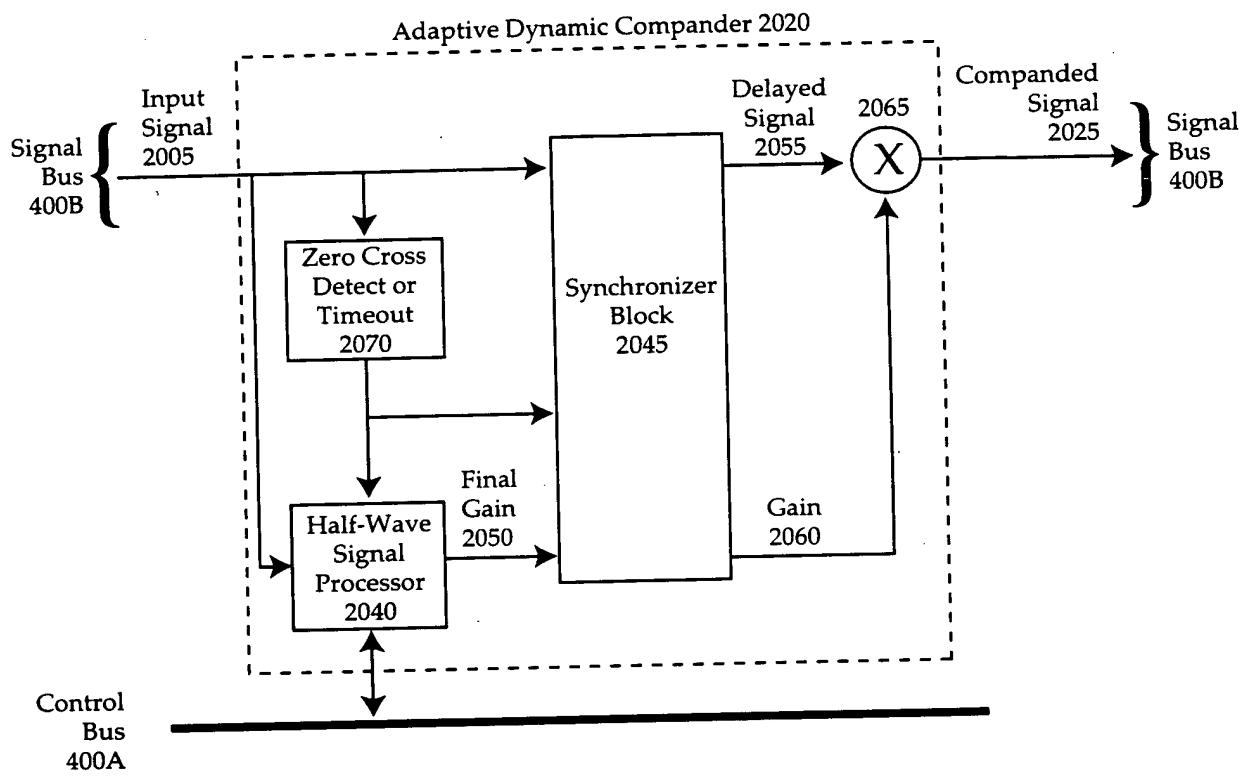


FIG. 20C

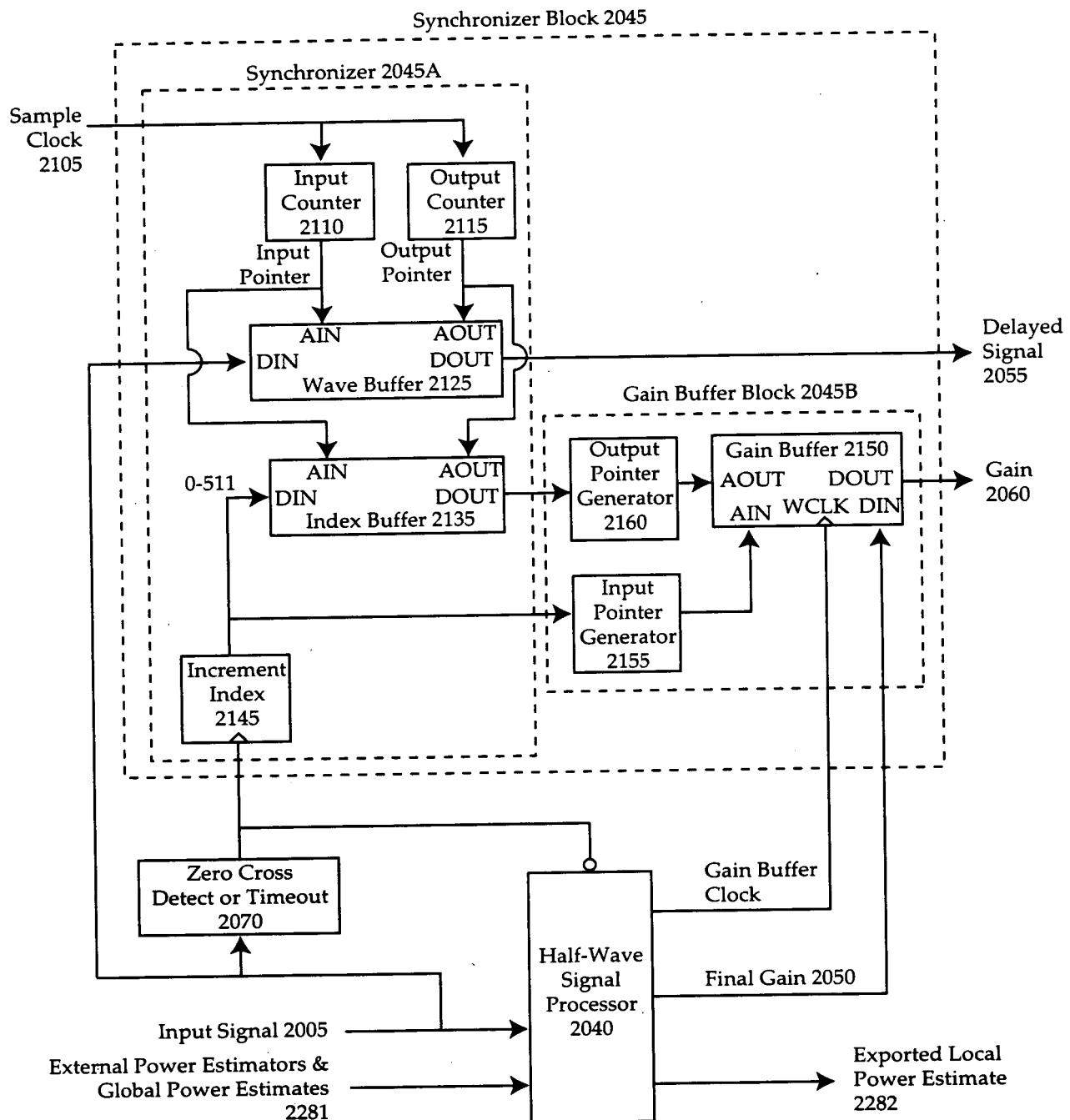


FIG. 21A

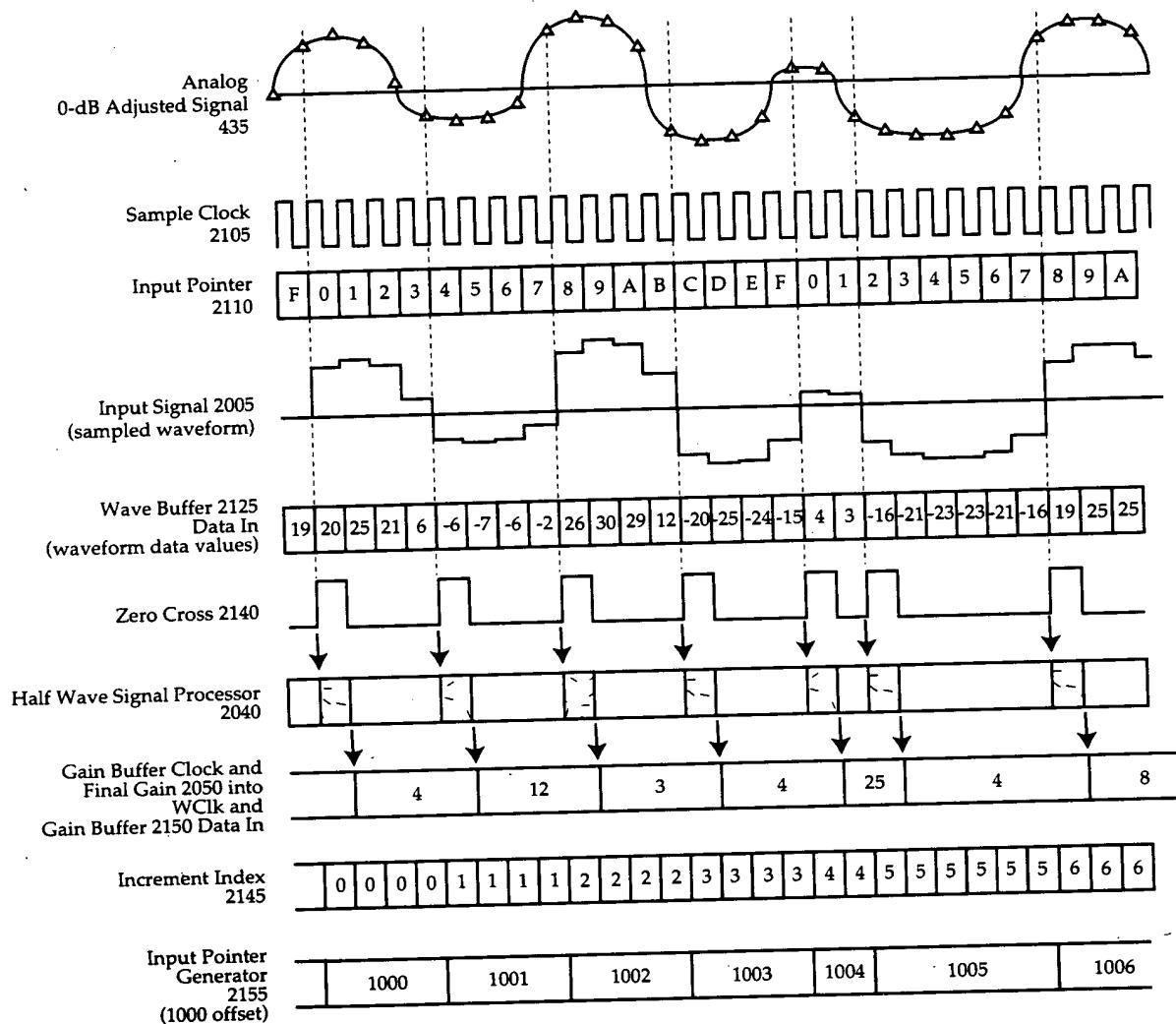


FIG. 21B

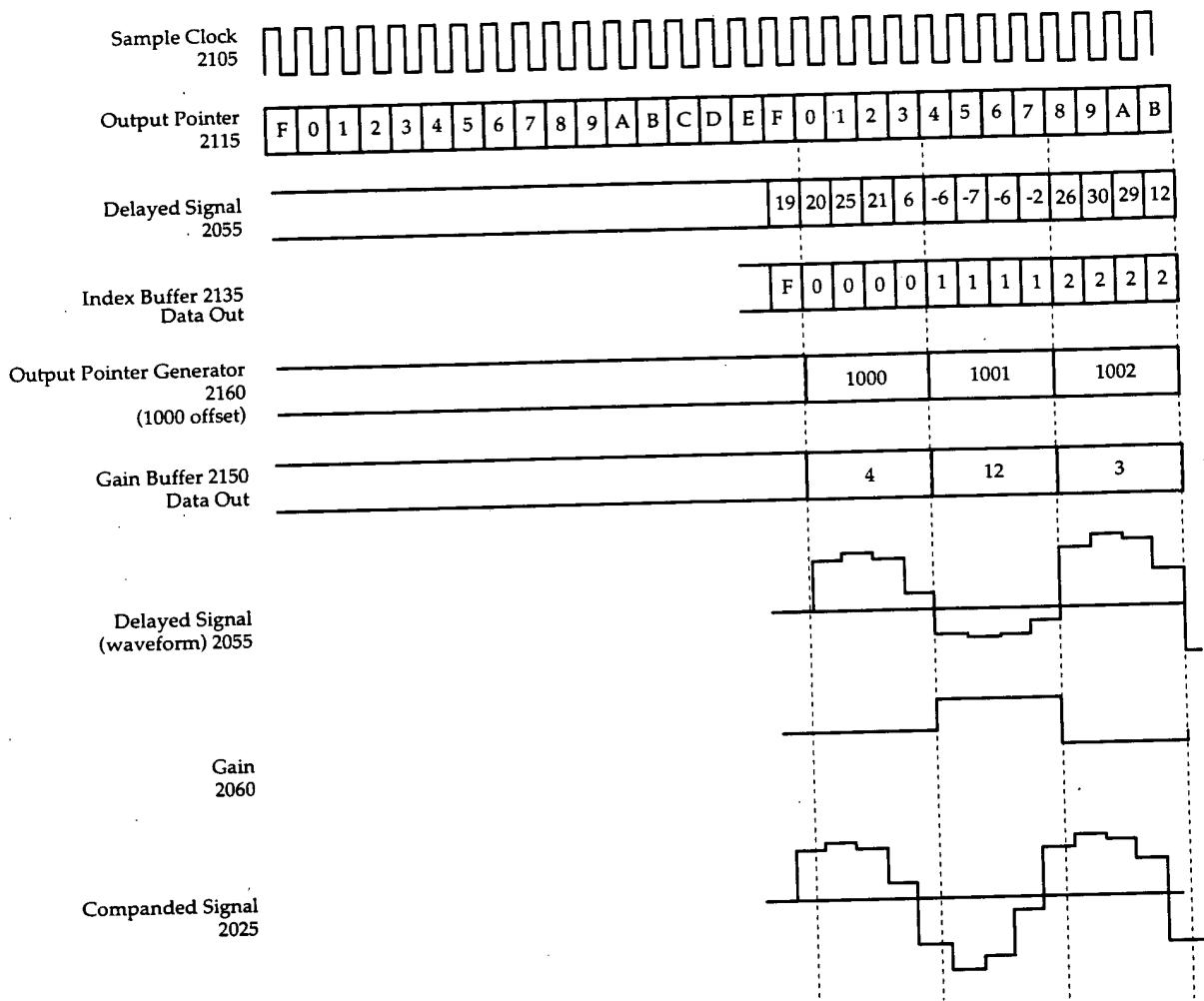


FIG. 21C

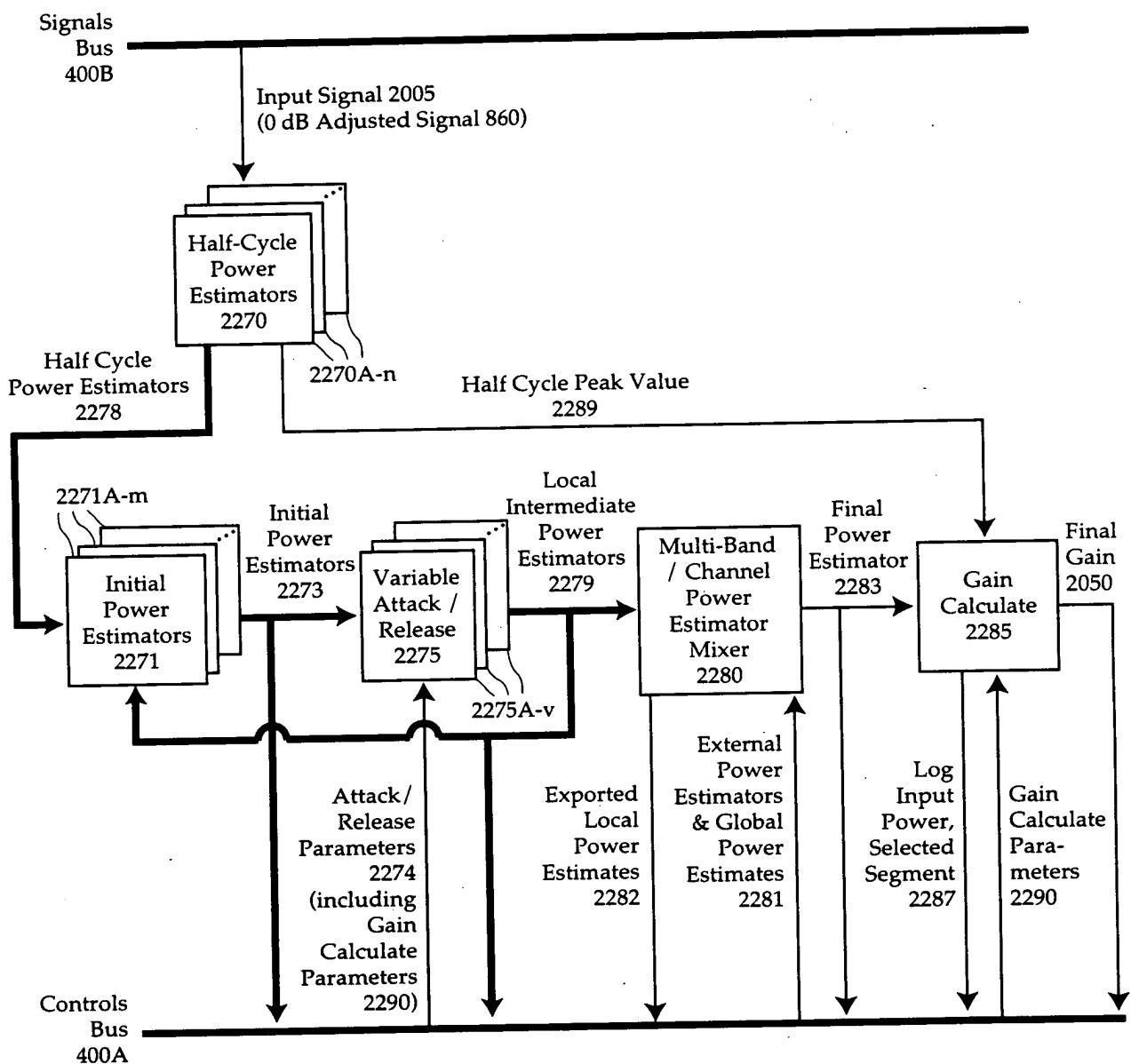


FIG. 22

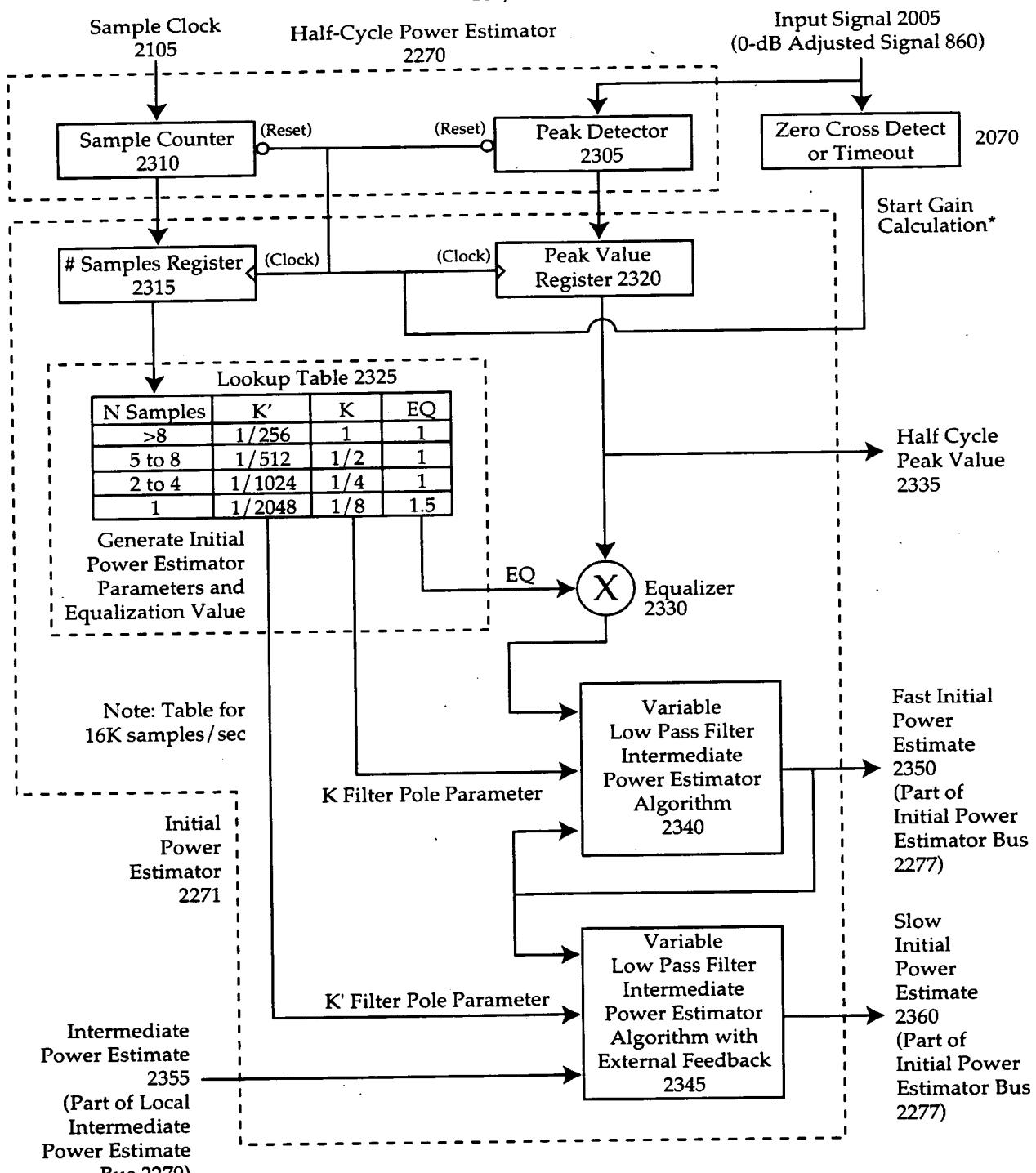


FIG. 23A

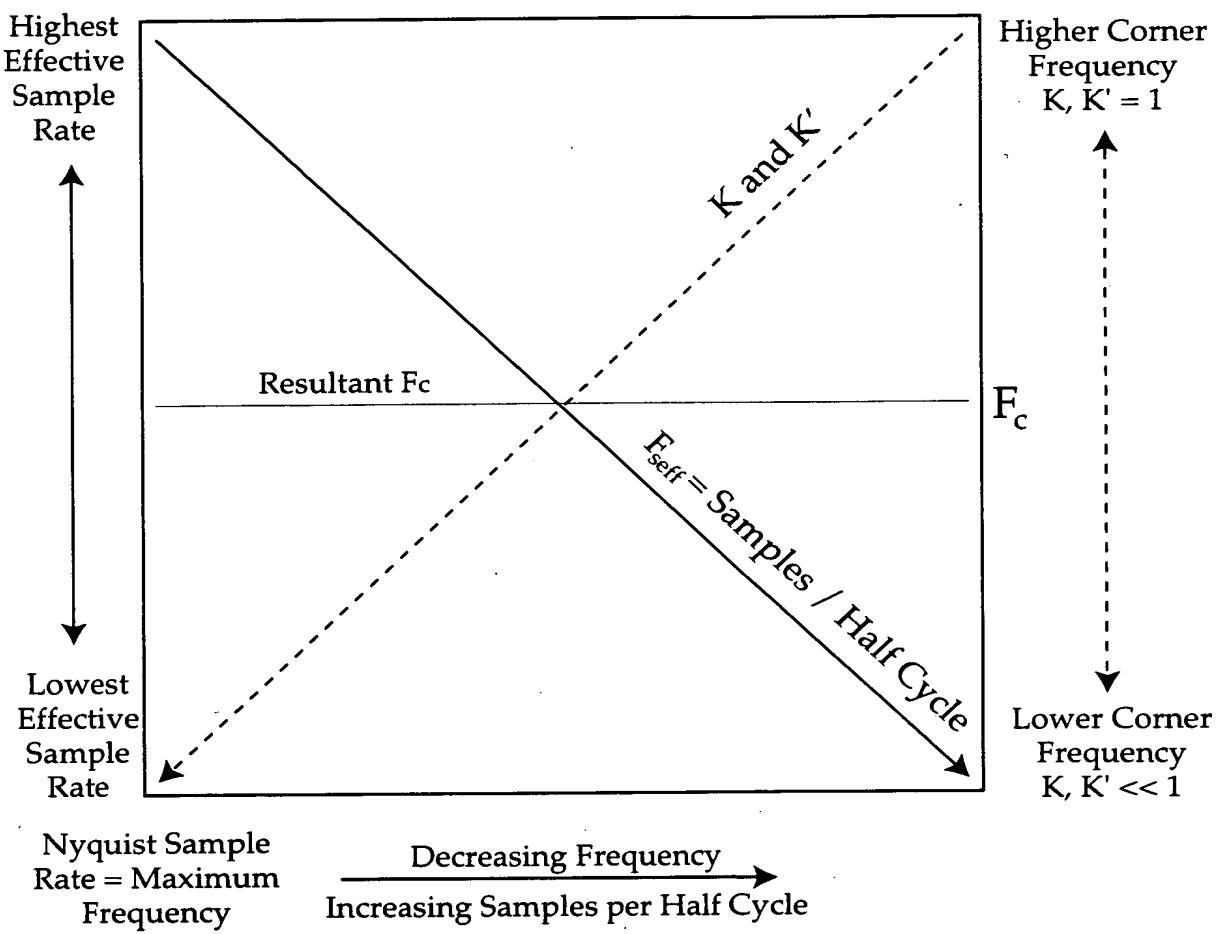


FIG. 23B

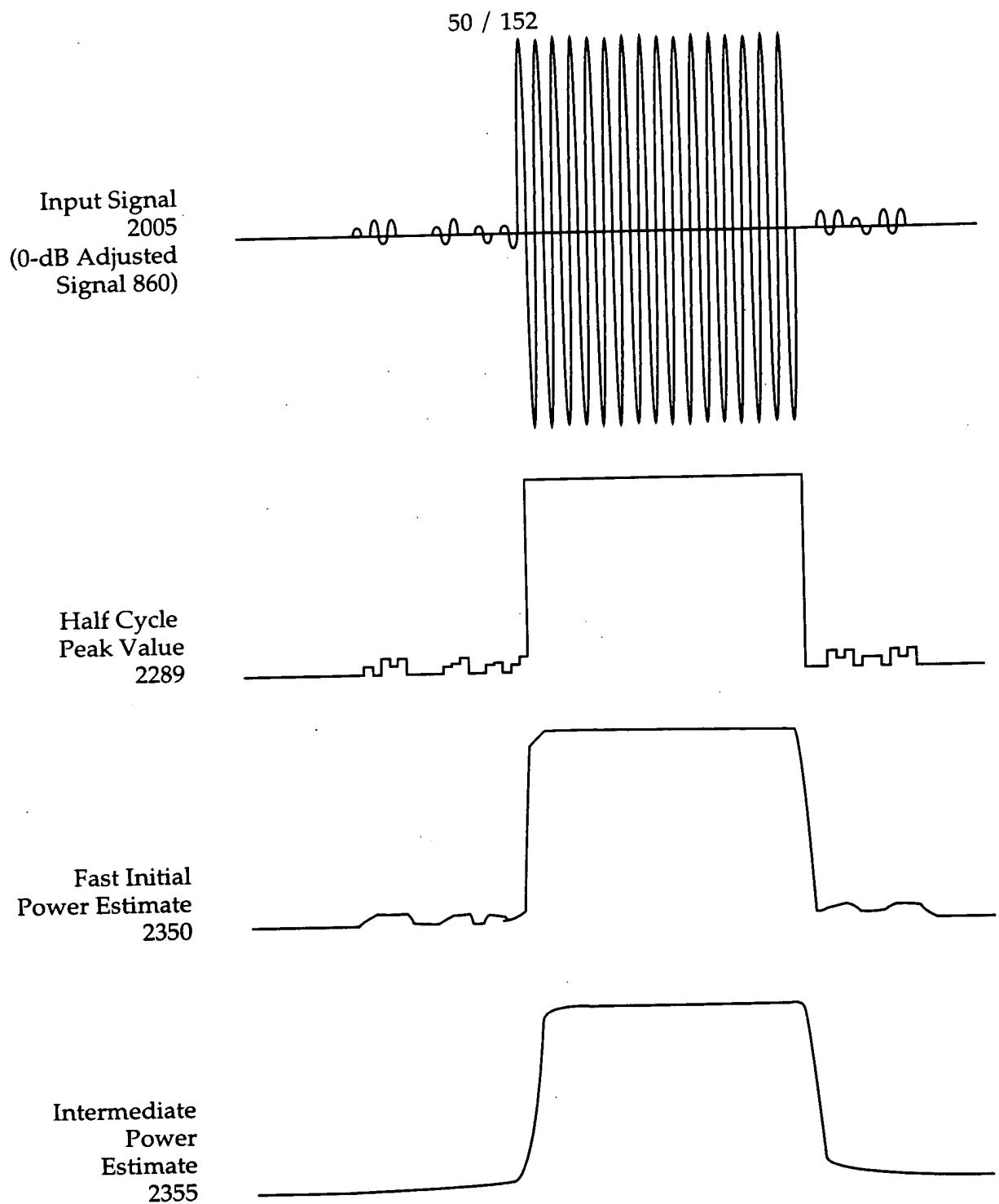


FIG. 23C

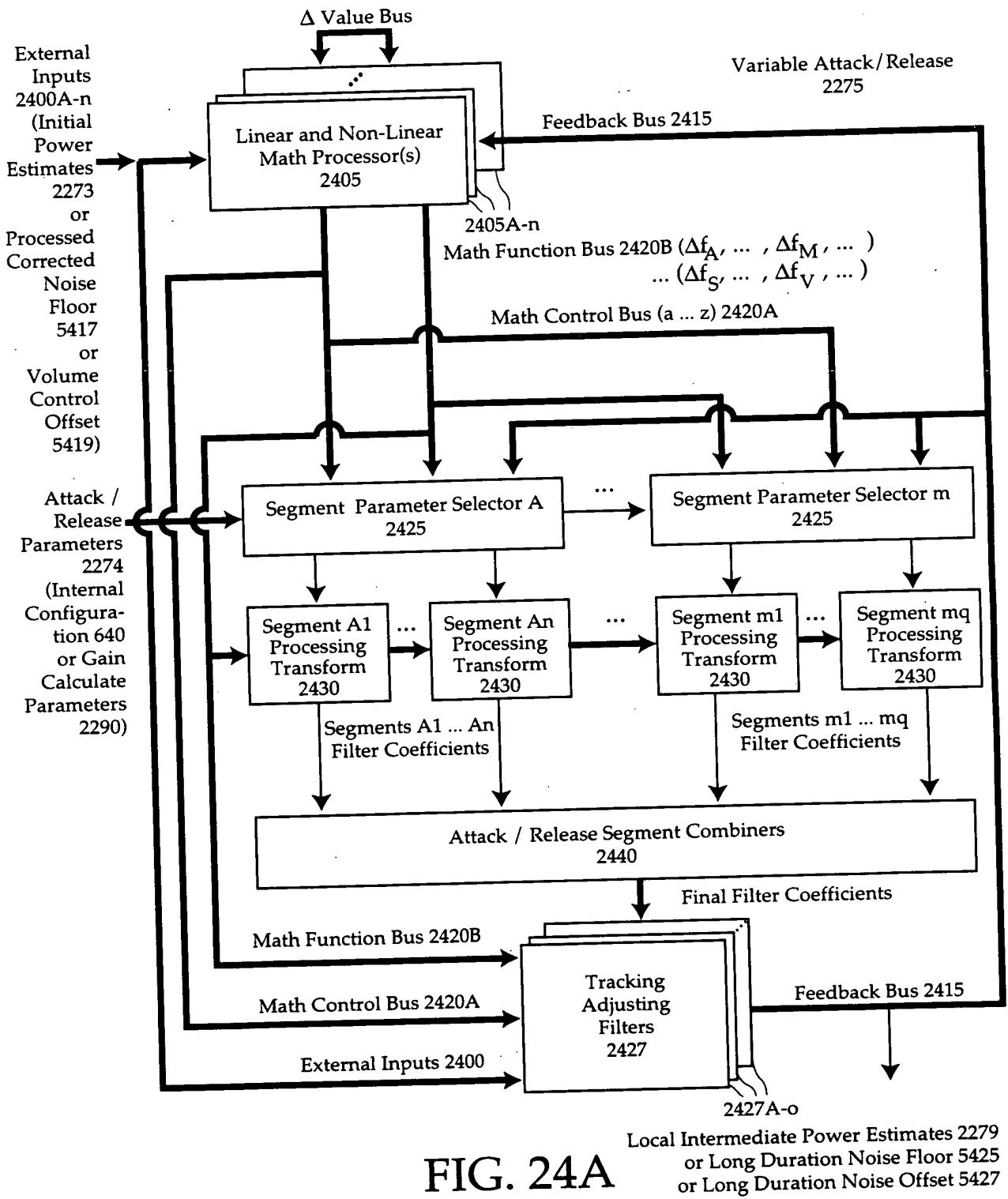


FIG. 24A

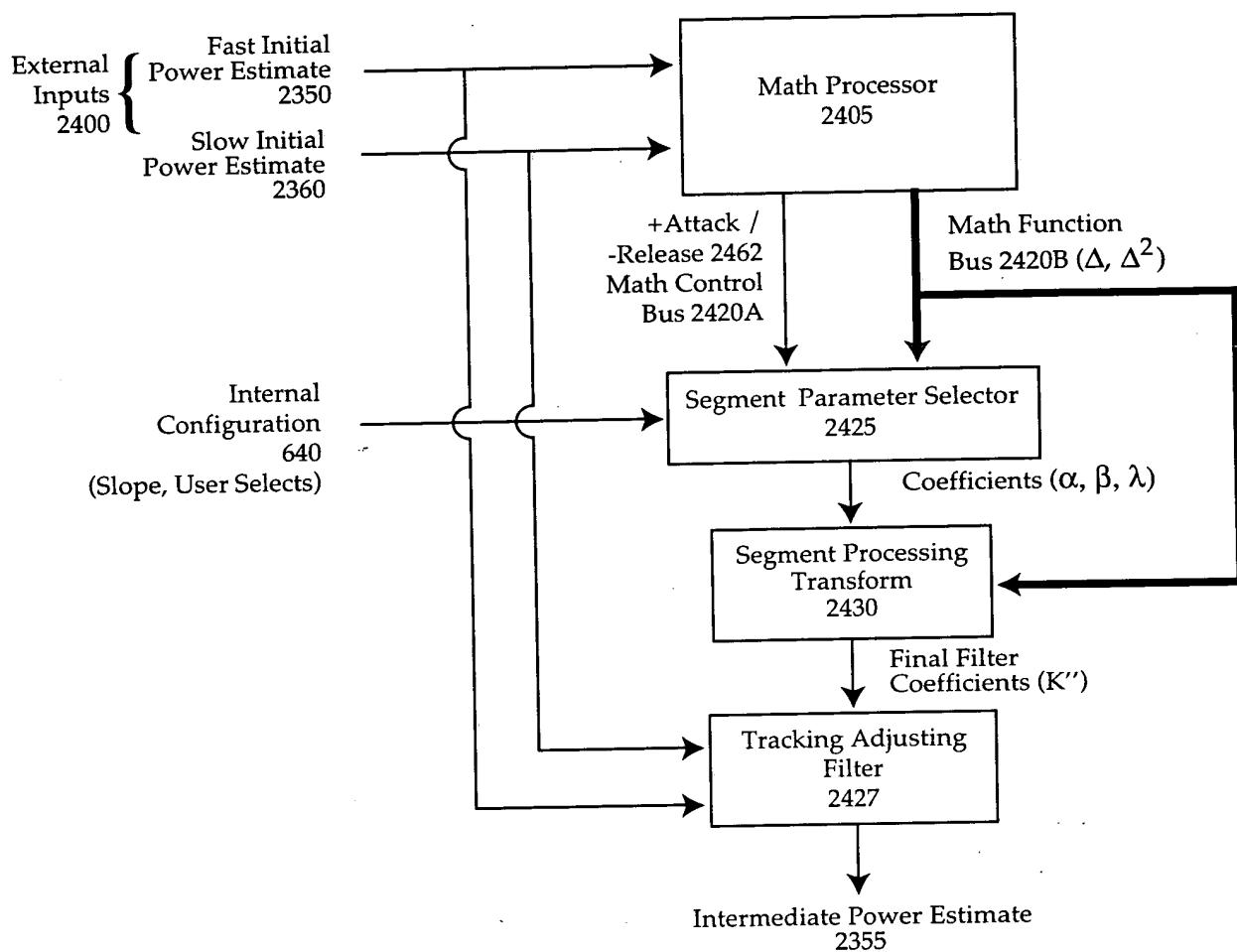


FIG. 24B

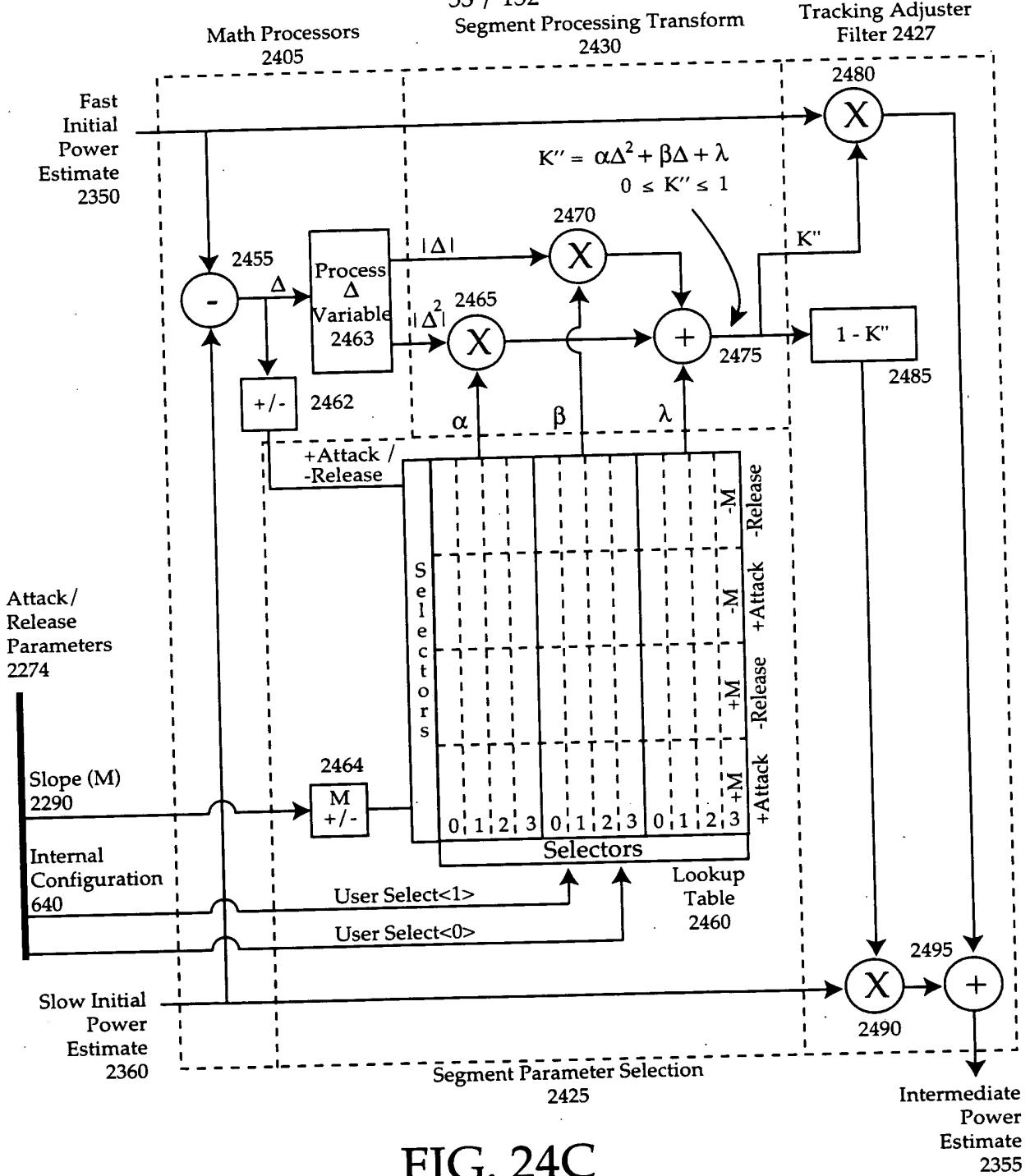


FIG. 24C

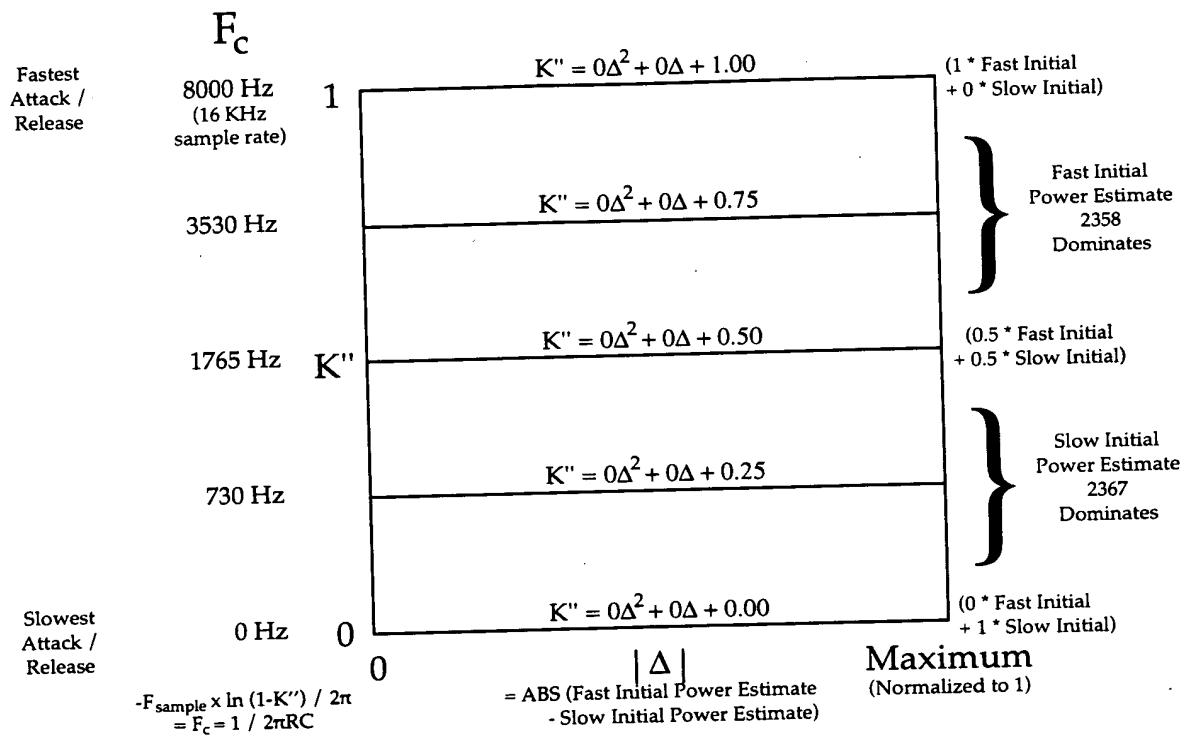


FIG. 24D

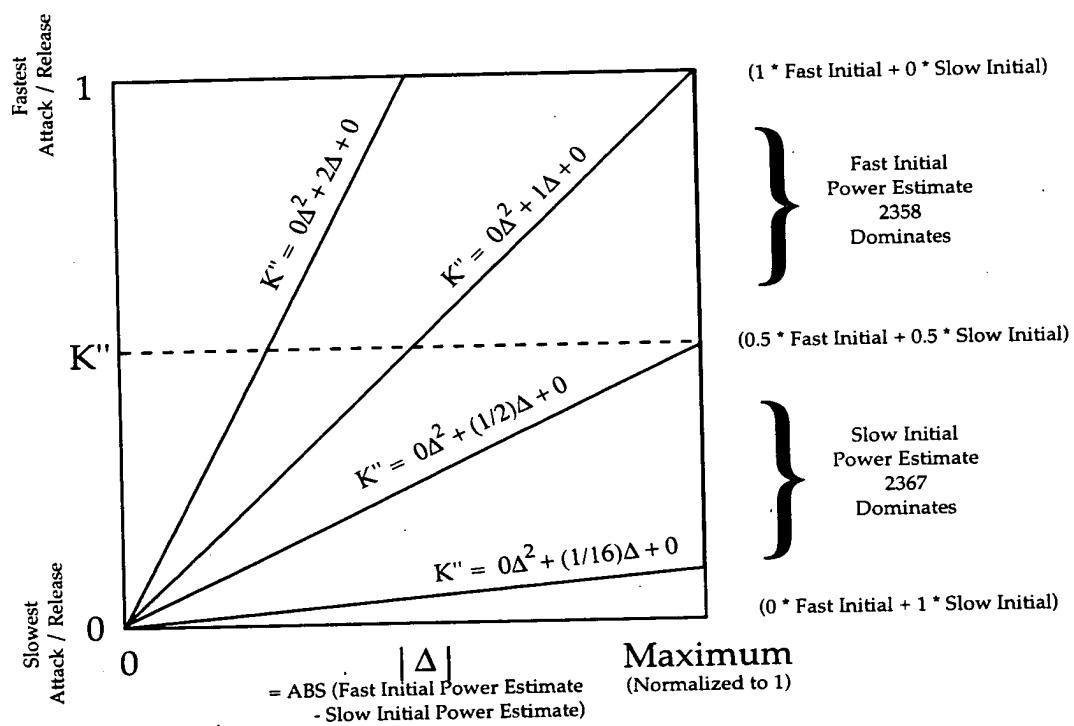


FIG. 24E

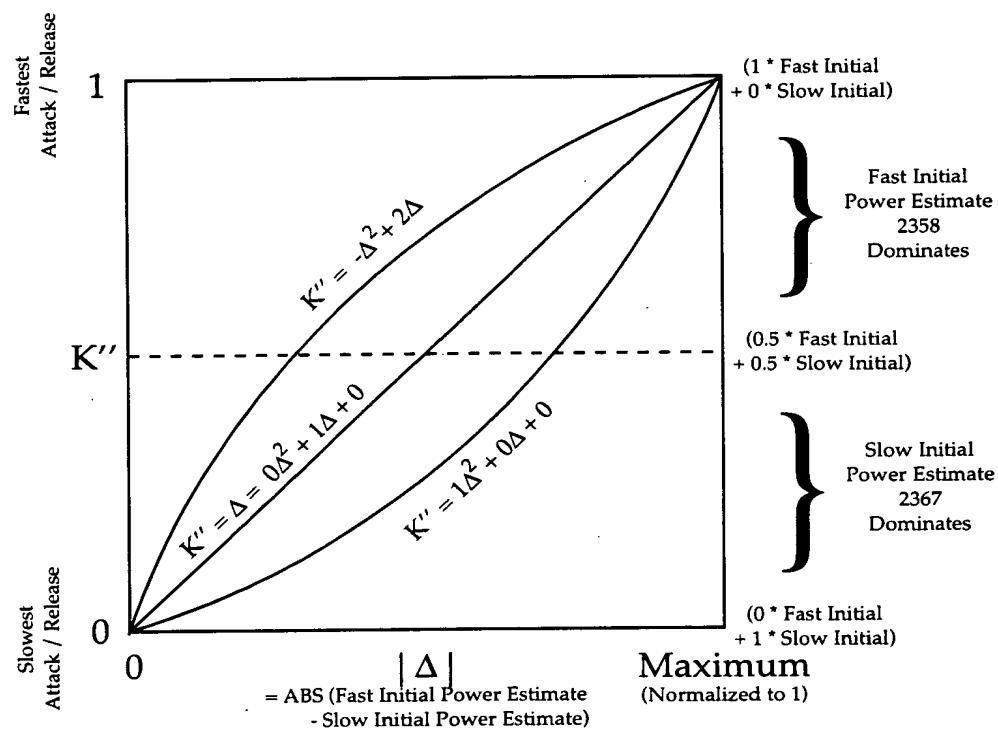


FIG. 24F

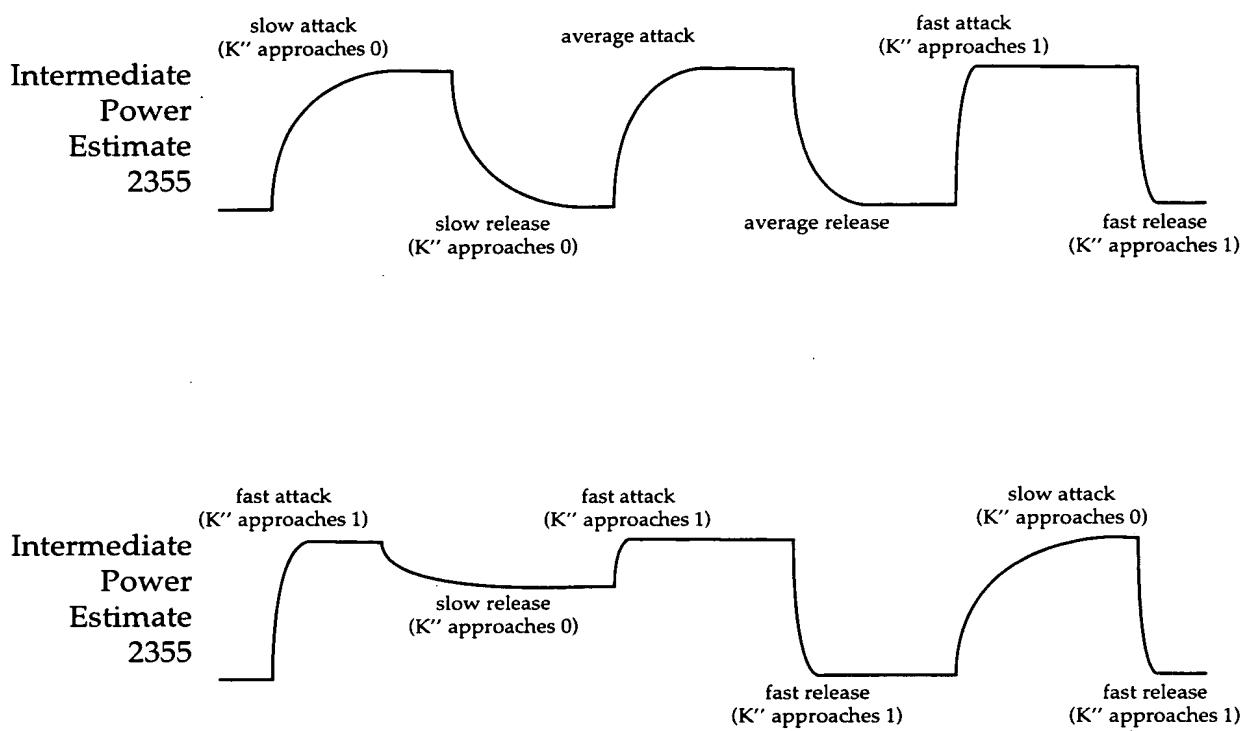


FIG. 24G

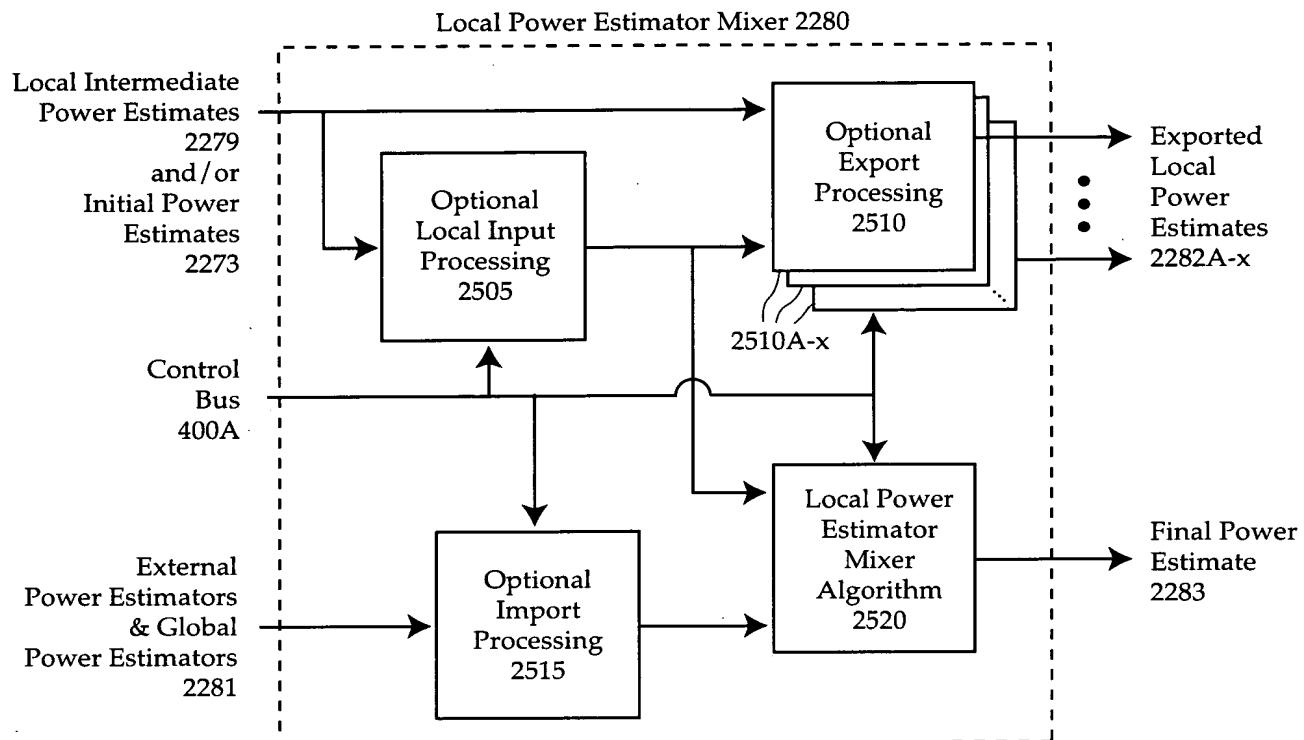


FIG. 25A

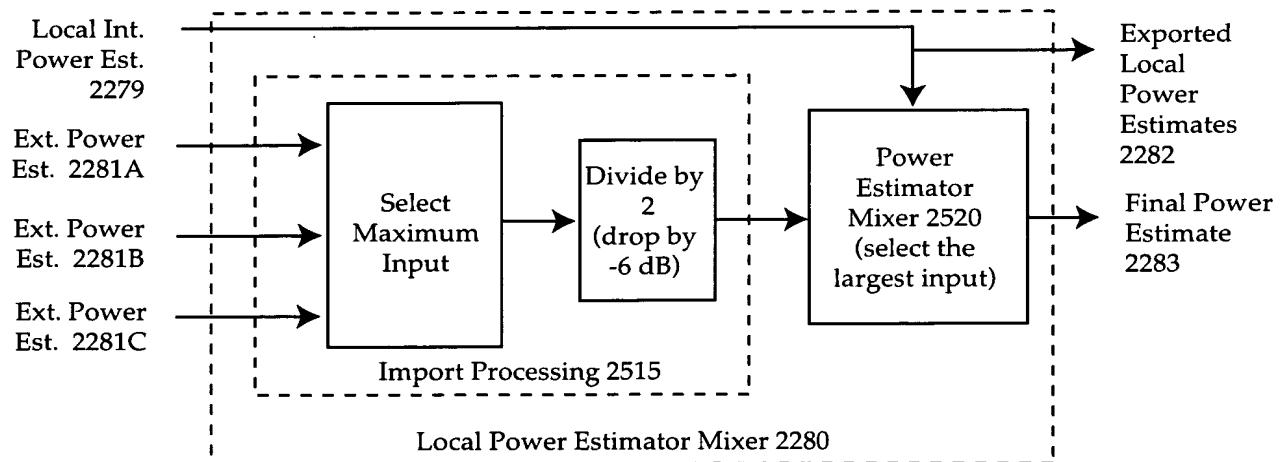


FIG. 25B

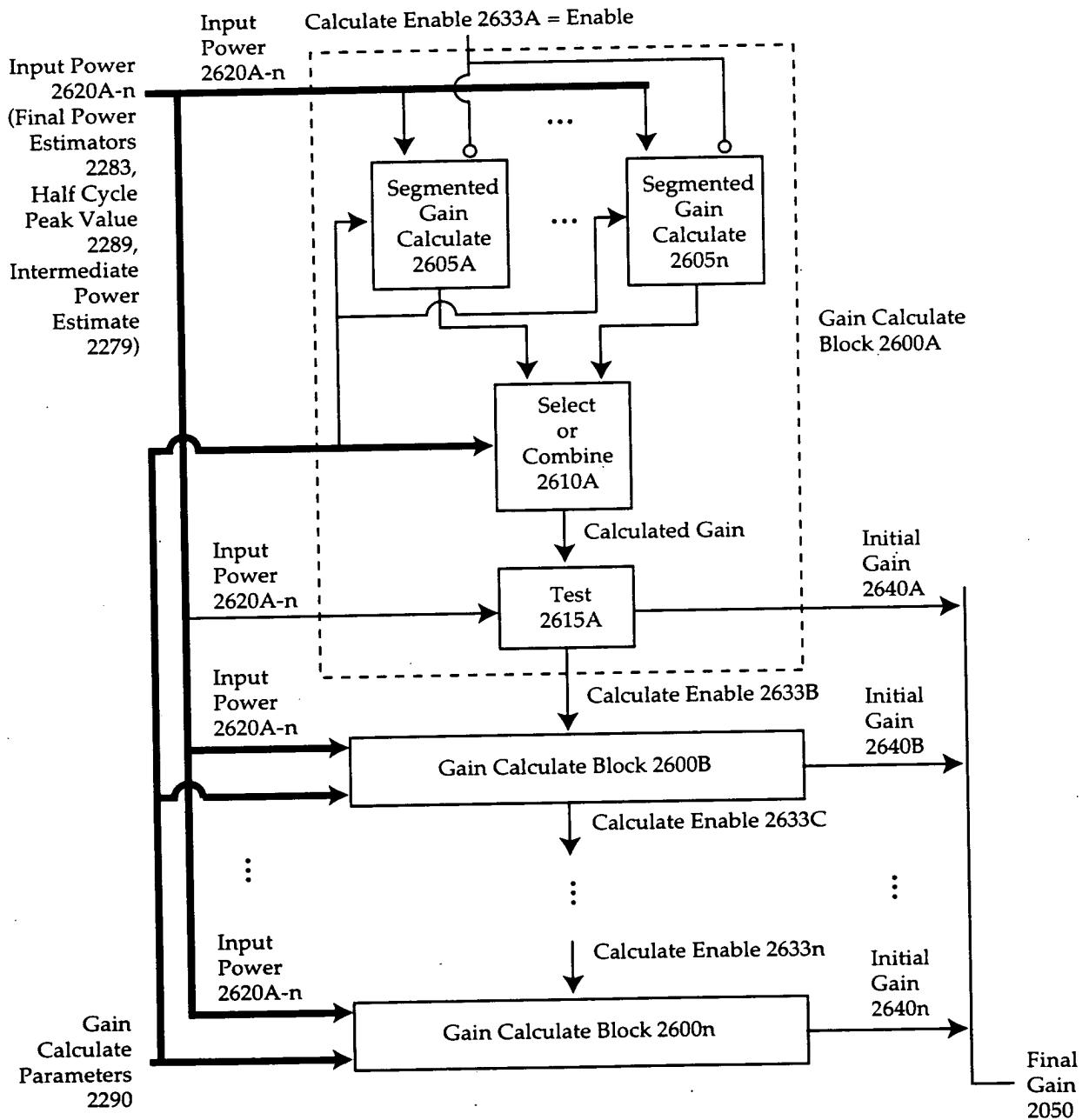


FIG. 26A

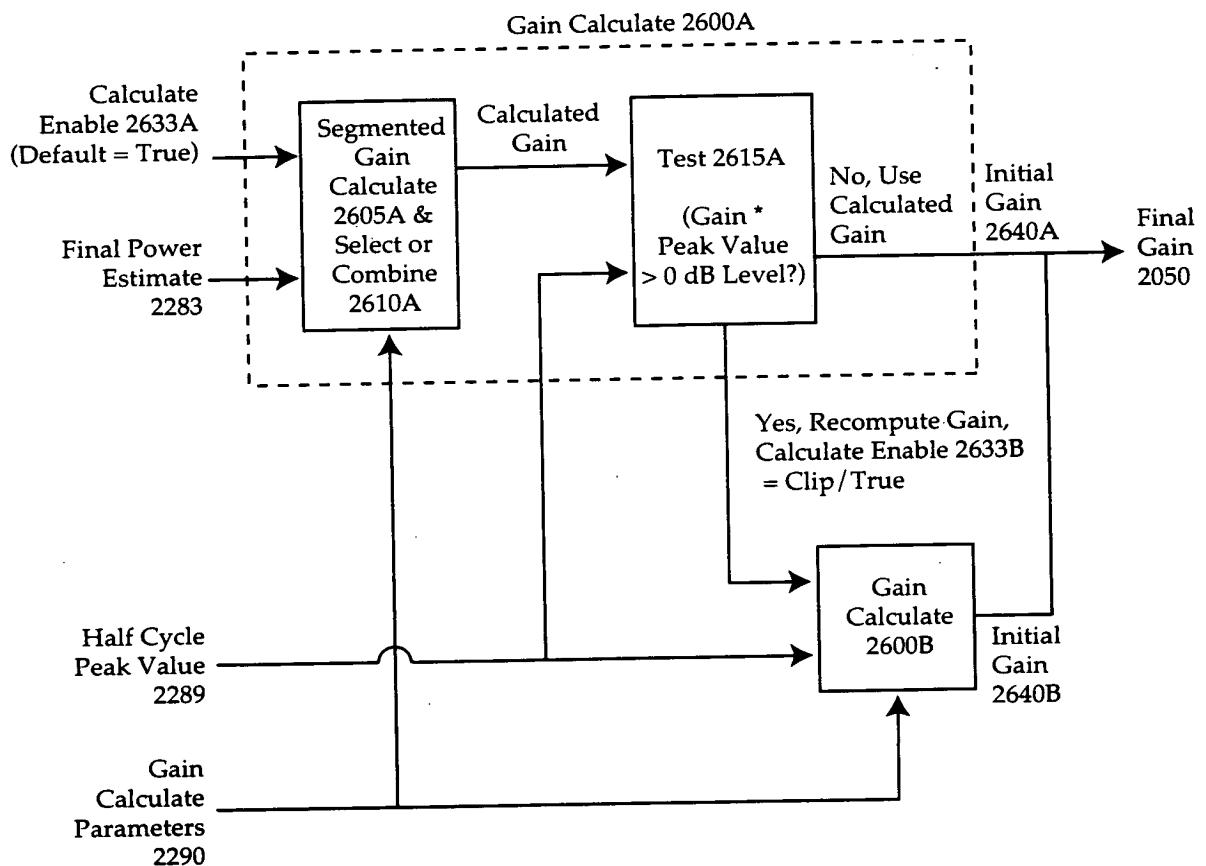


FIG. 26B

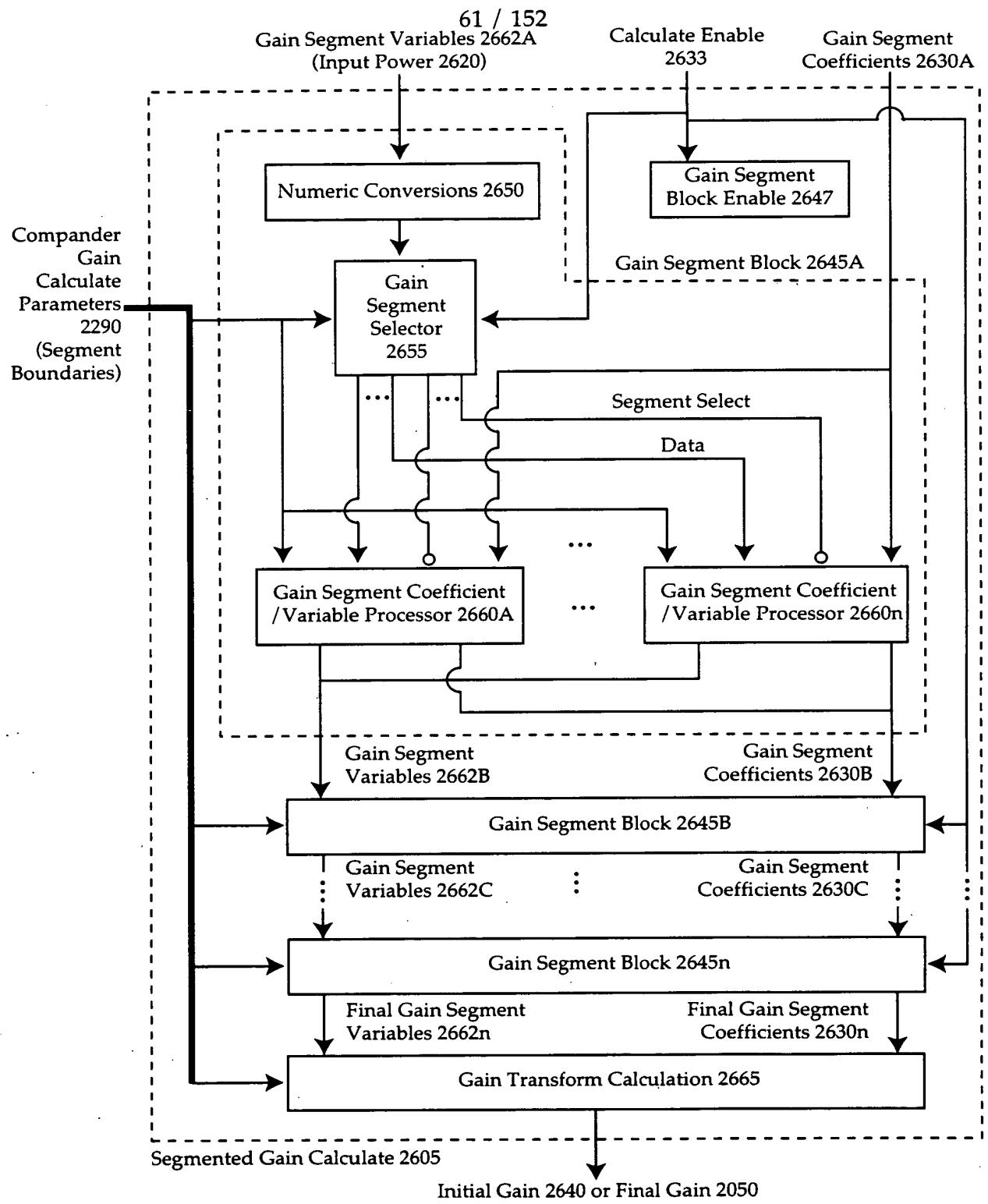


FIG. 26C

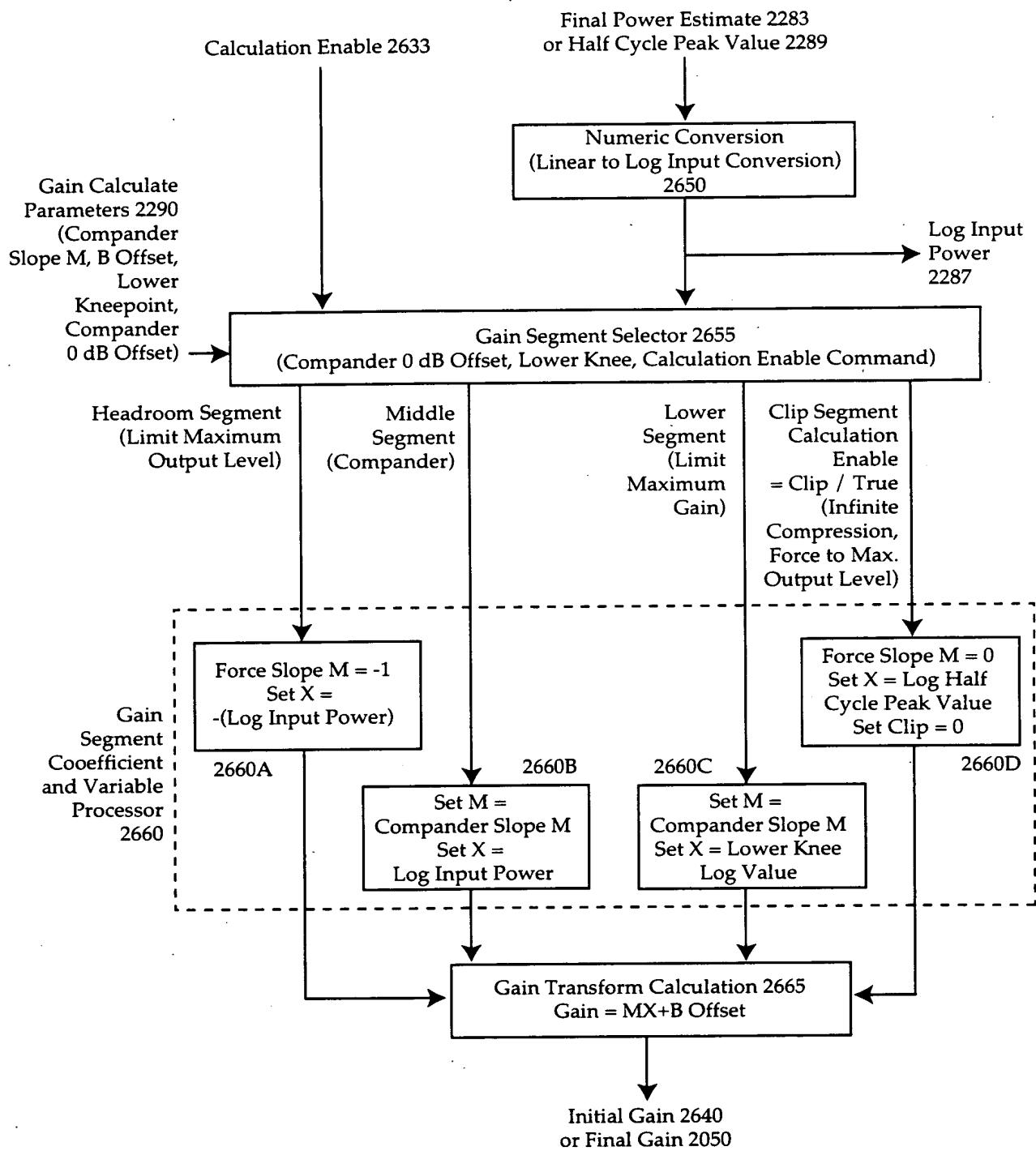


FIG. 26D

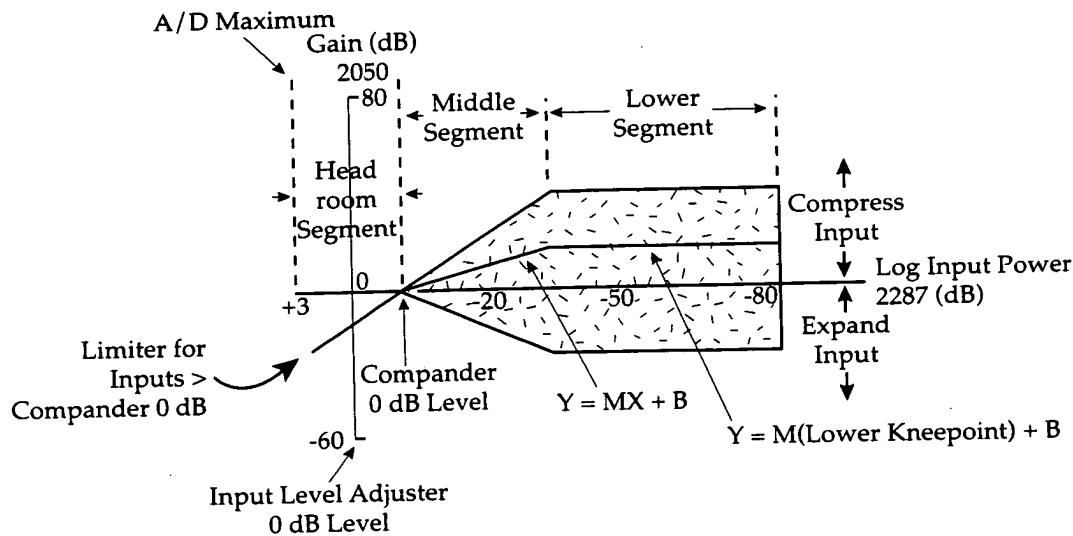


FIG. 26E

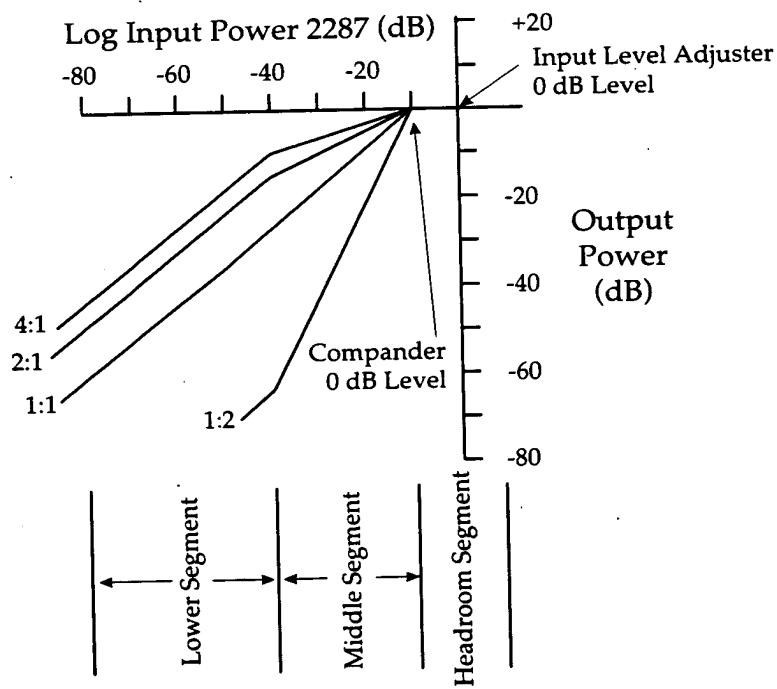


FIG. 26F

$$Y = MX + B$$

= Gain Table Address

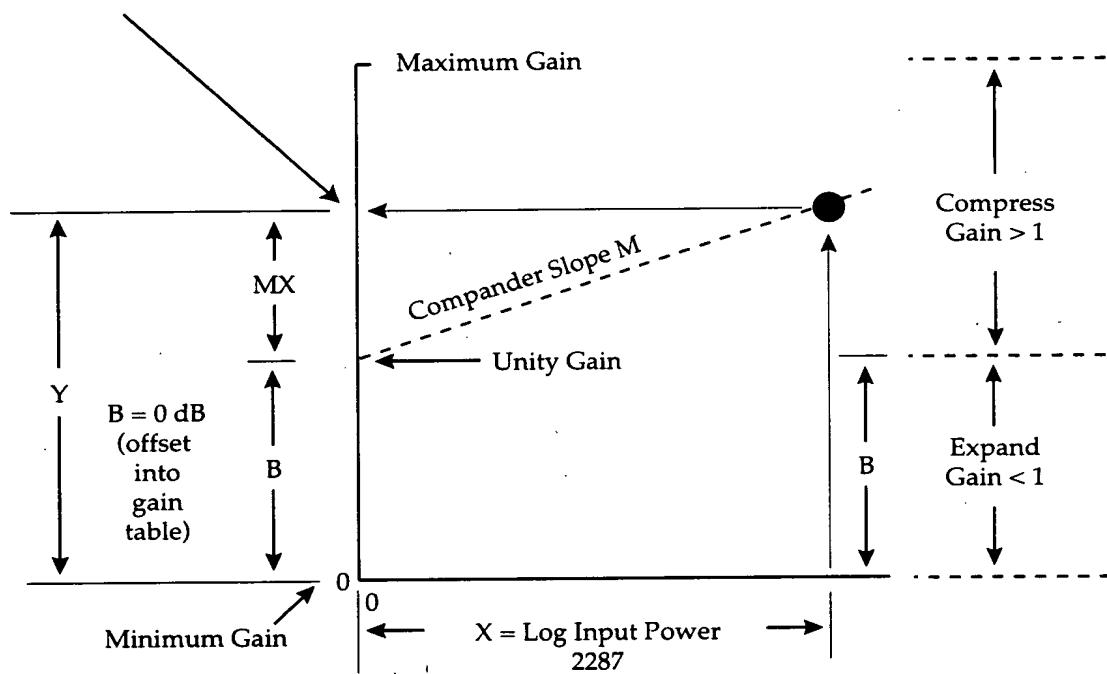


FIG. 26G

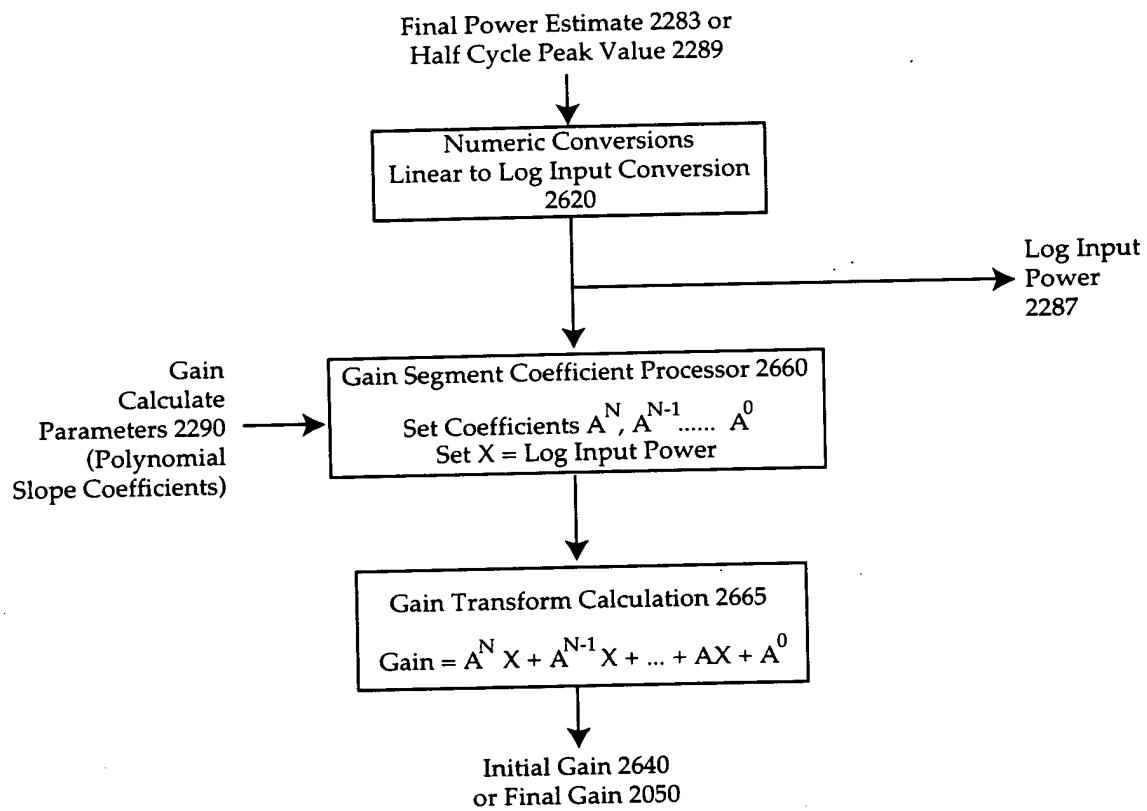


FIG. 26H

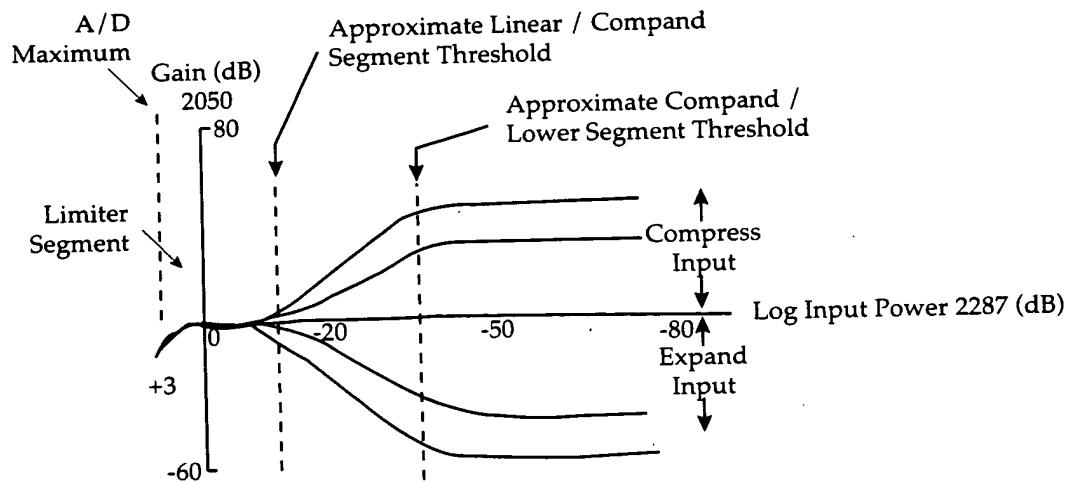


FIG. 26I

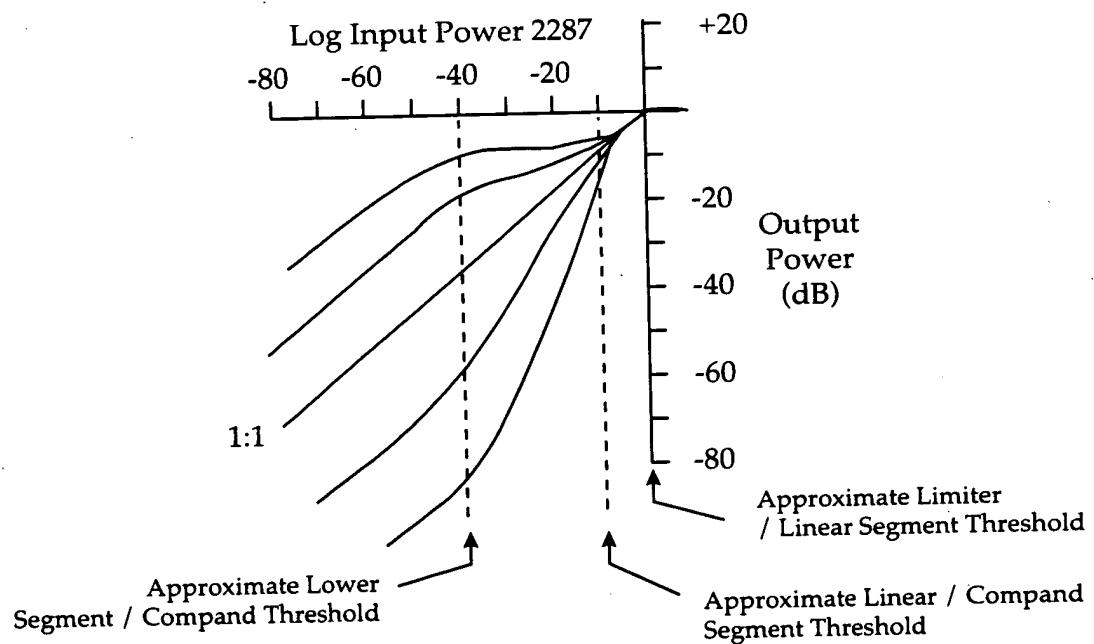


FIG. 26J

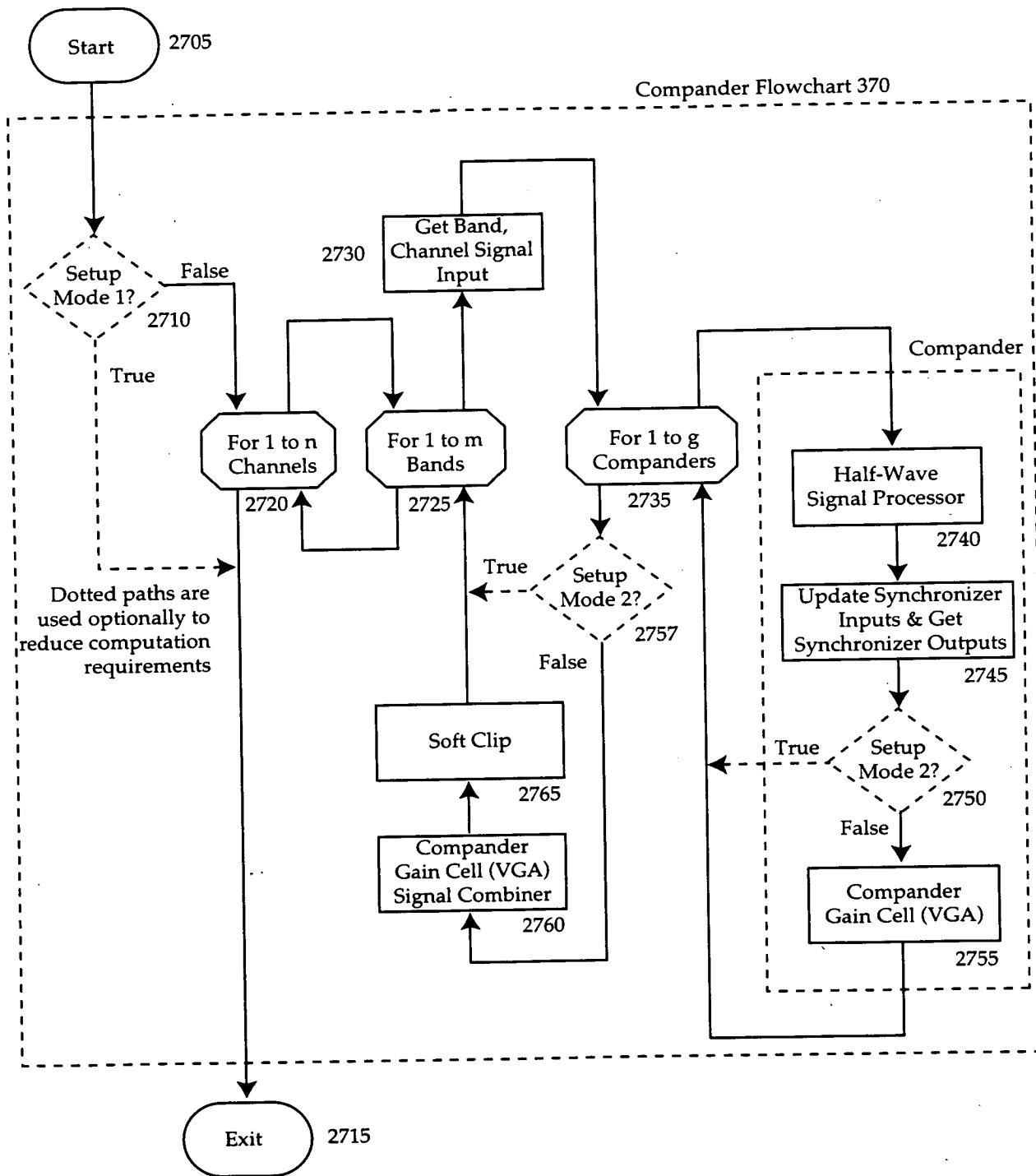


FIG. 27A

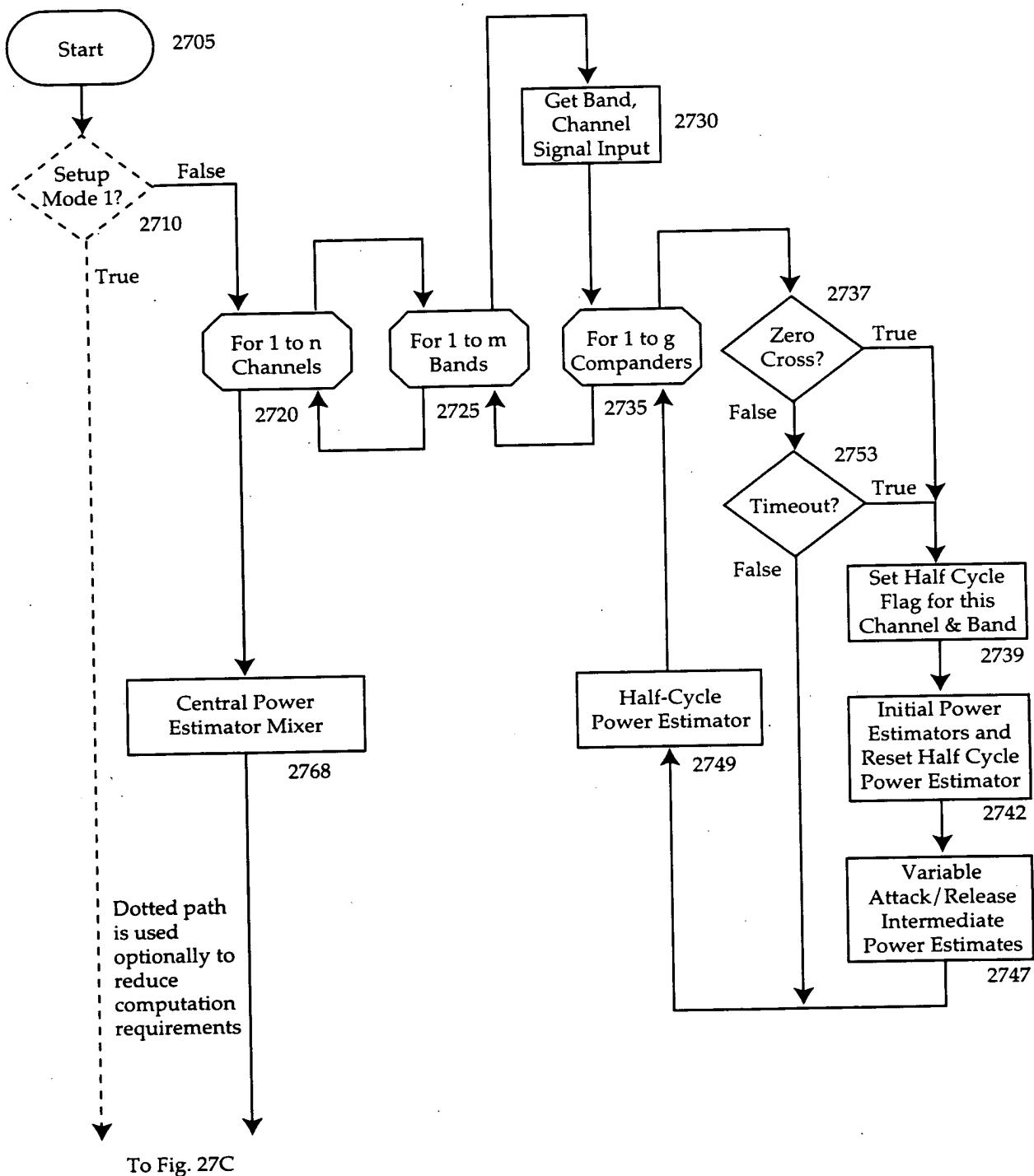


FIG. 27B

From Fig. 27B

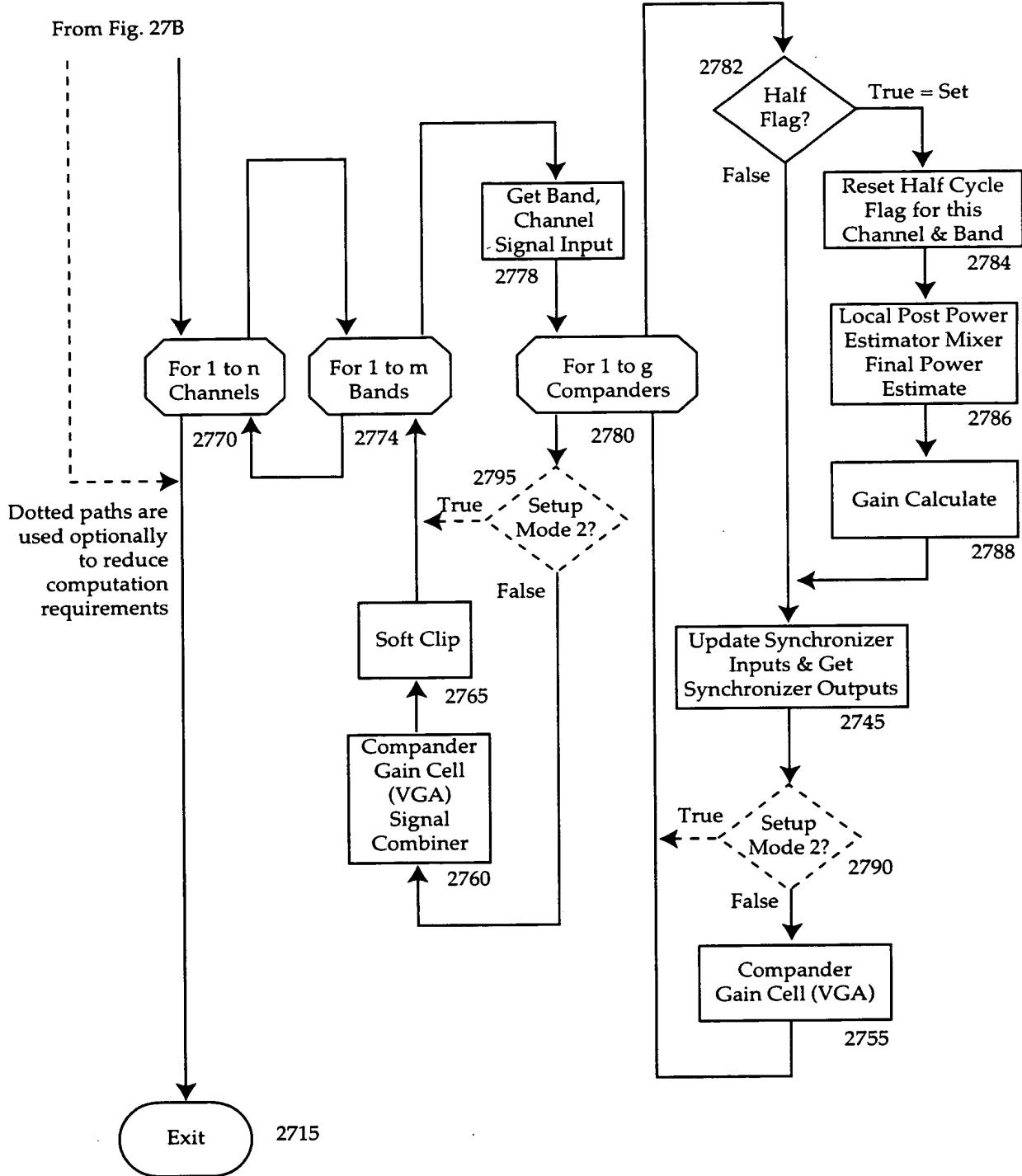


FIG. 27C

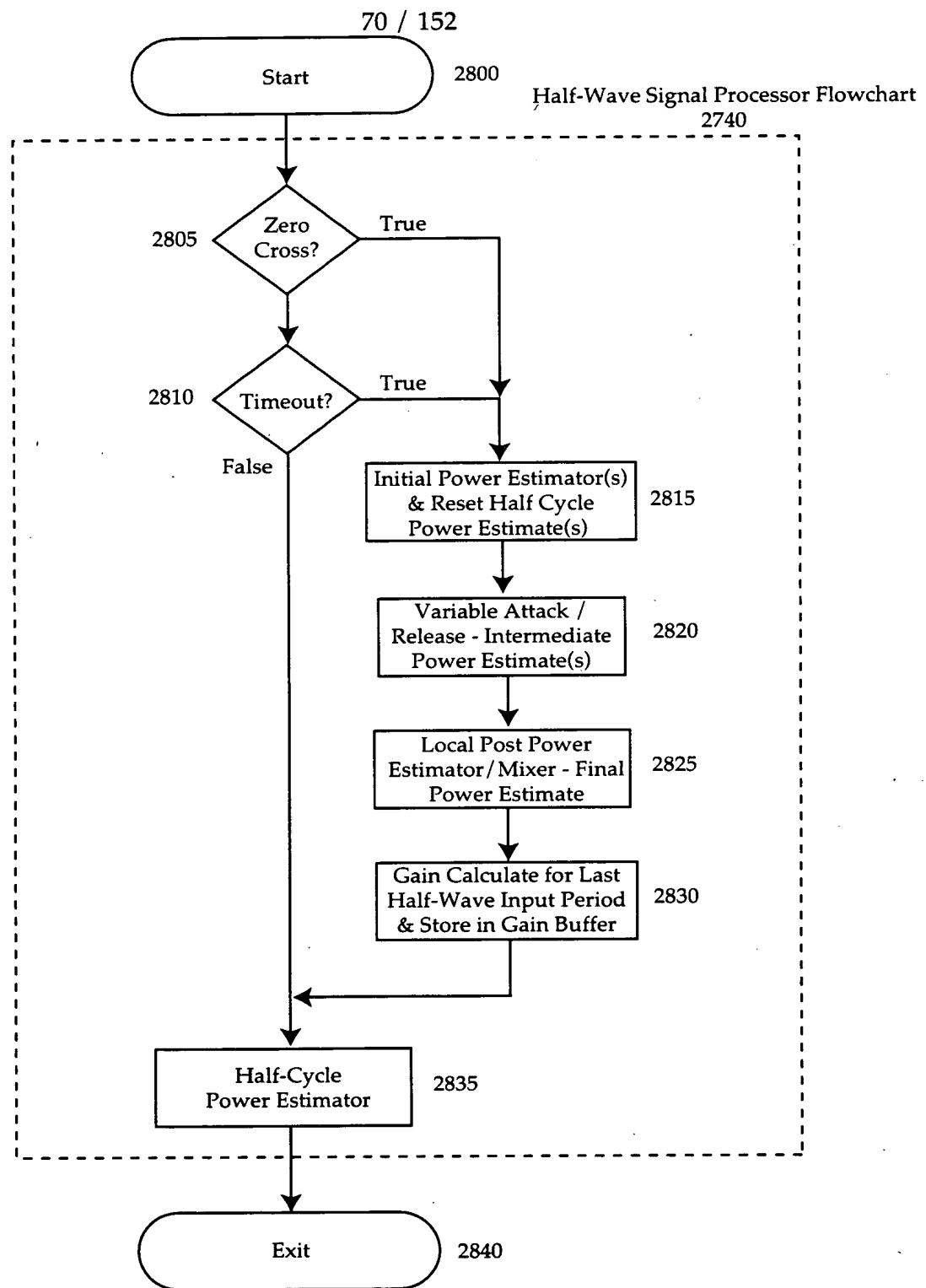


FIG. 28

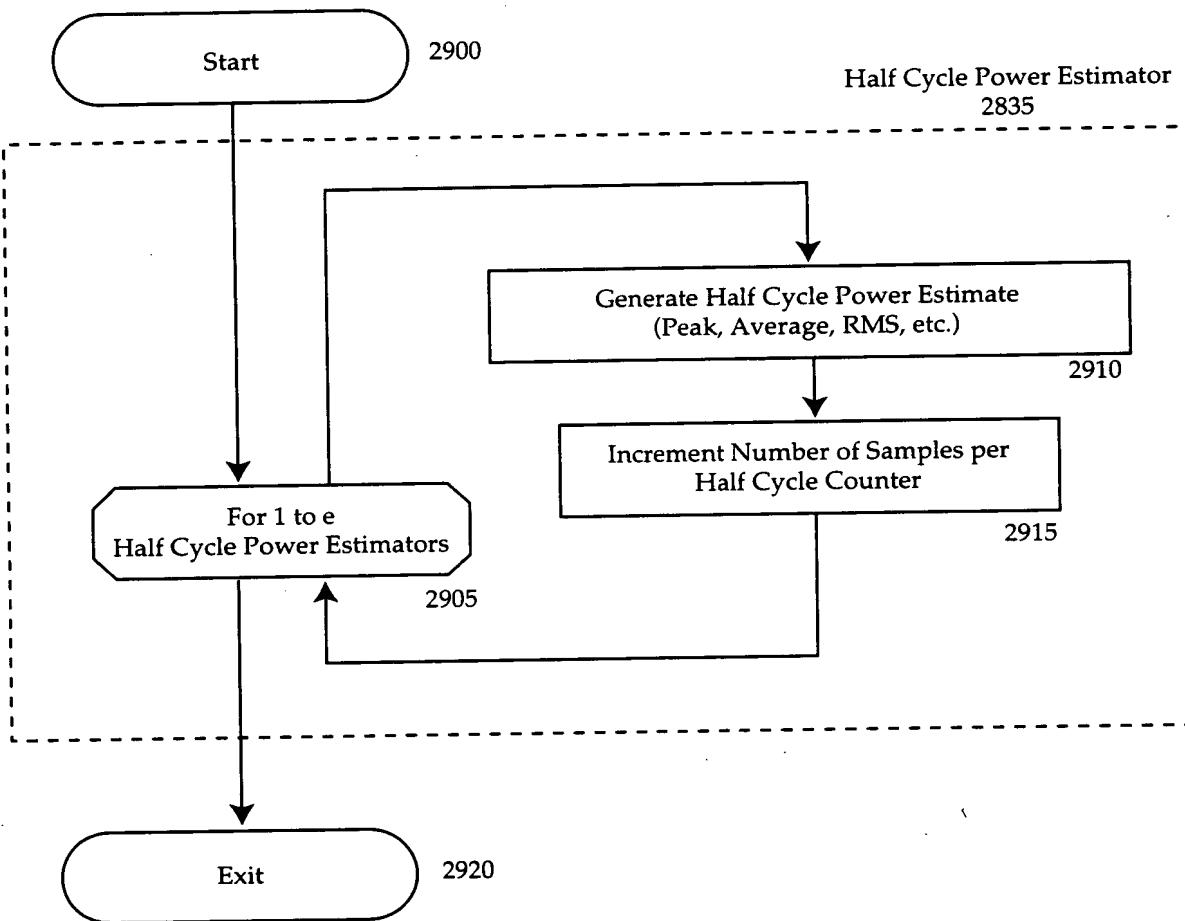


FIG. 29

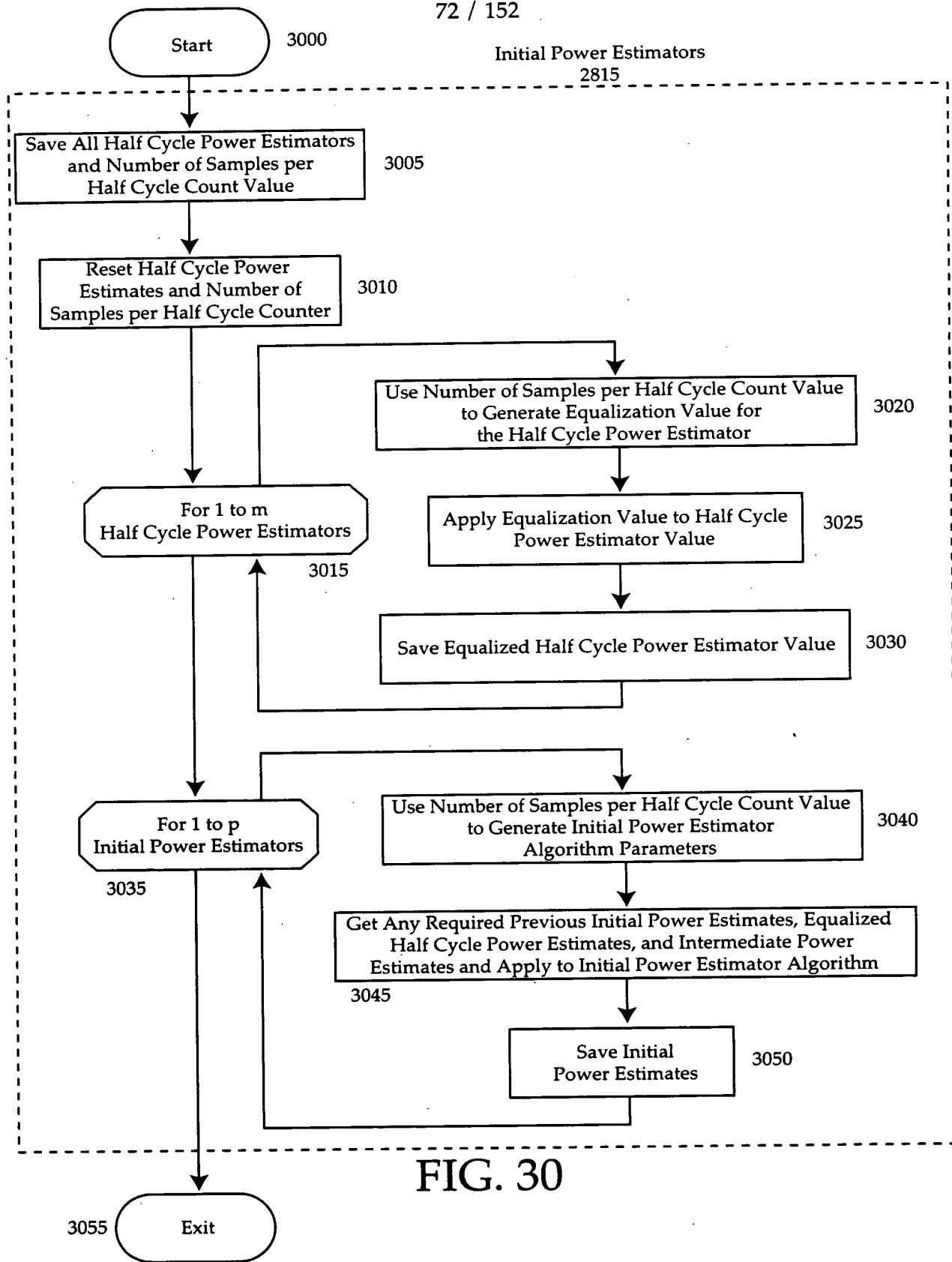


FIG. 30

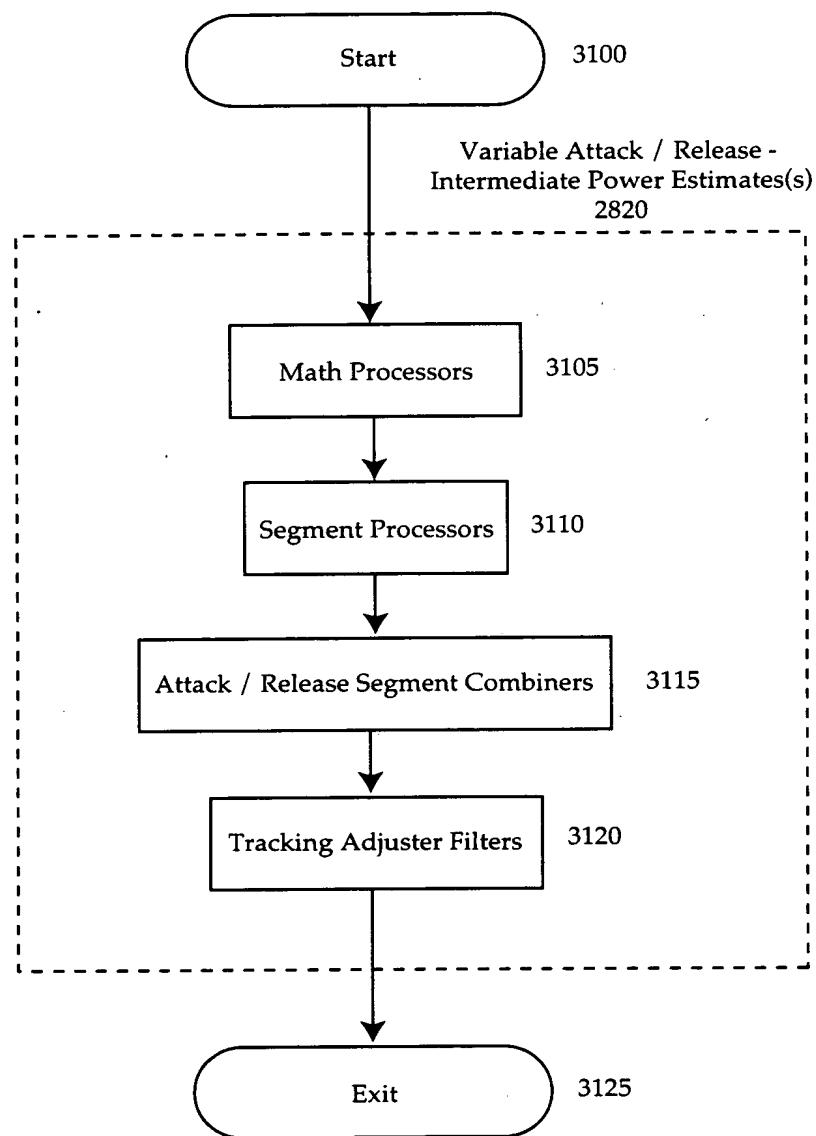


FIG. 31

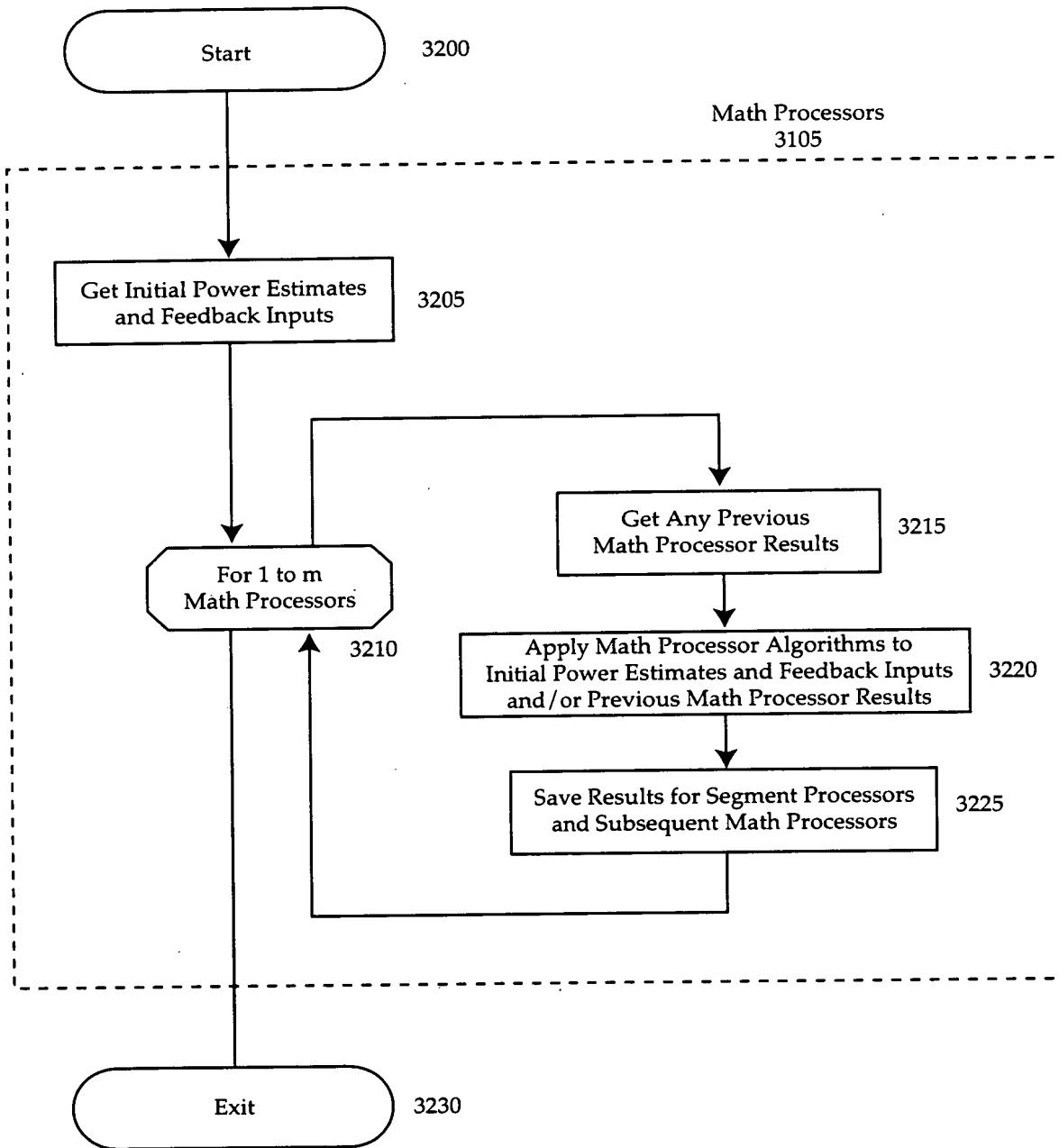


FIG. 32

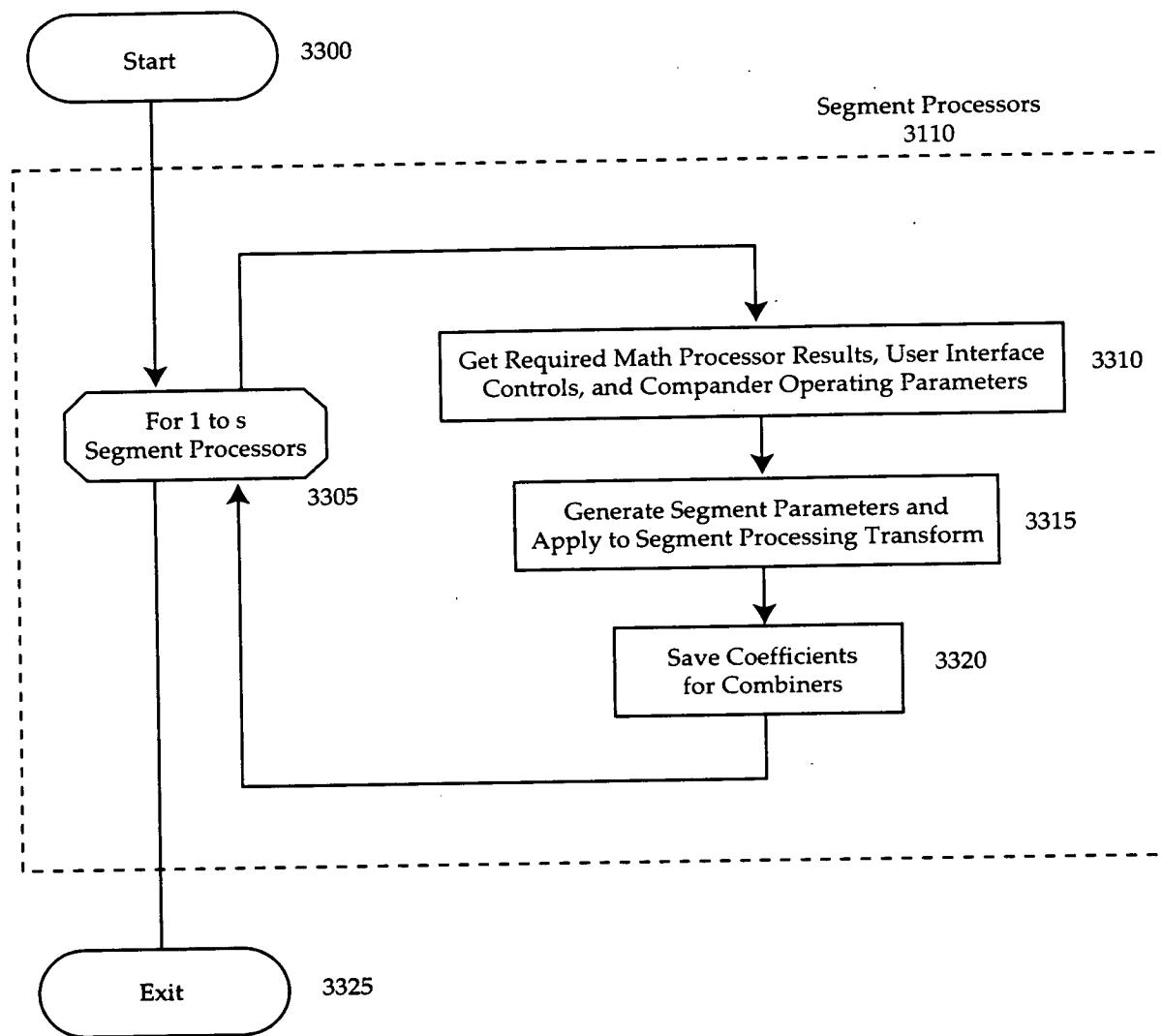


FIG. 33

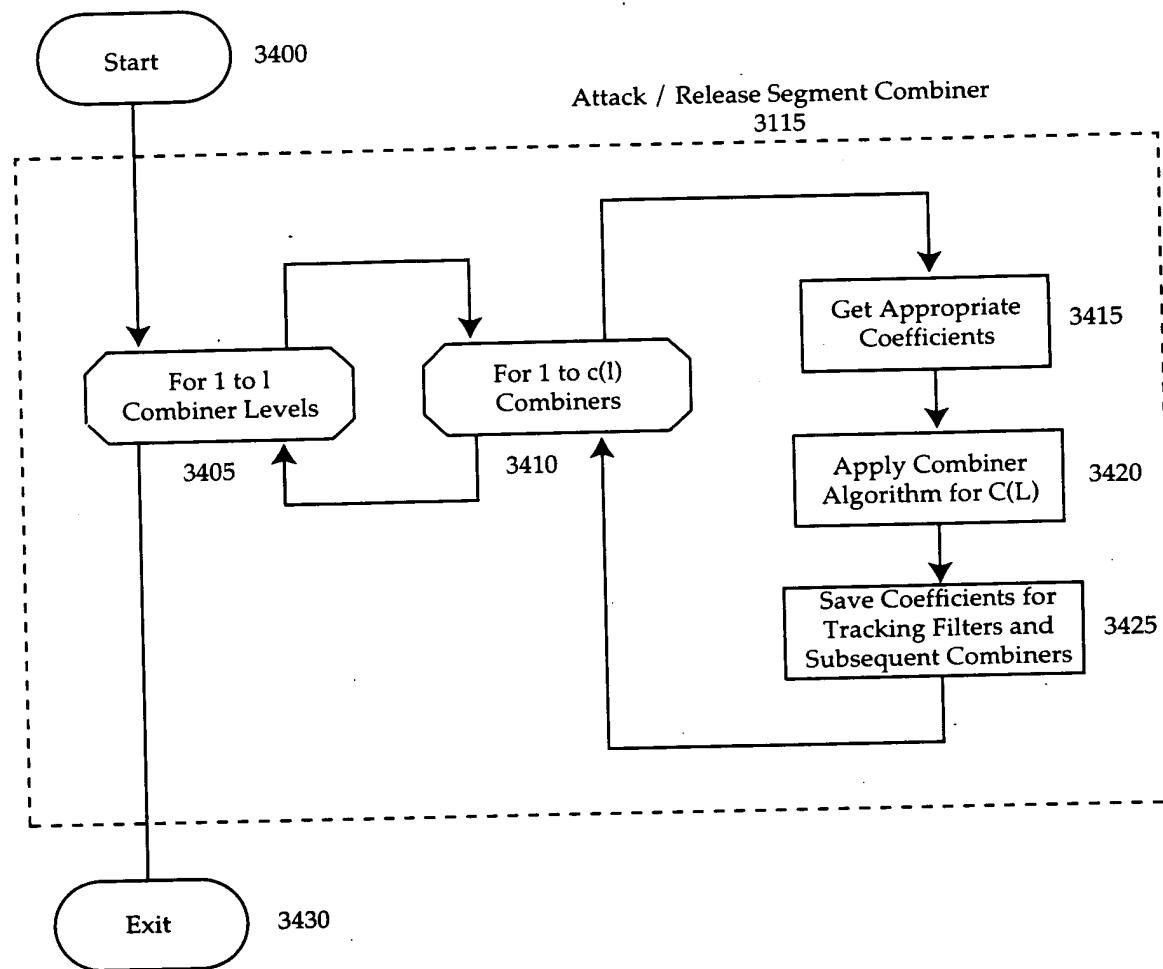


FIG. 34

00000000000000000000000000000000

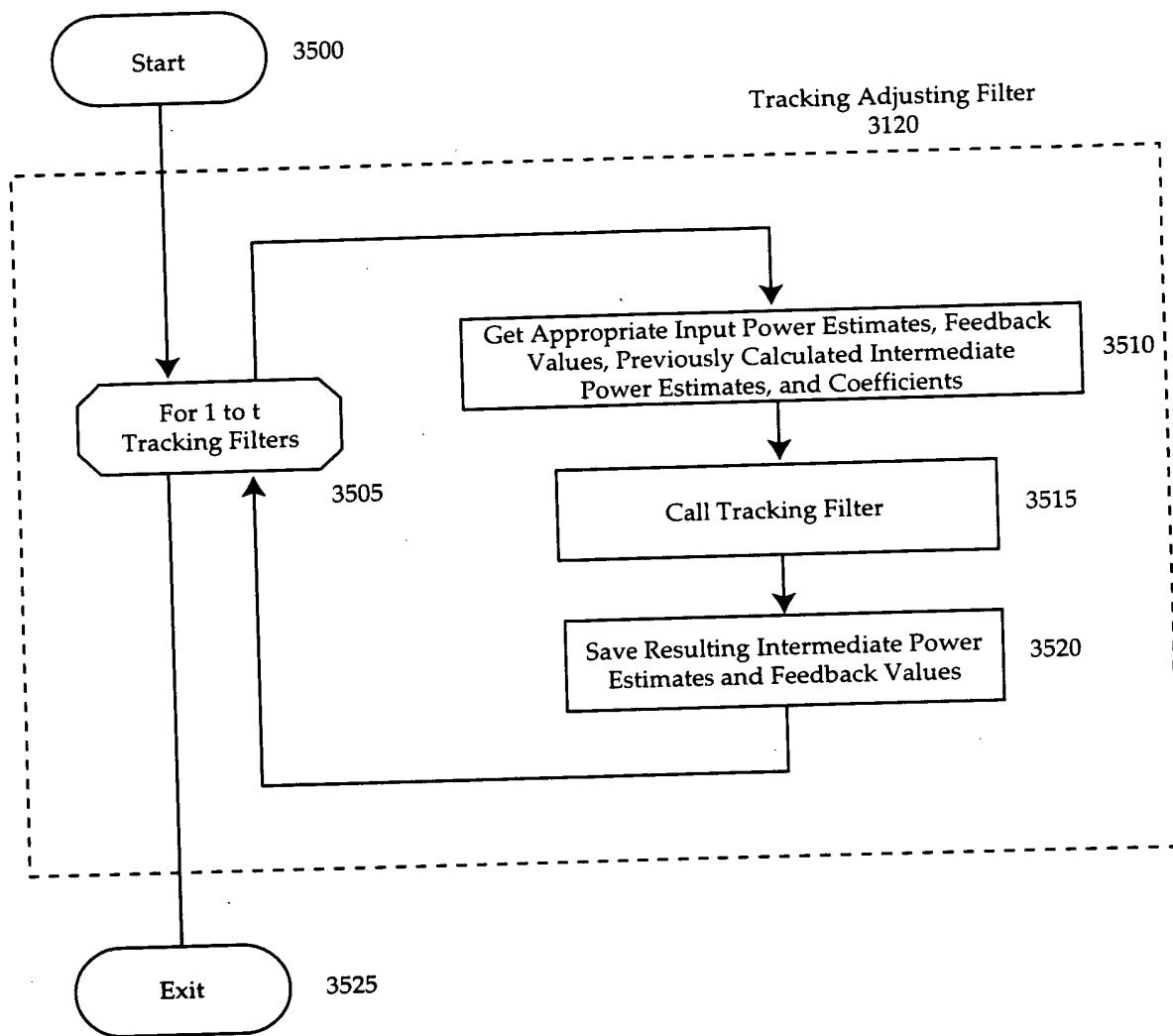


FIG. 35

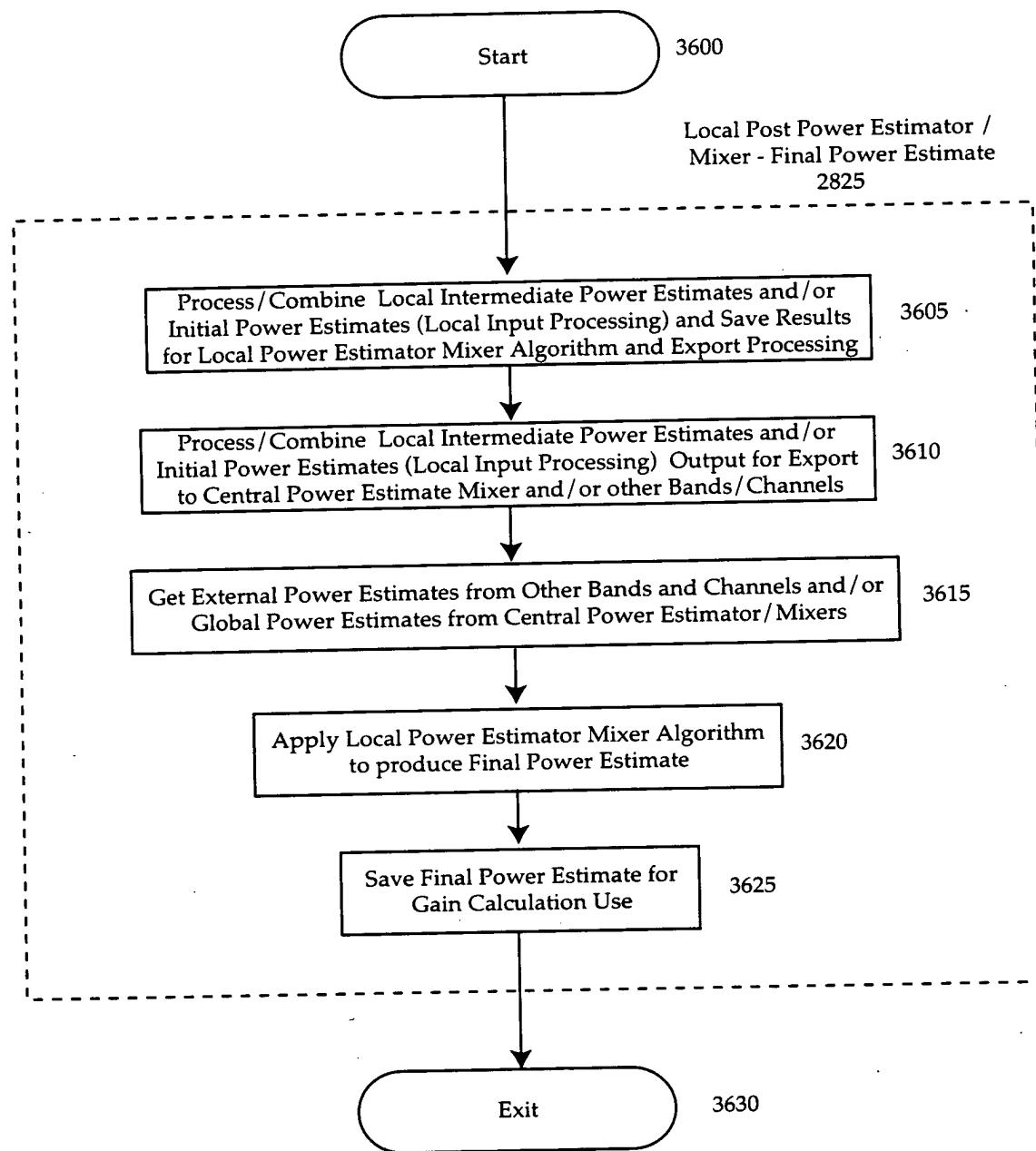


FIG. 36

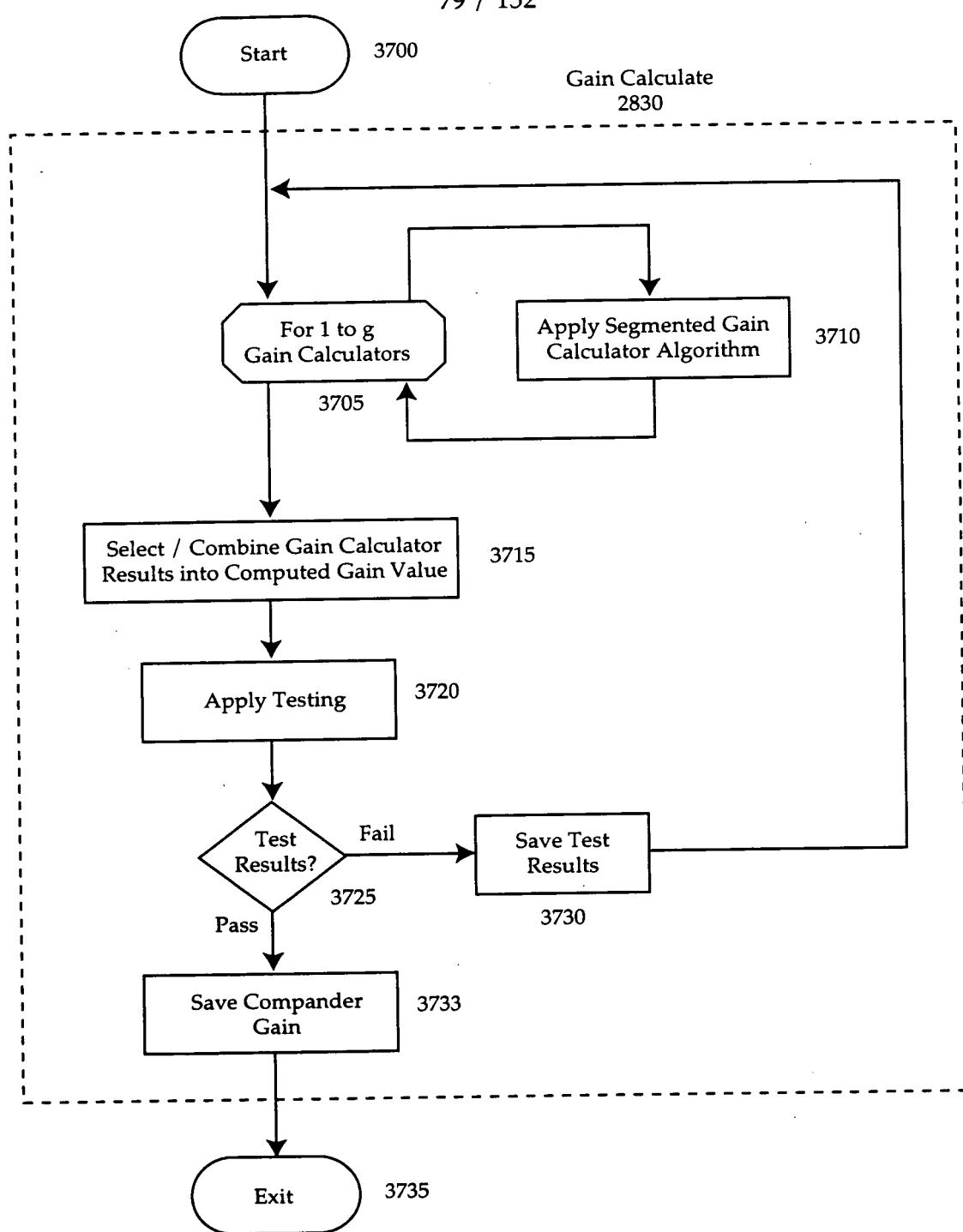


FIG. 37A

DRAFT - DRAFTING STAGE

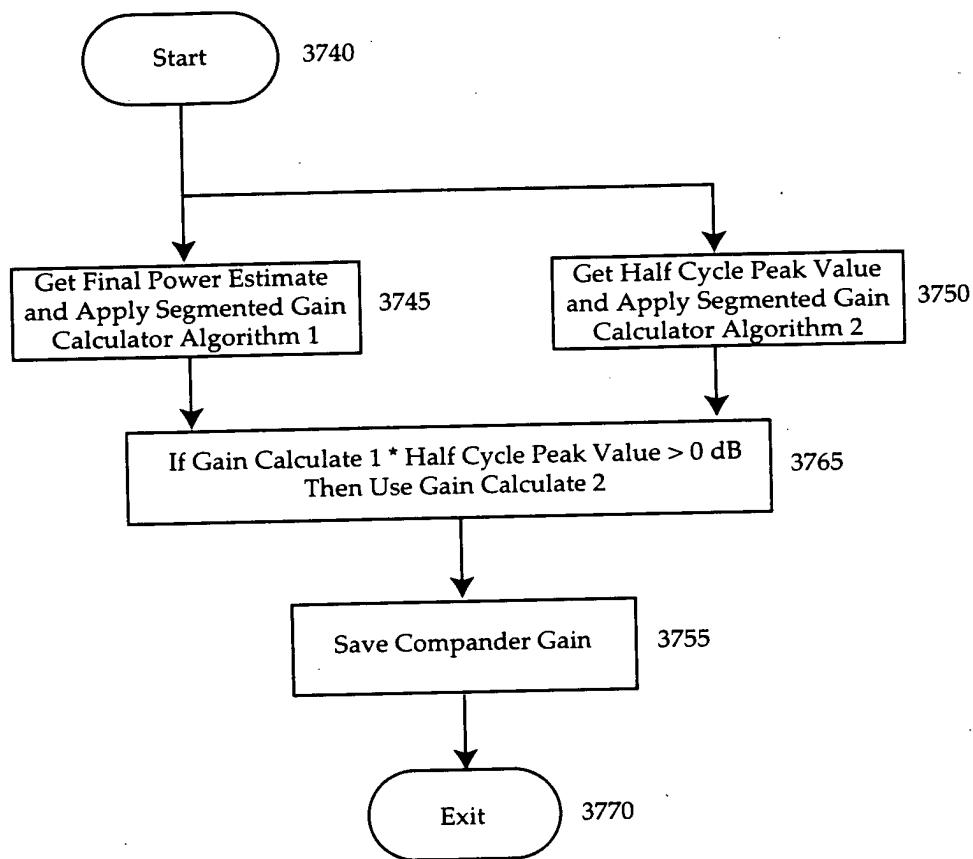


FIG. 37B

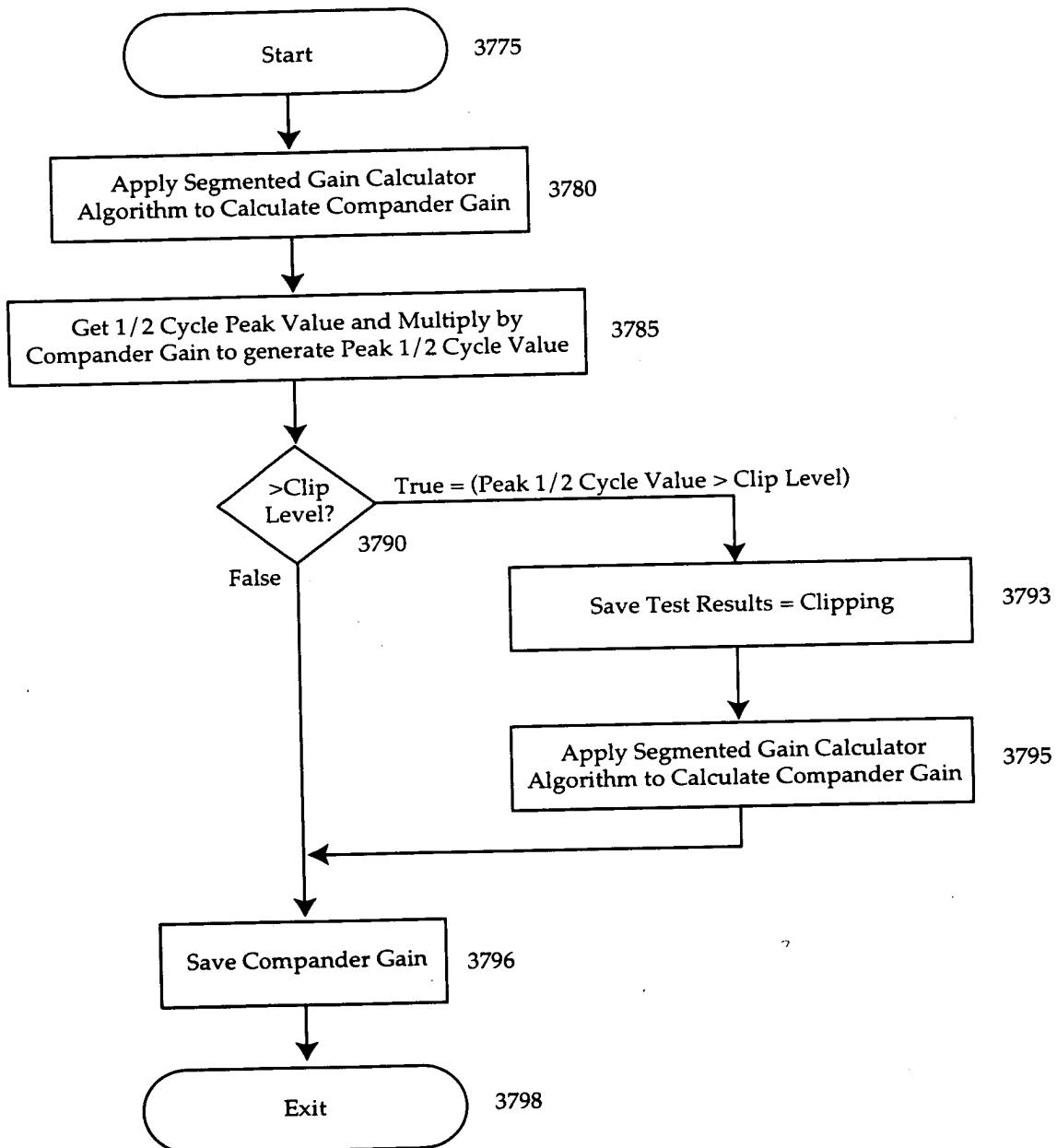


FIG. 37C

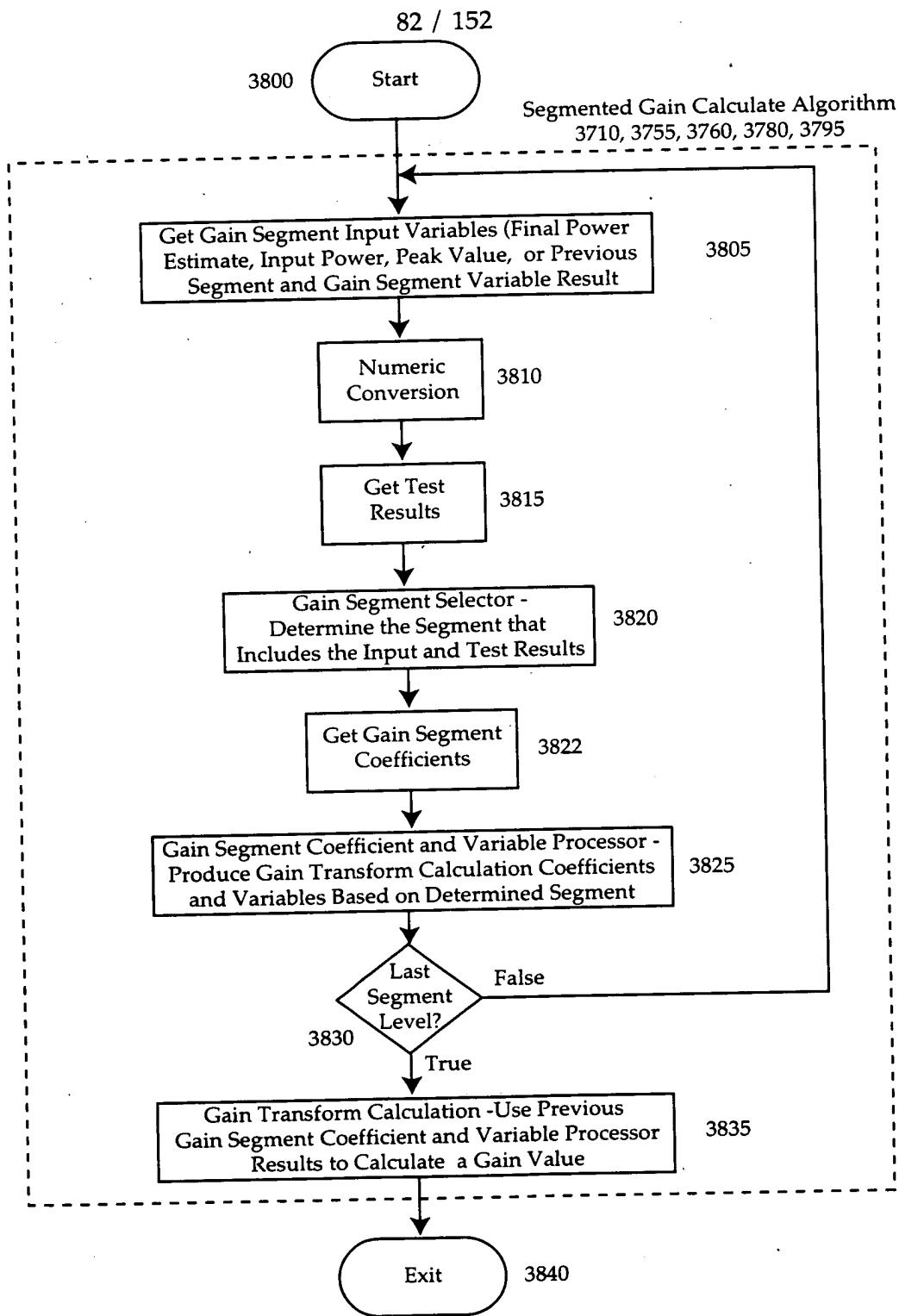


FIG. 38

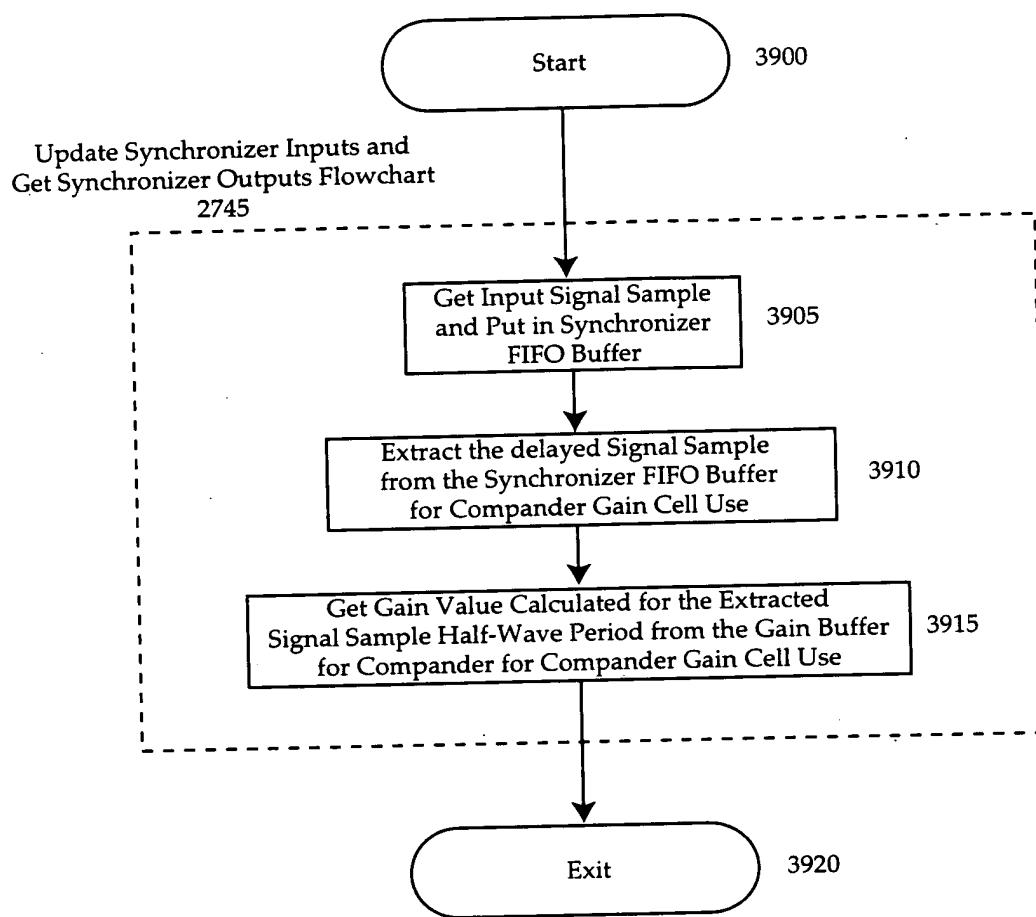


FIG. 39

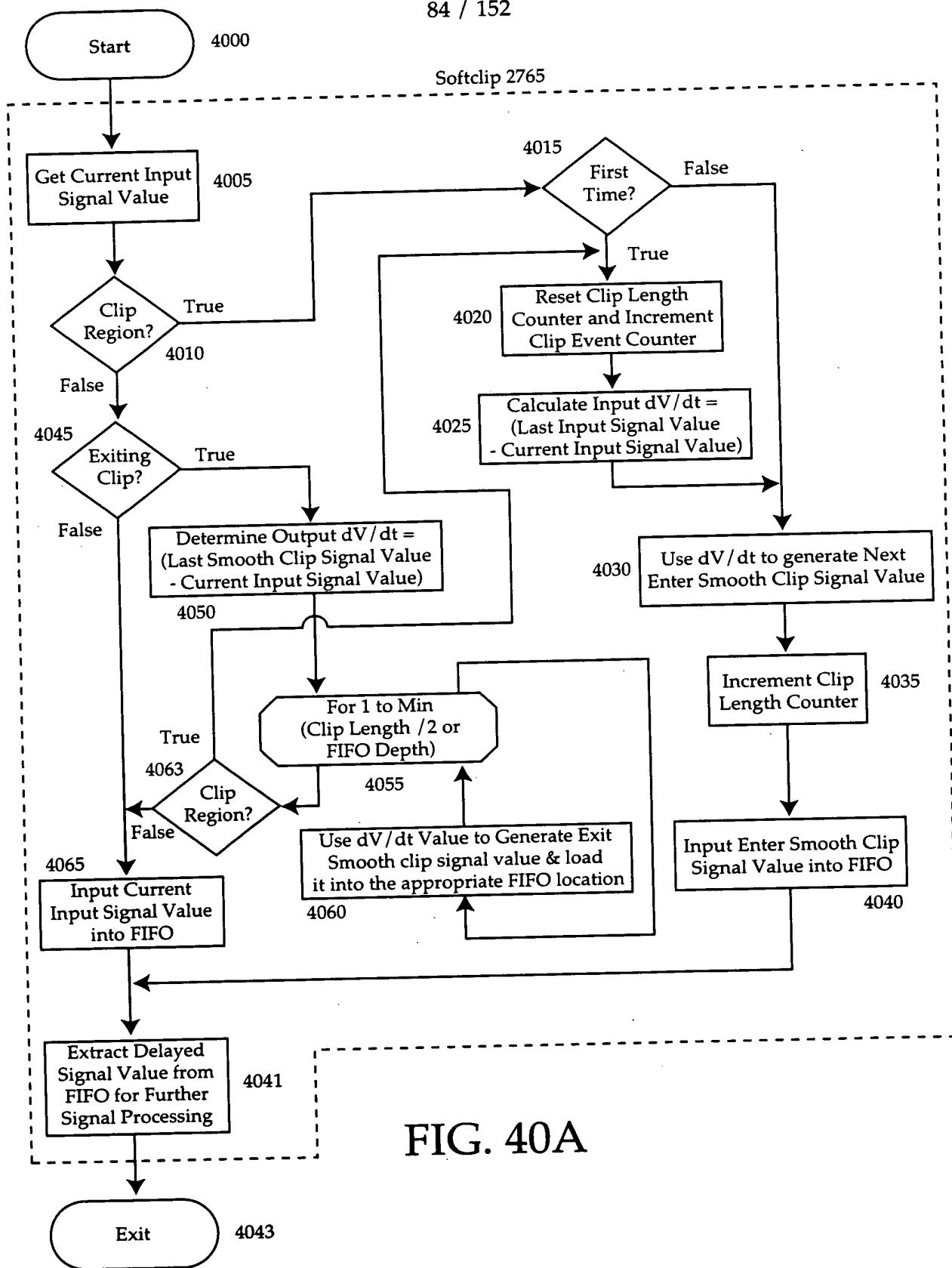


FIG. 40A

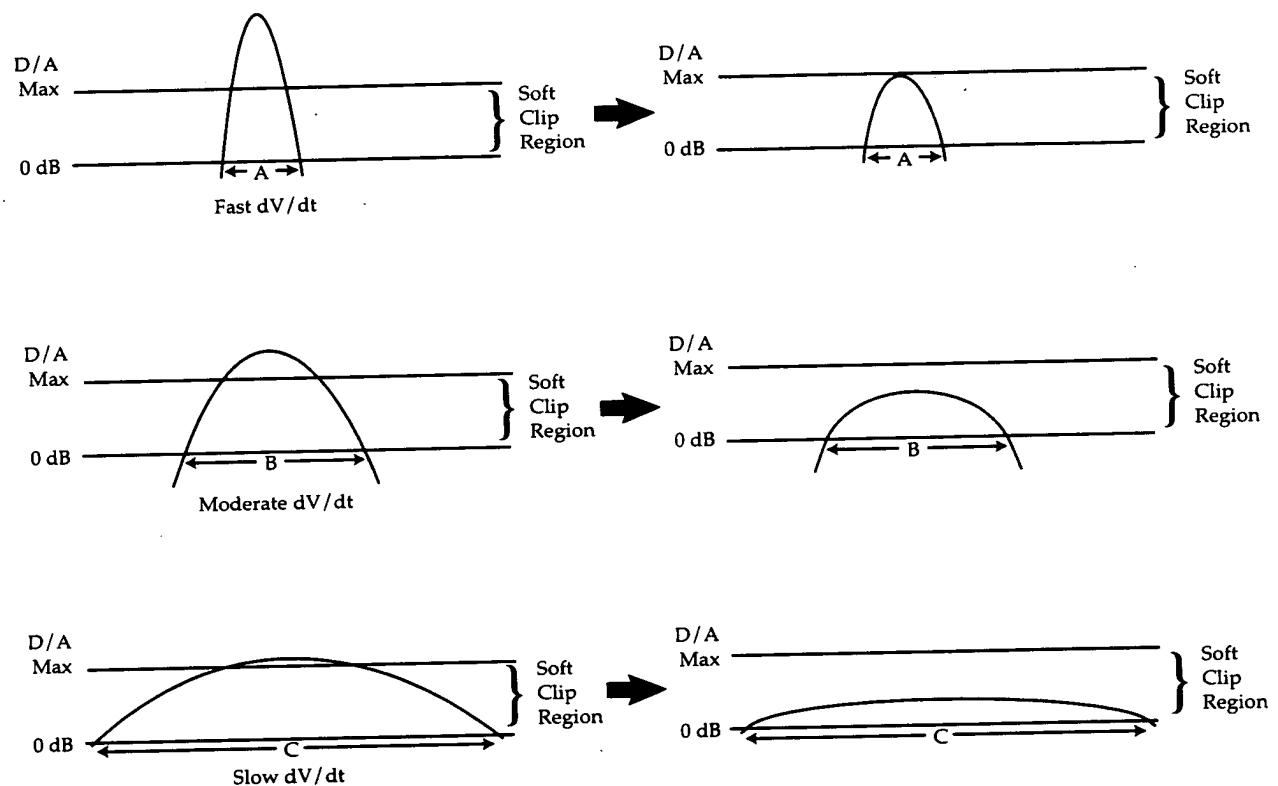


FIG. 40B

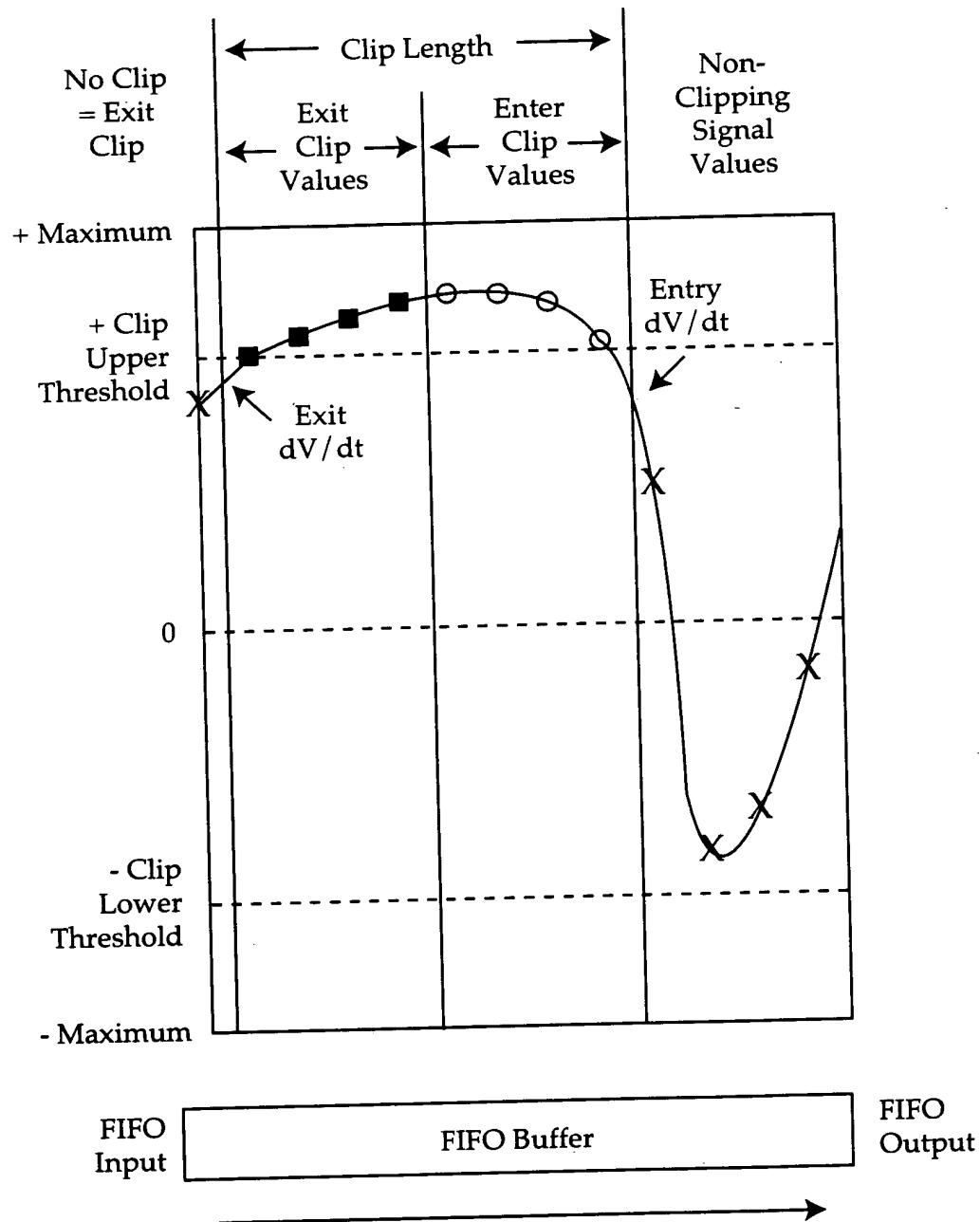


FIG. 40C

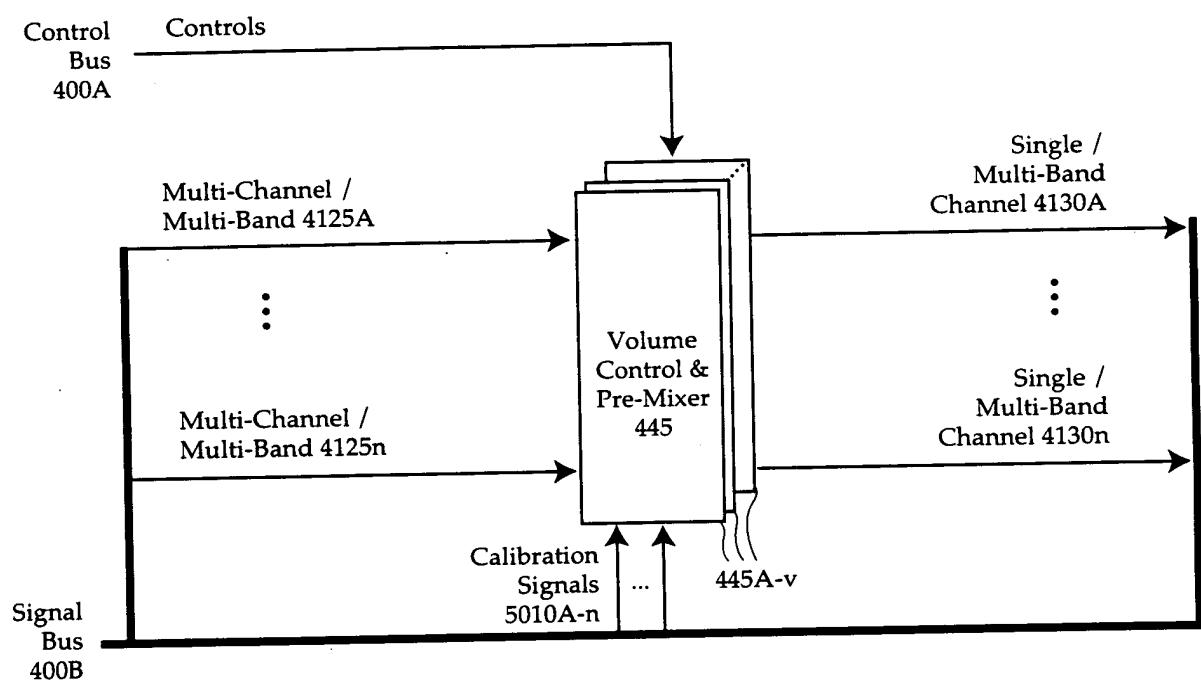


FIG. 41A

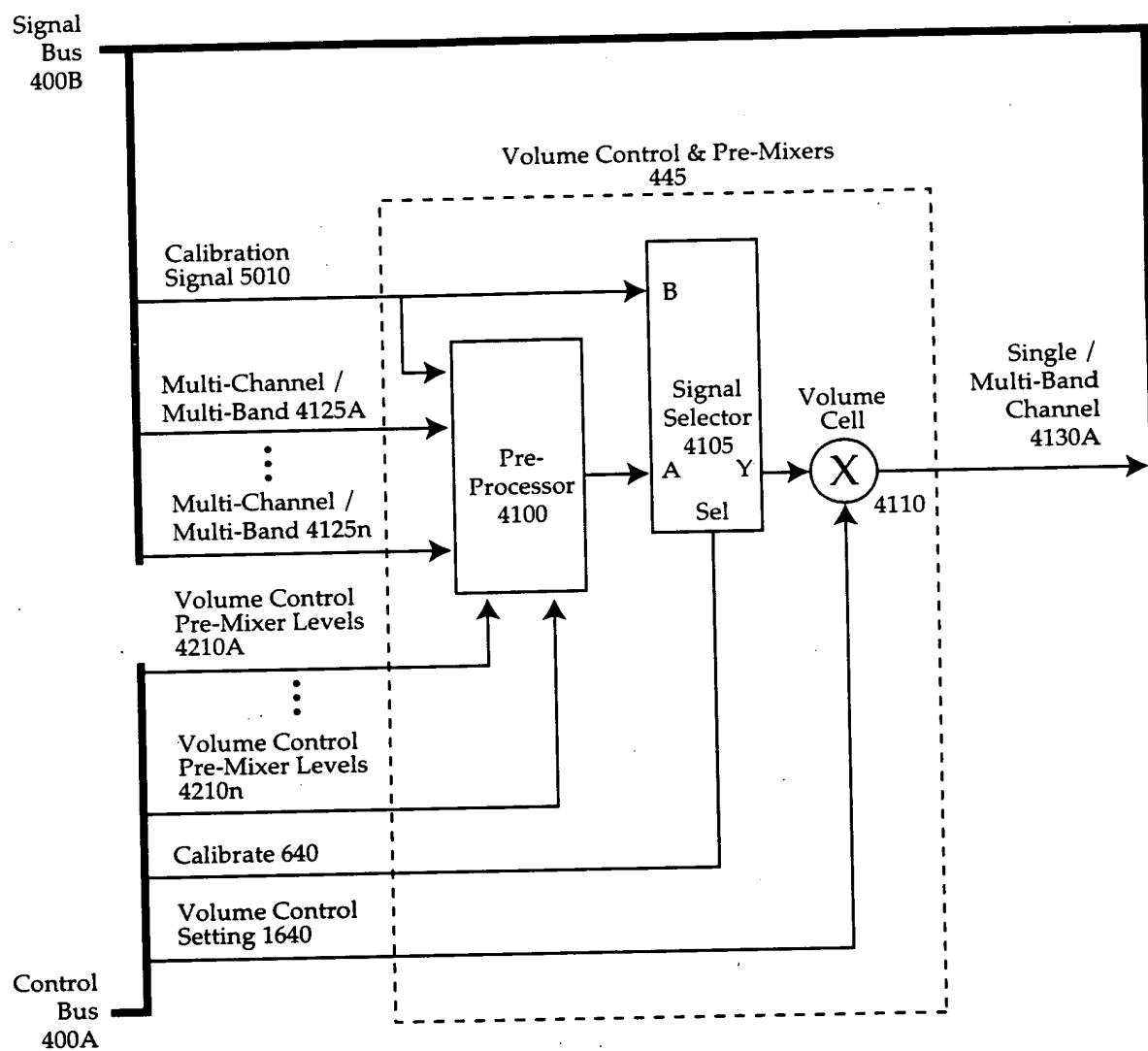


FIG. 41B

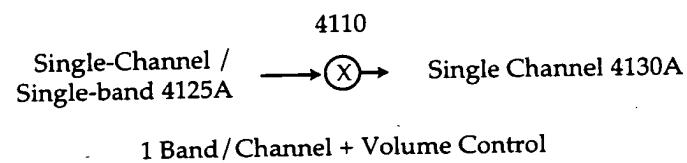


FIG. 42A

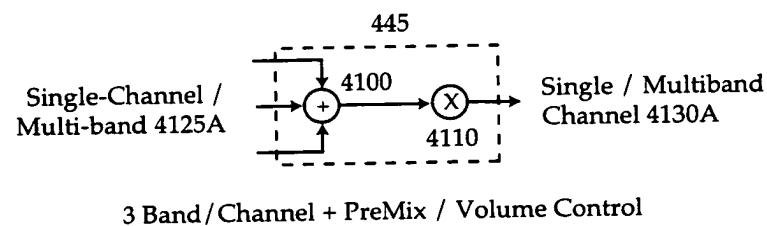
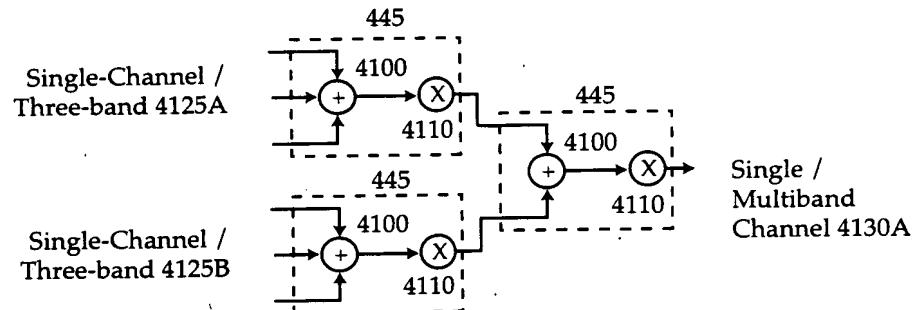
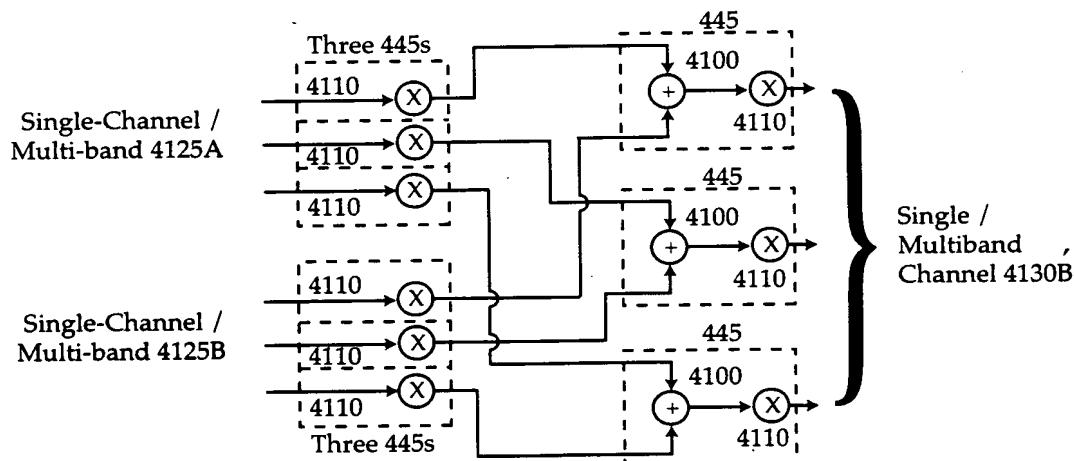


FIG. 42B



3 Band/Channel + Pre-Mix / Volume Control > Premix Volume Control
3 Band/Channel + Pre-Mix / Volume Control

FIG. 42C



3 Band/Channel + 3 Volume Control > 3 Premix Volume Control
3 Band/Channel + 3 Volume Control

FIG. 42D

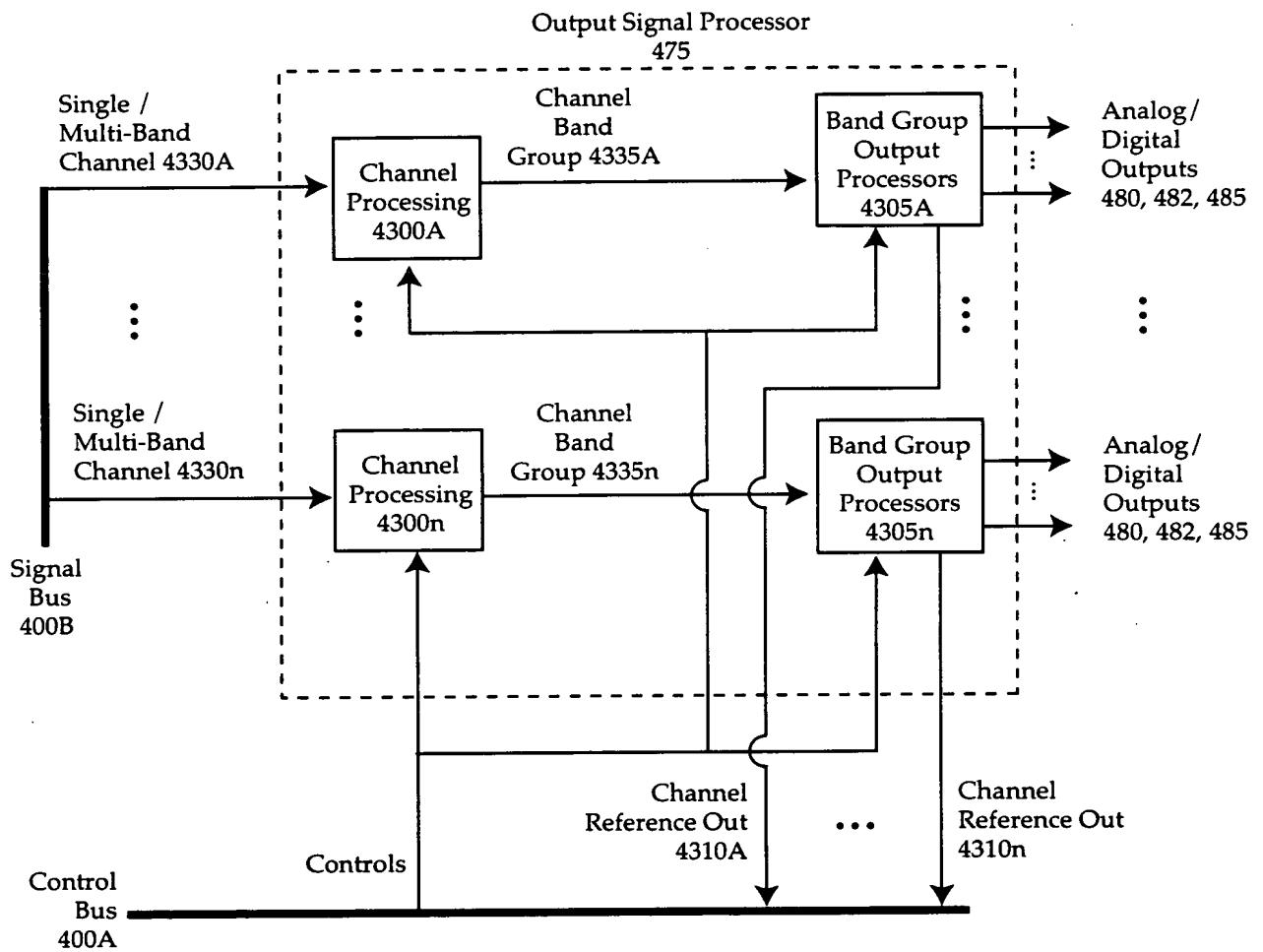


FIG. 43A

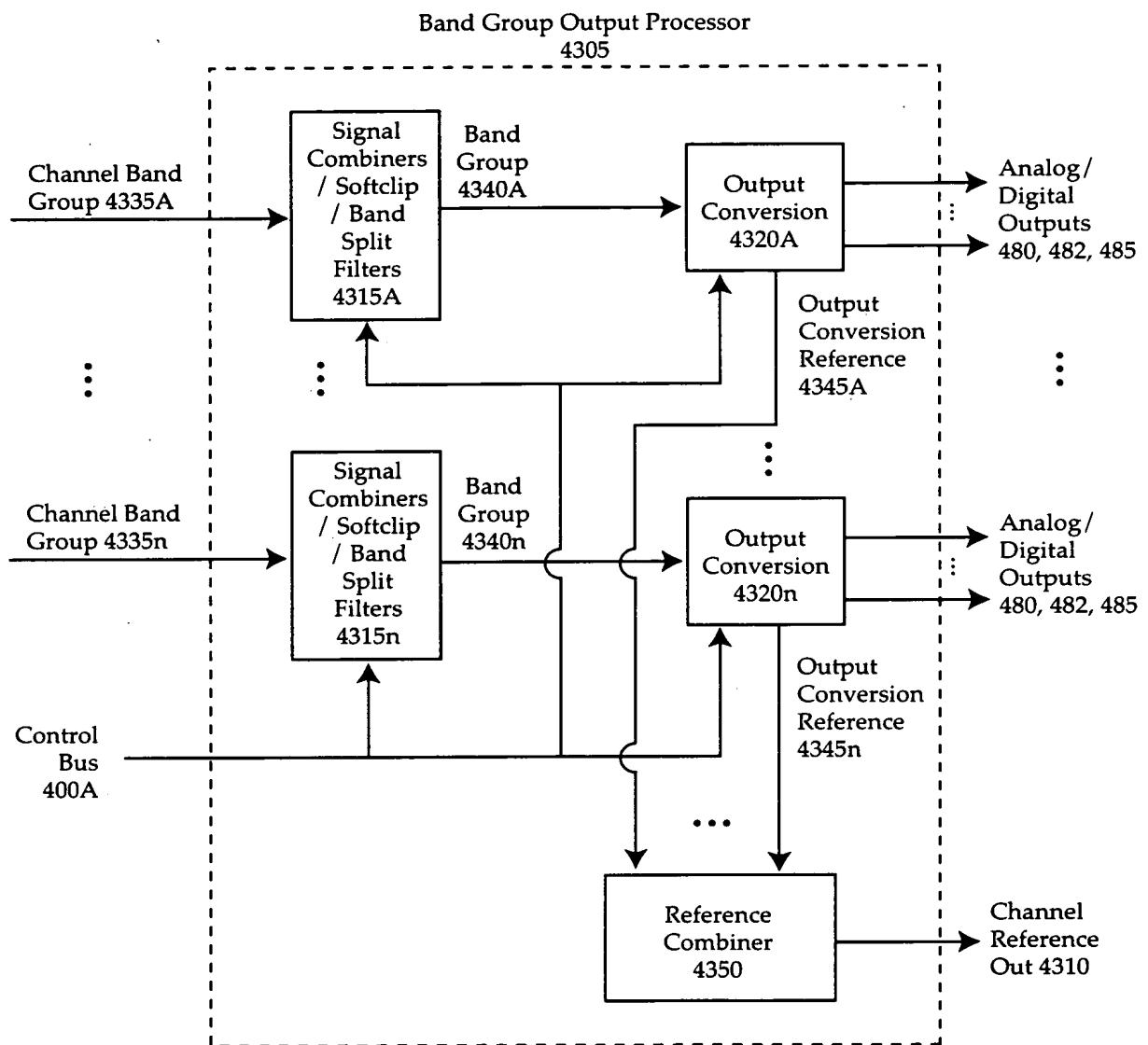


FIG. 43B

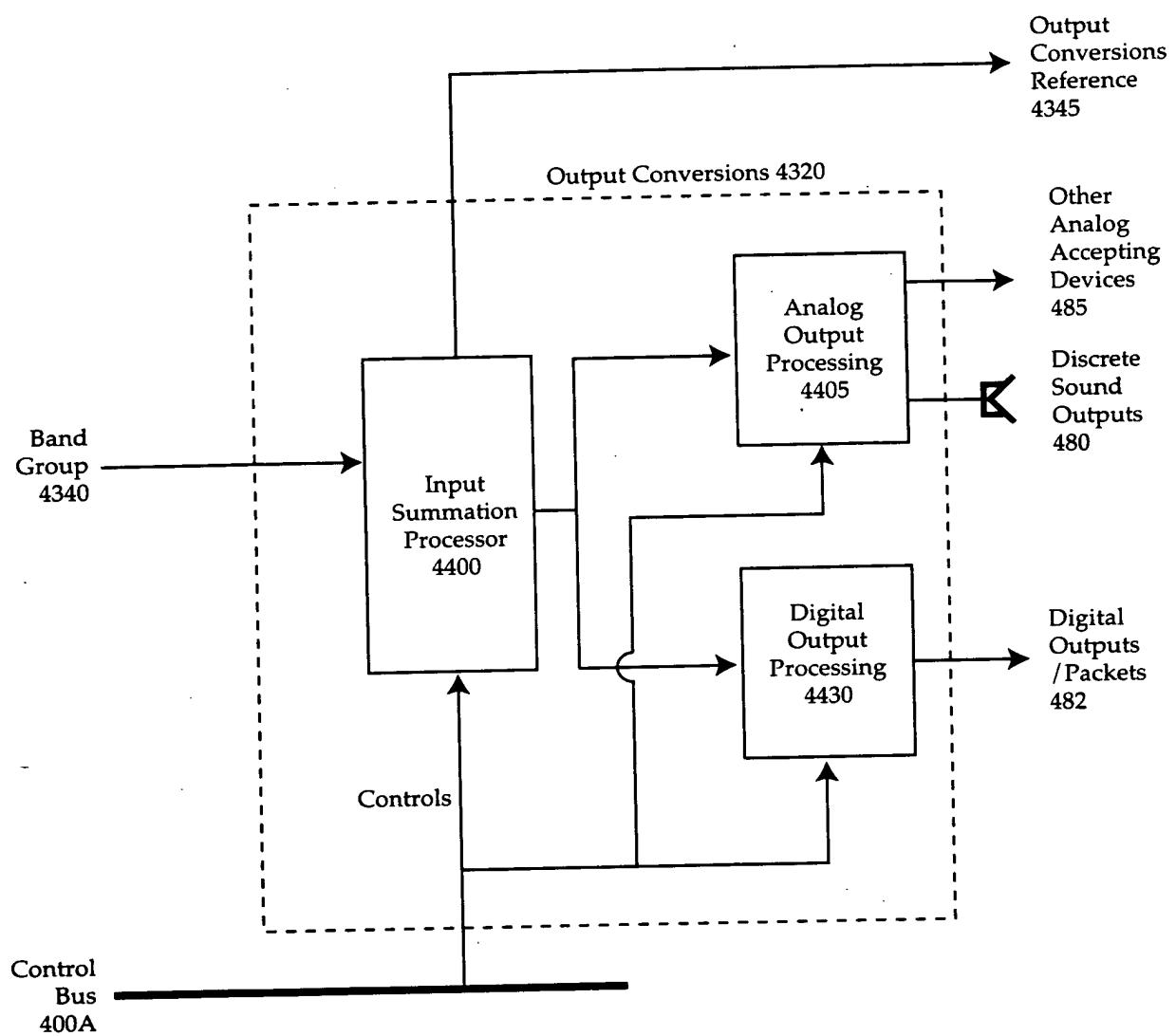


FIG. 44

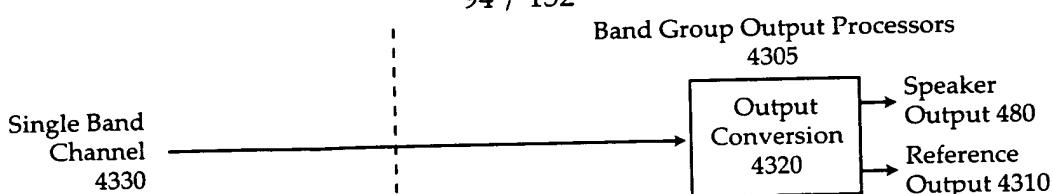


FIG. 45A

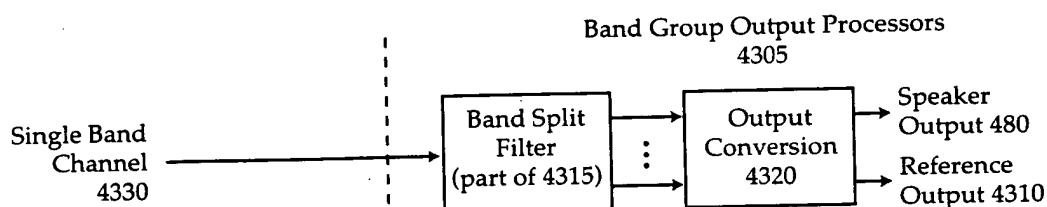


FIG. 45B

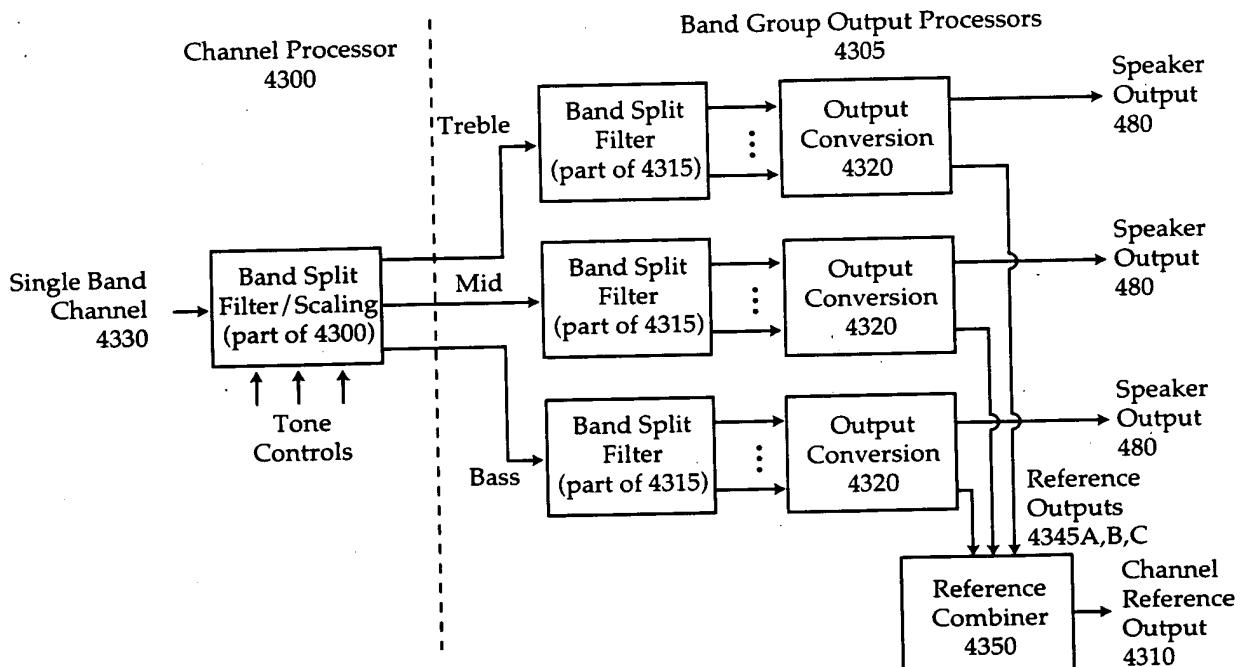


FIG. 45C

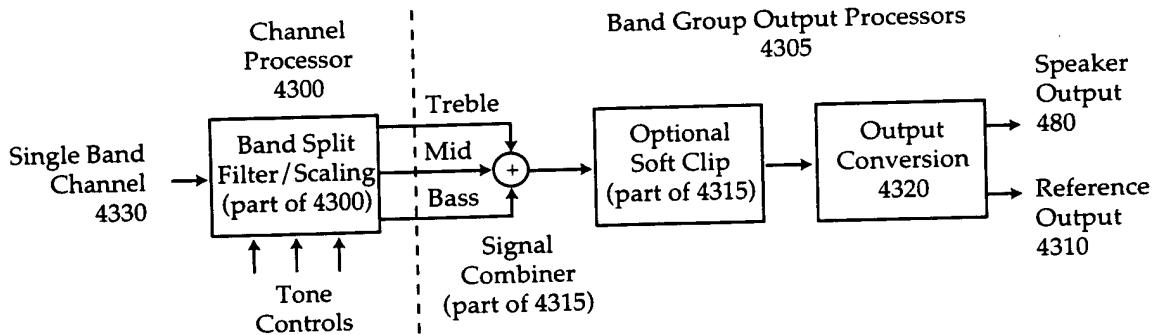


FIG. 45D

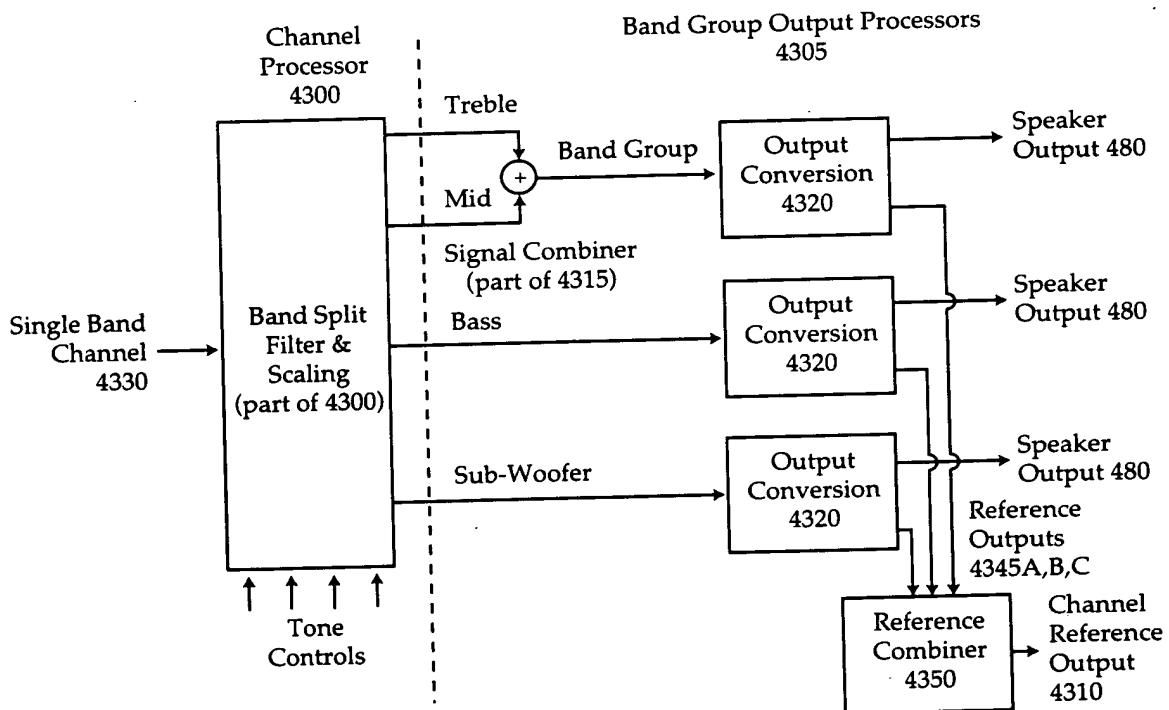


FIG. 45E

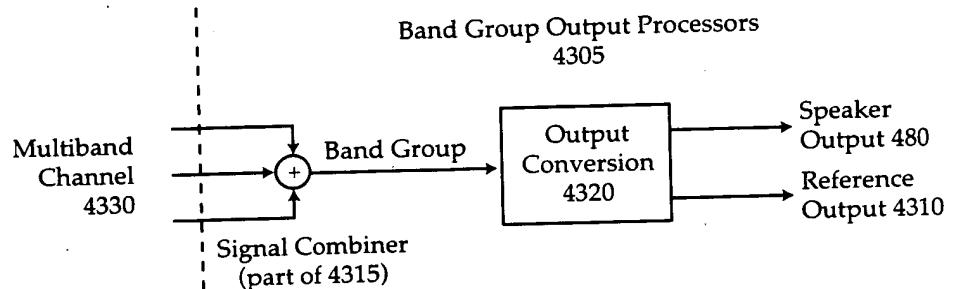


FIG. 45F

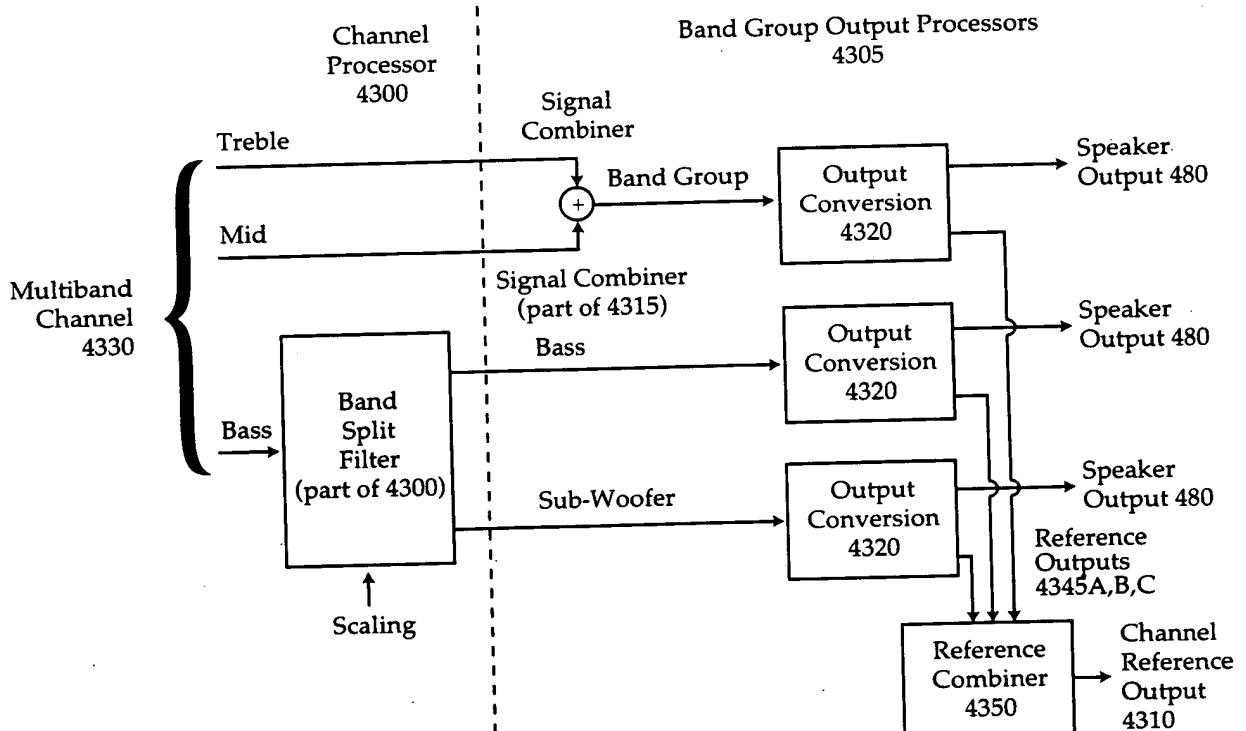


FIG. 45G

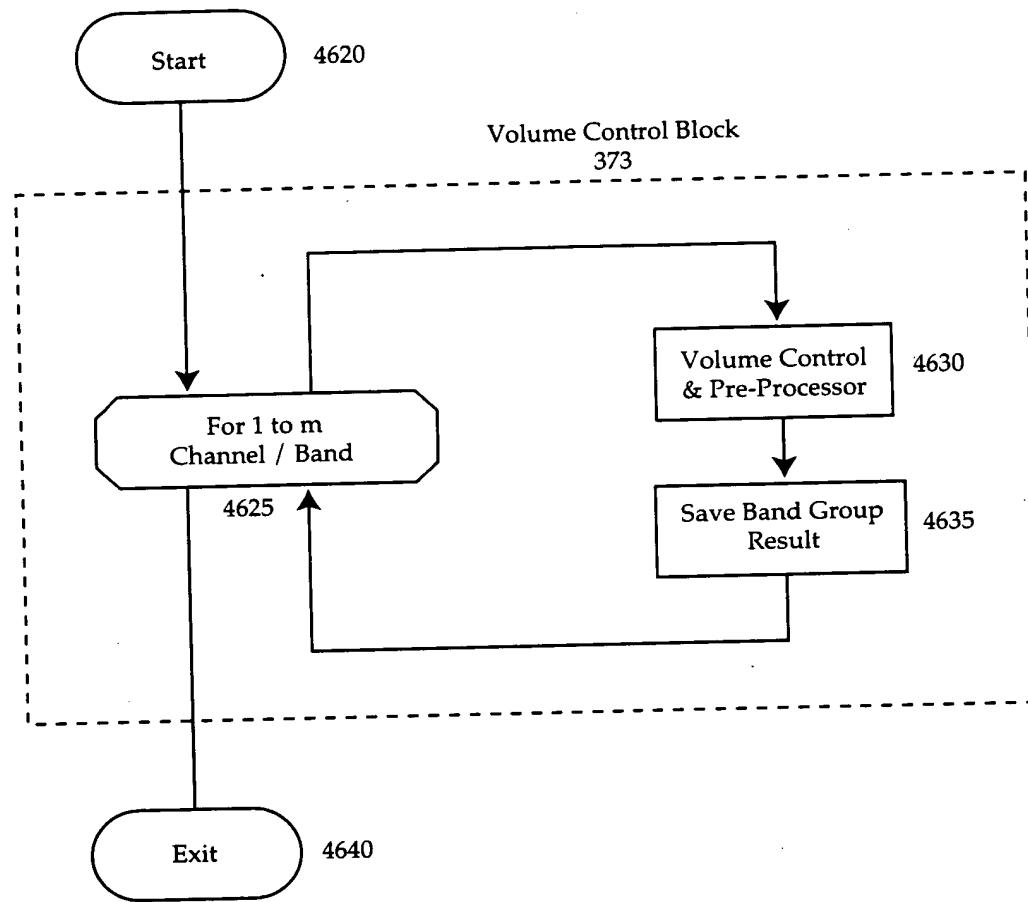


FIG. 46

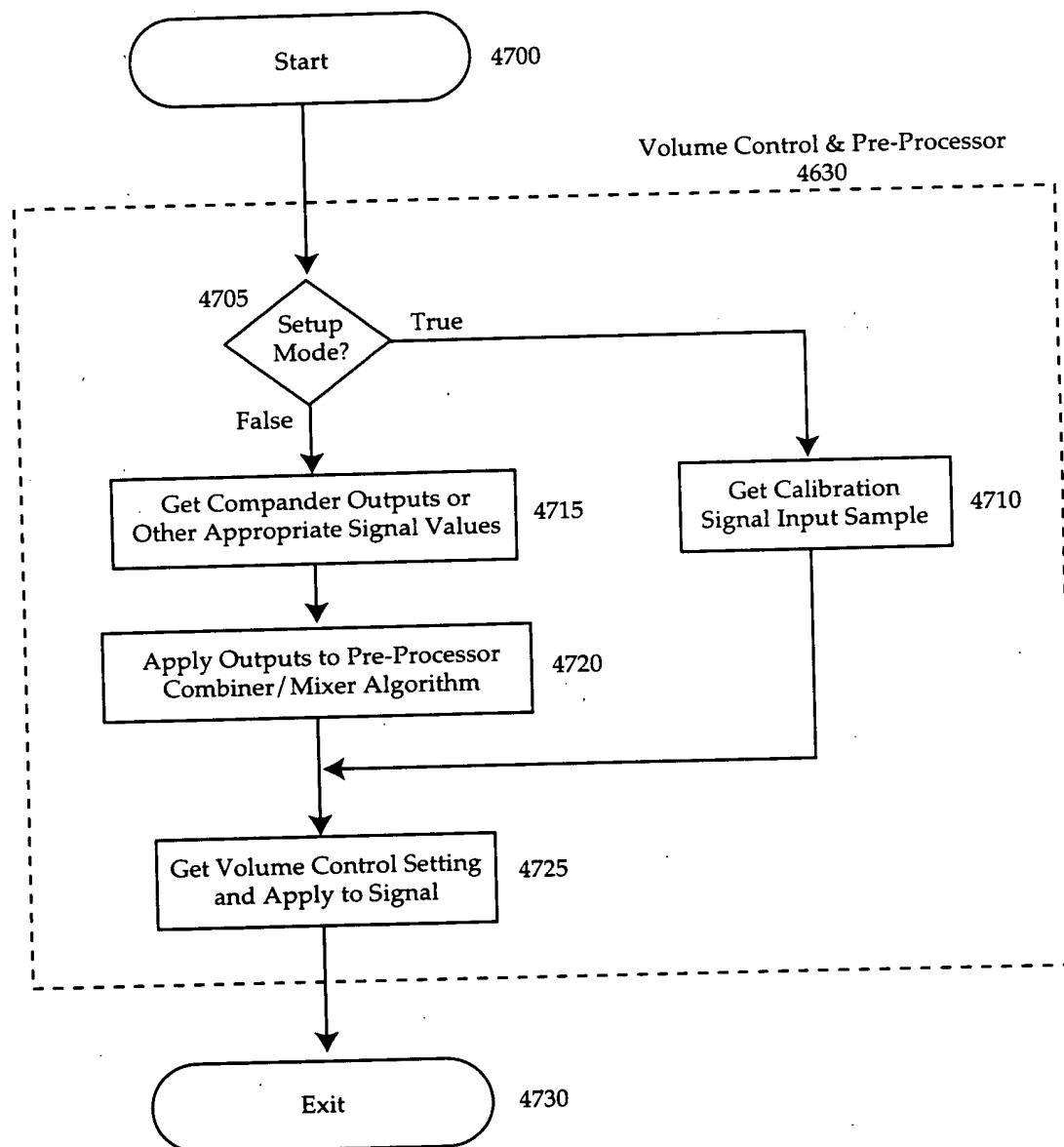
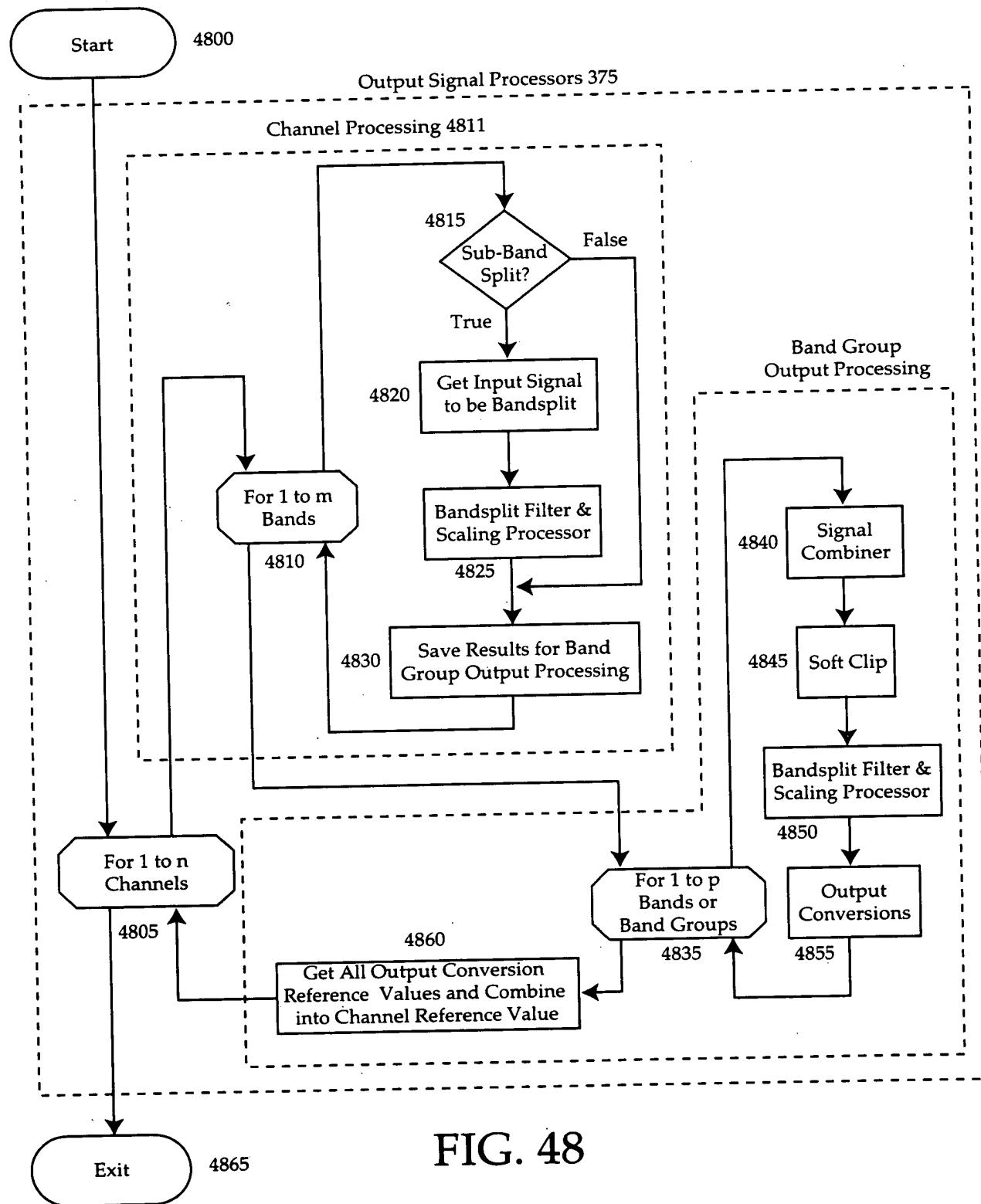


FIG. 47



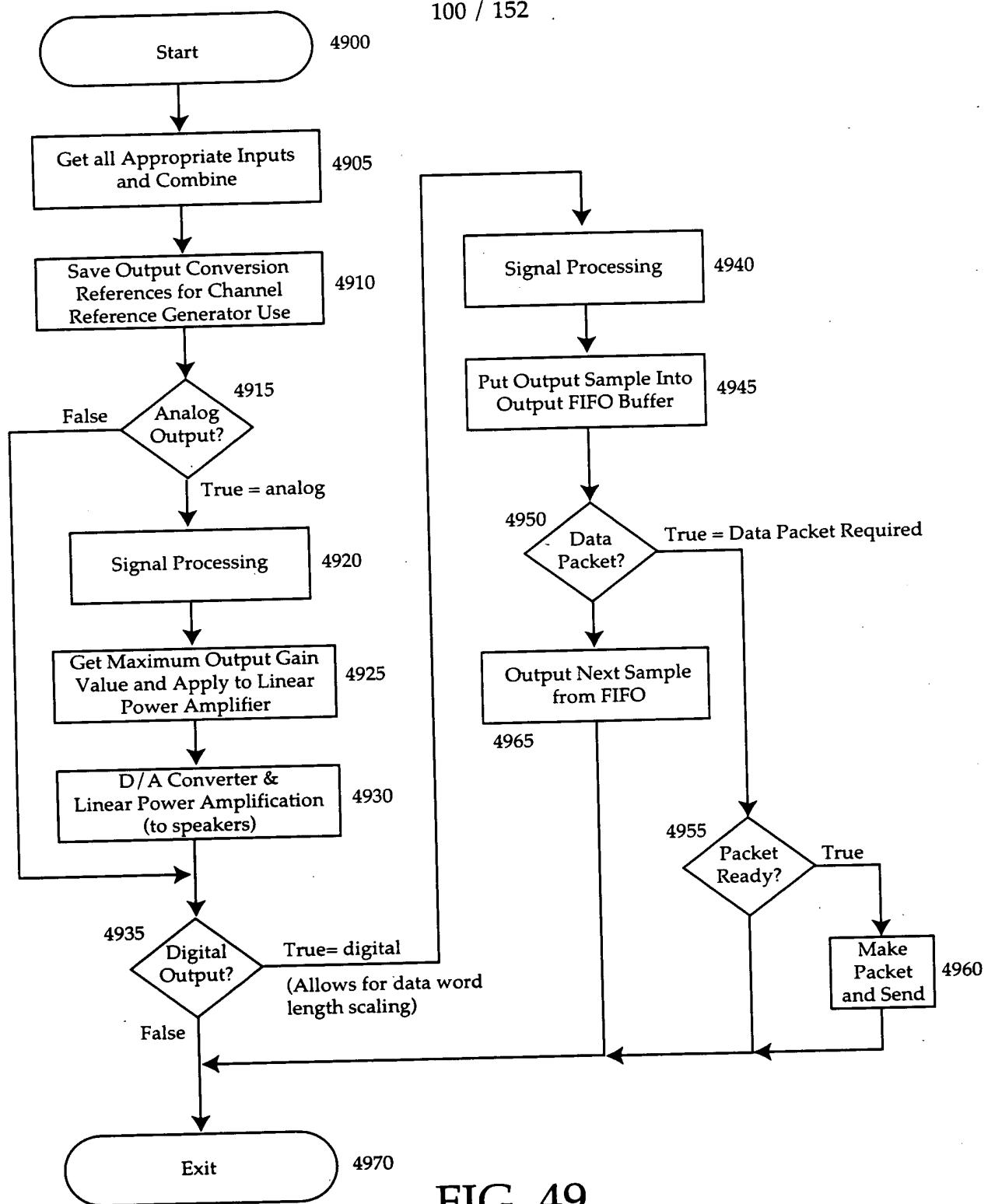


FIG. 49

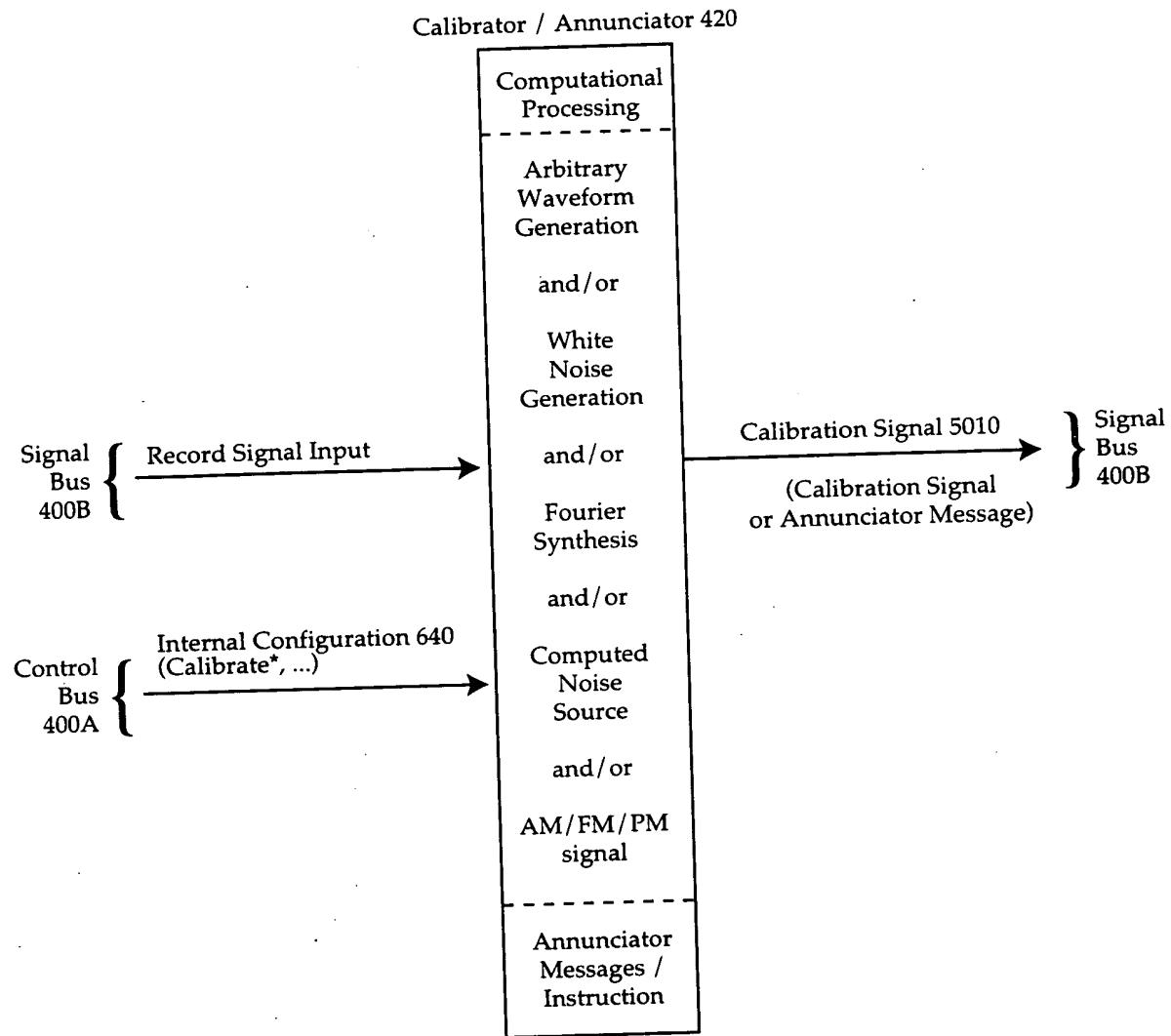


FIG. 50

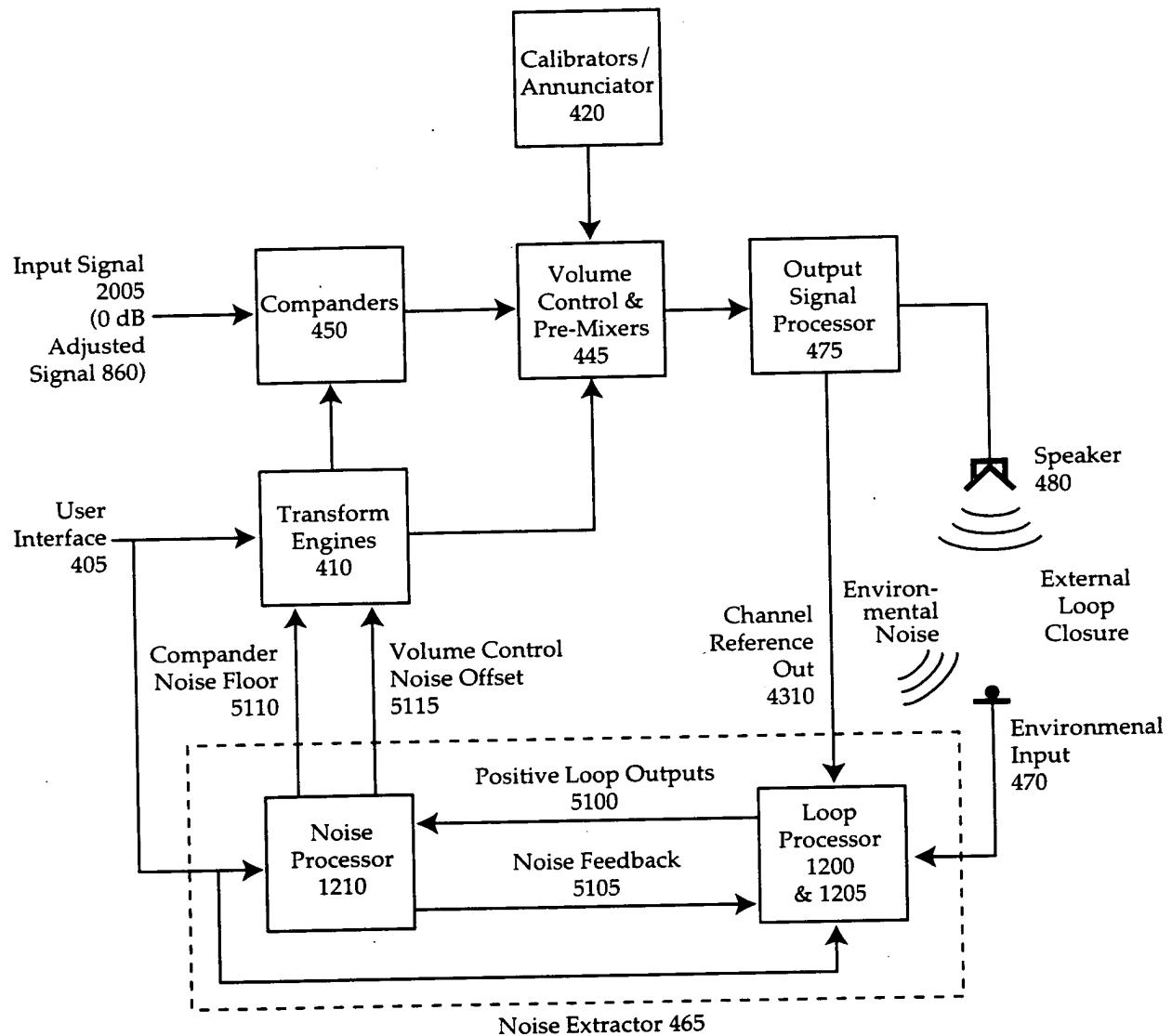


FIG. 51

Environment Power Estimator Bus 5220

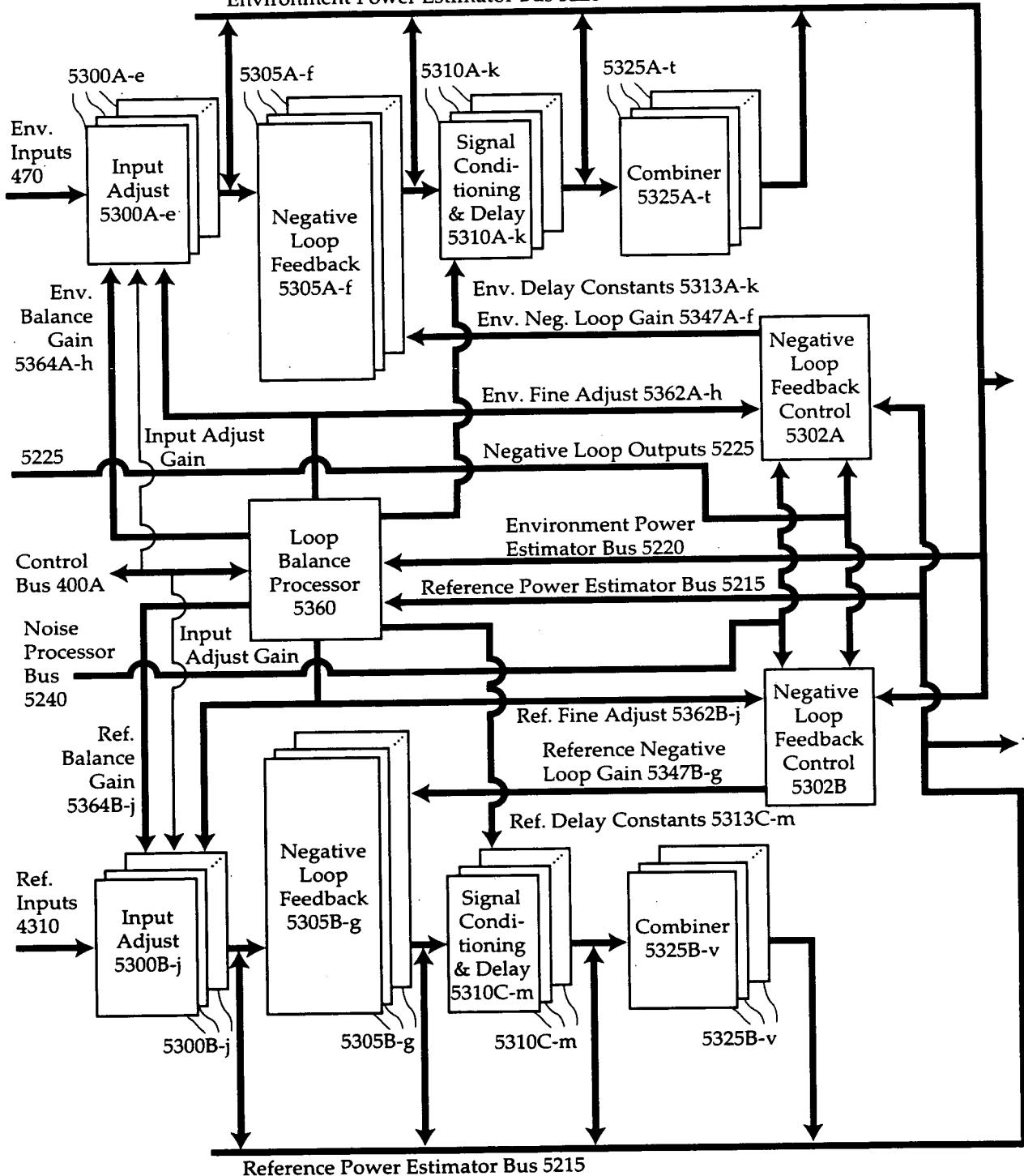


FIG. 52A

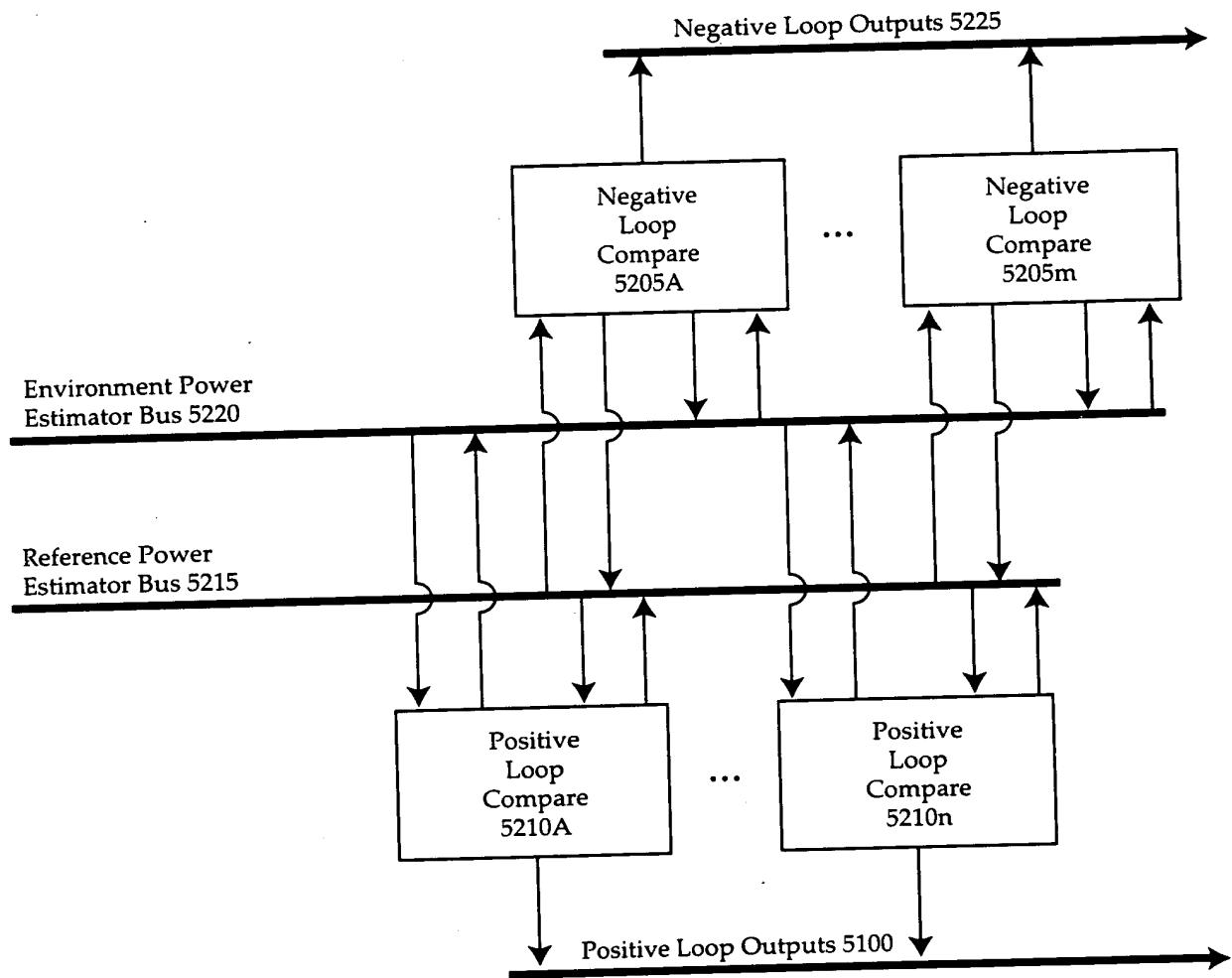


FIG. 52B

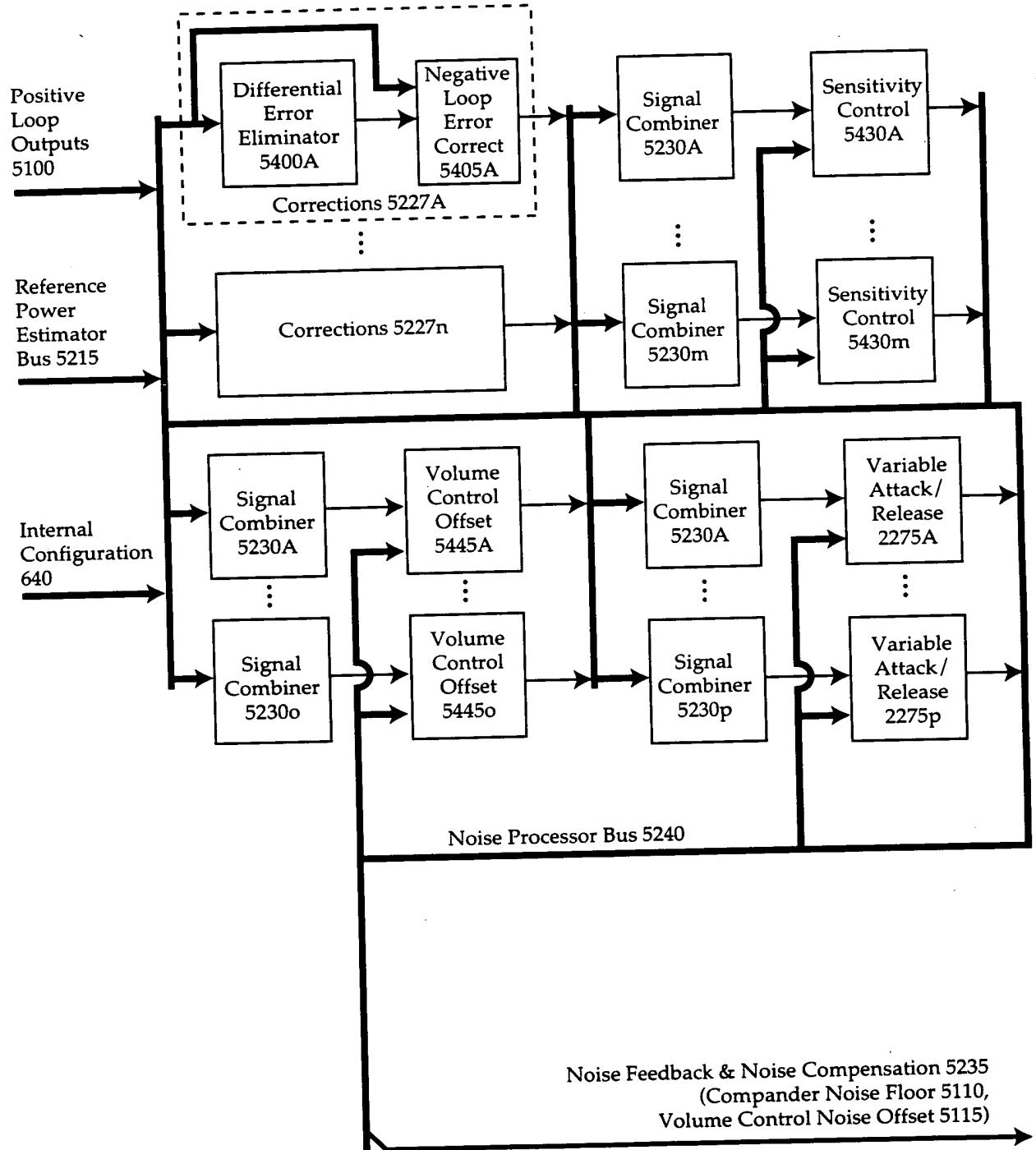


FIG. 52C

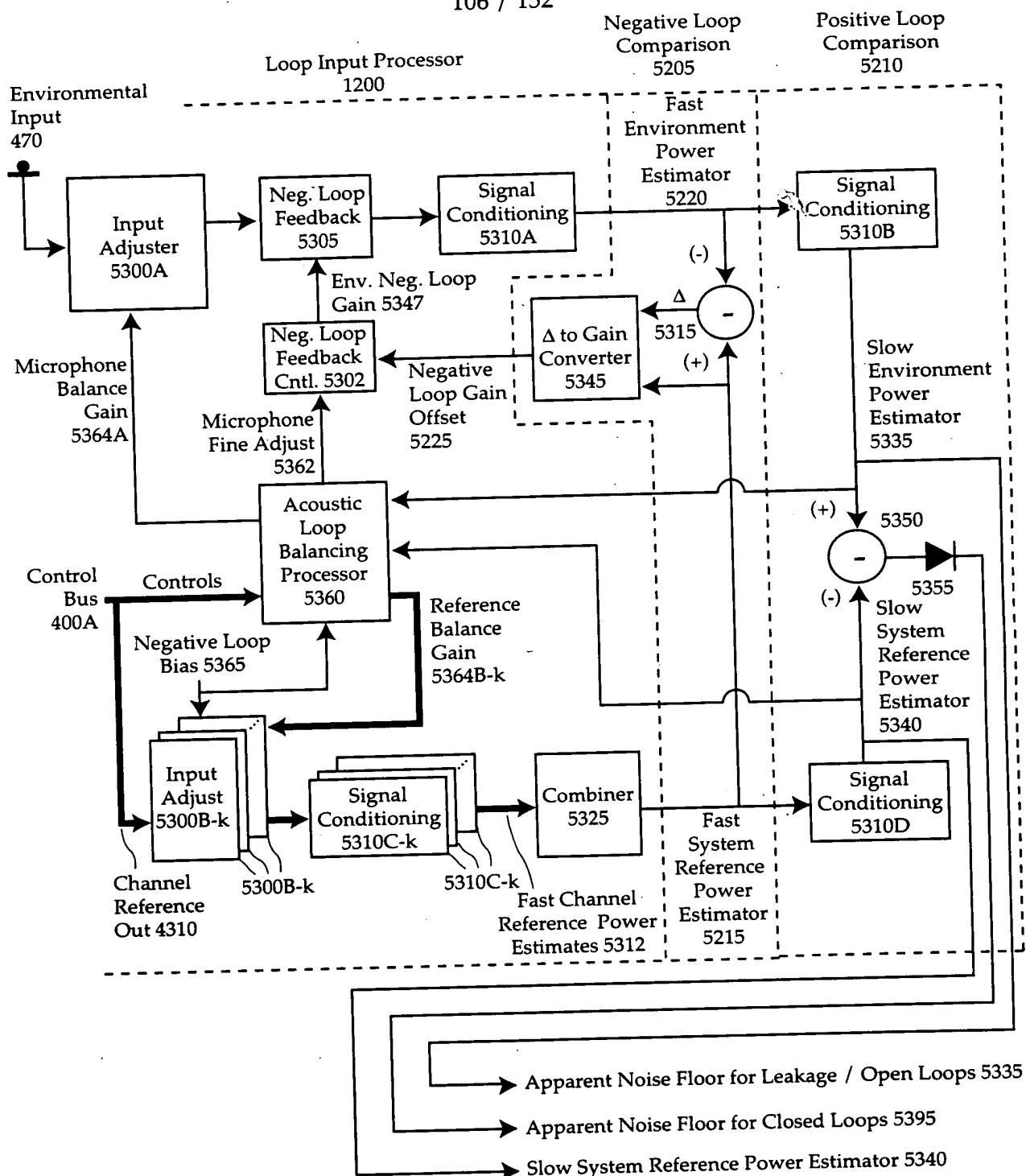


FIG. 53A

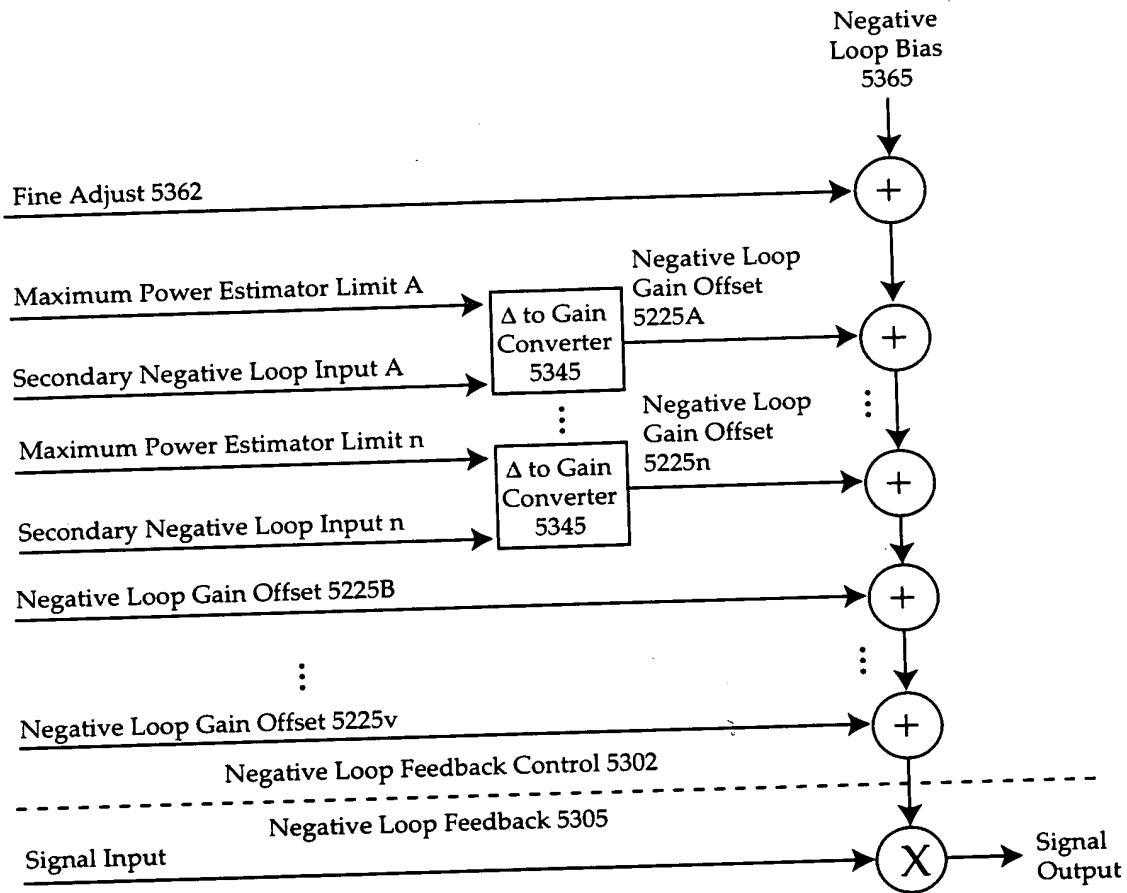


FIG. 53B

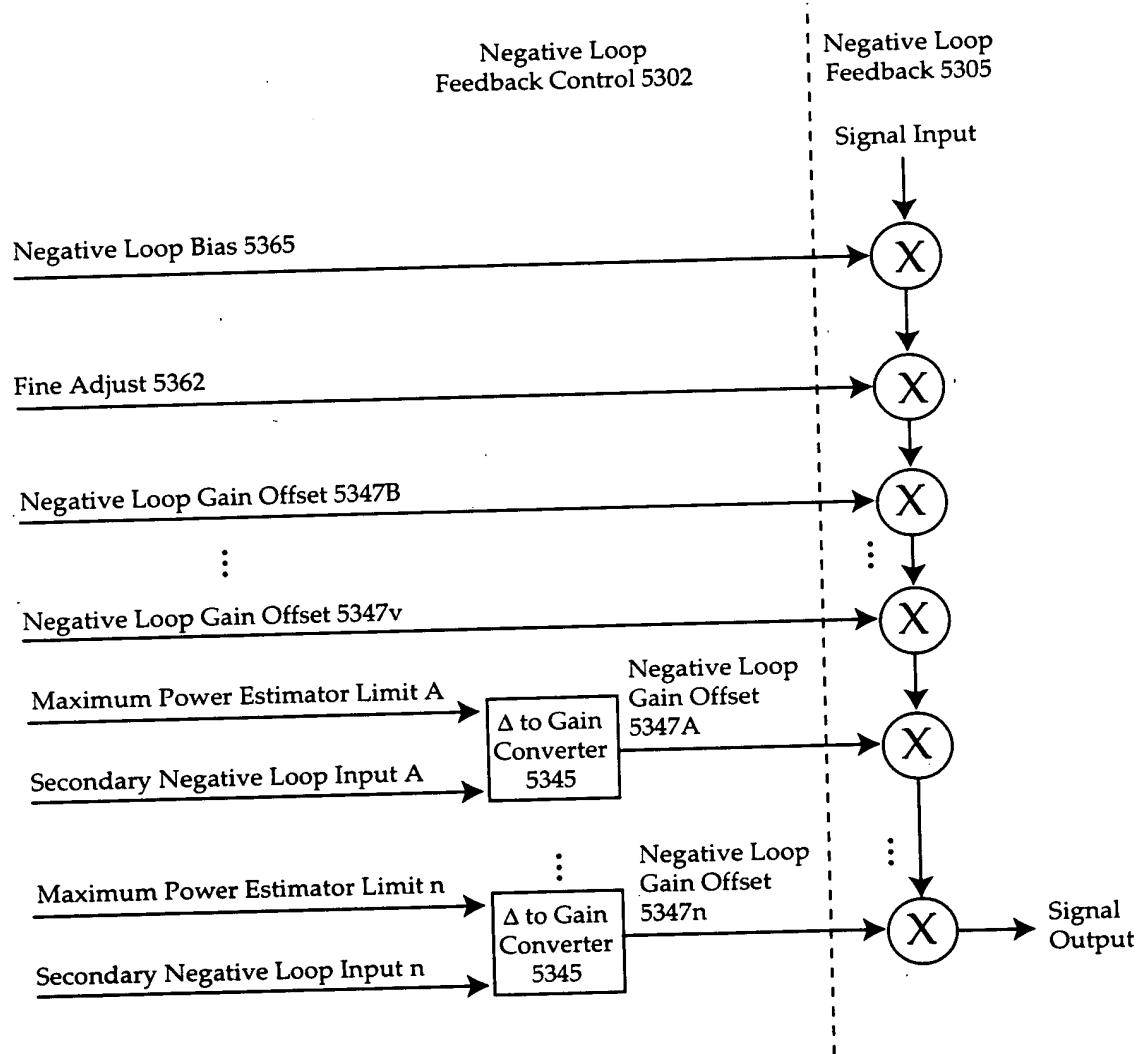


FIG. 53C

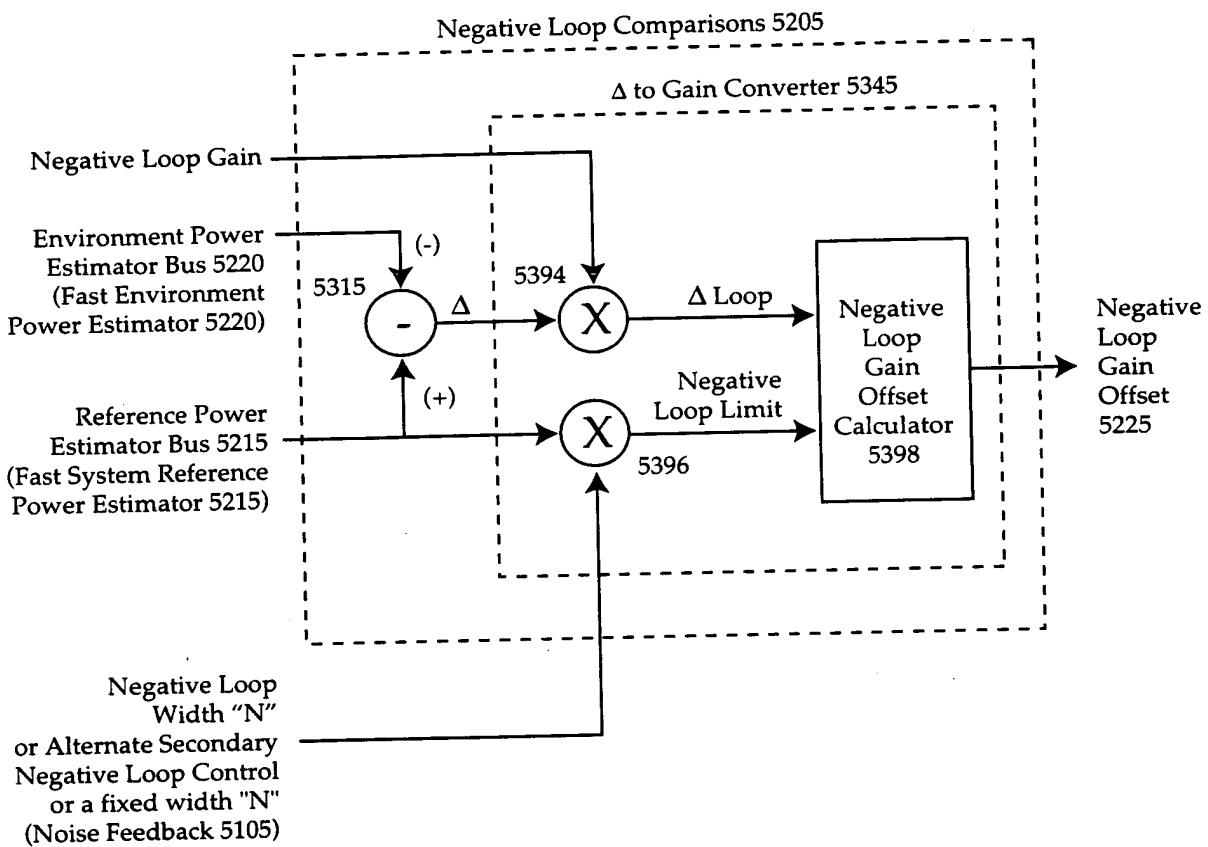


FIG. 53D

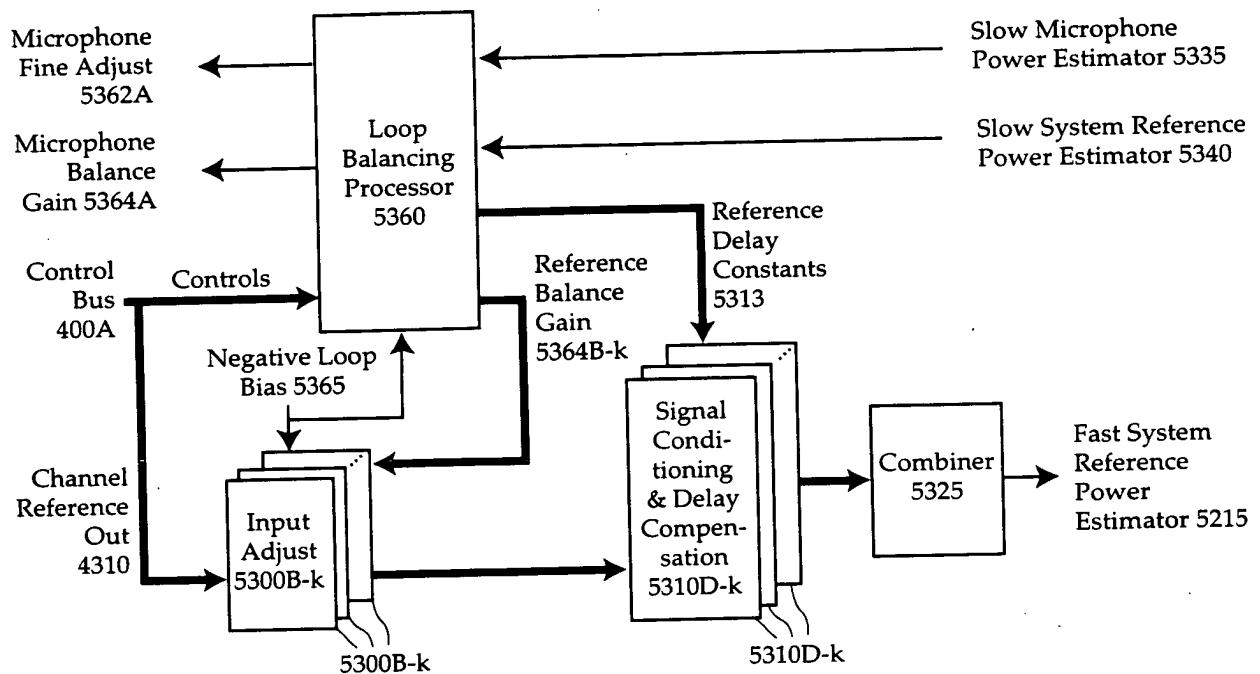


FIG. 53E

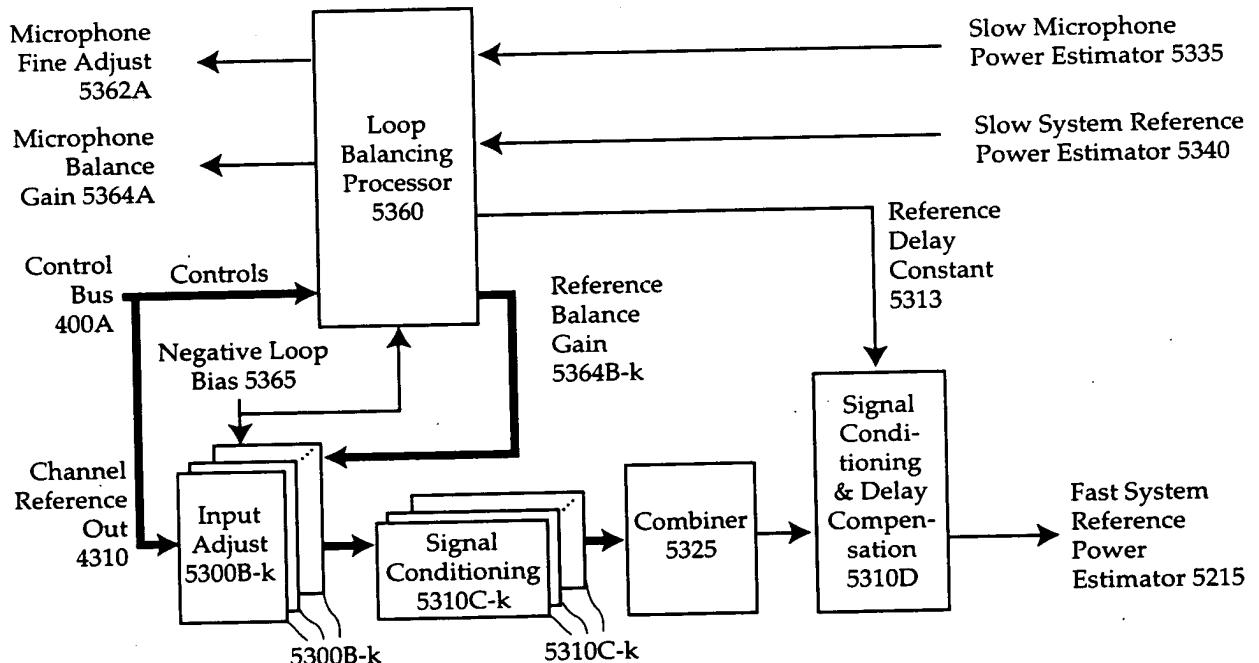


FIG. 53F

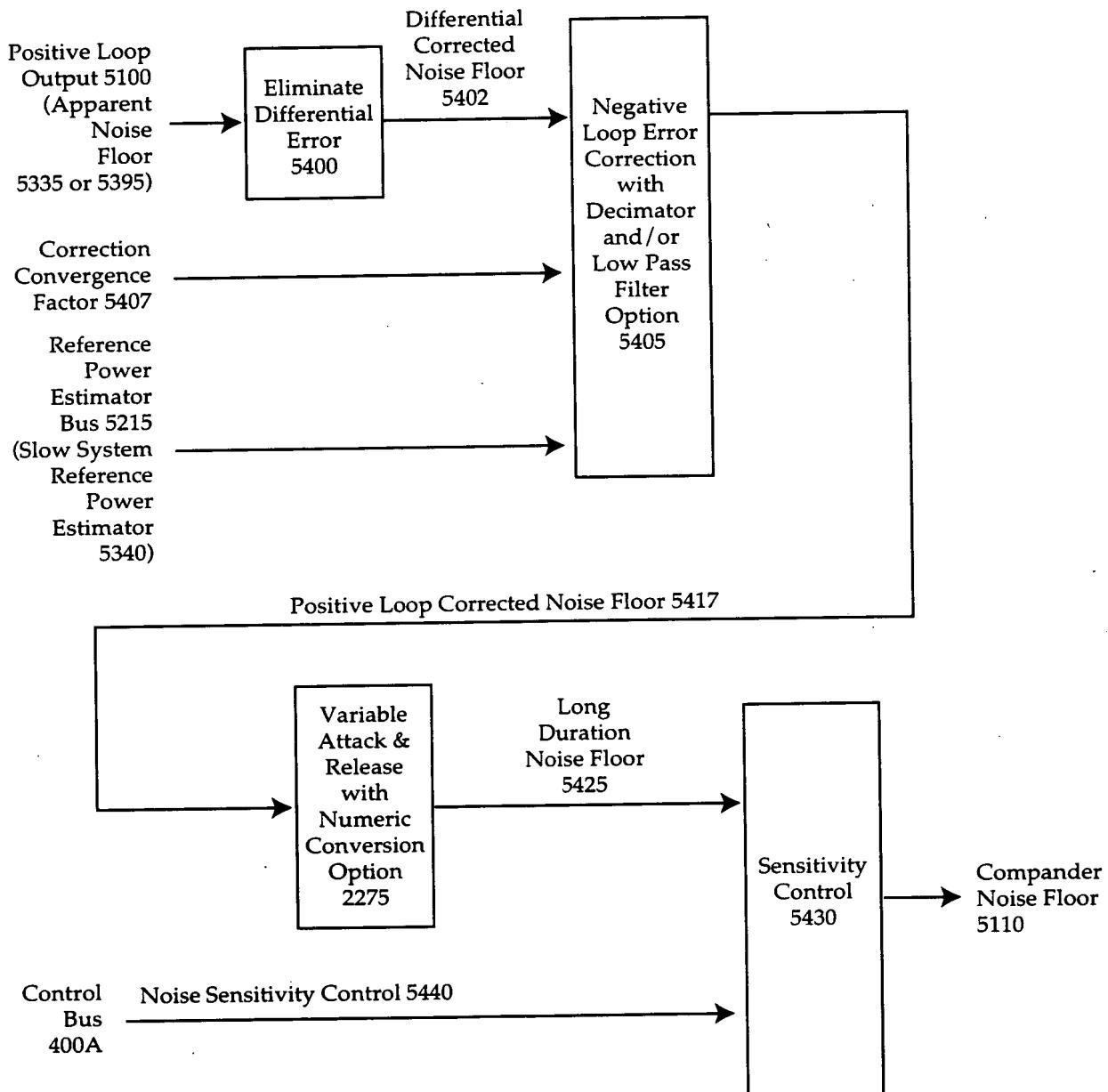


FIG. 54A

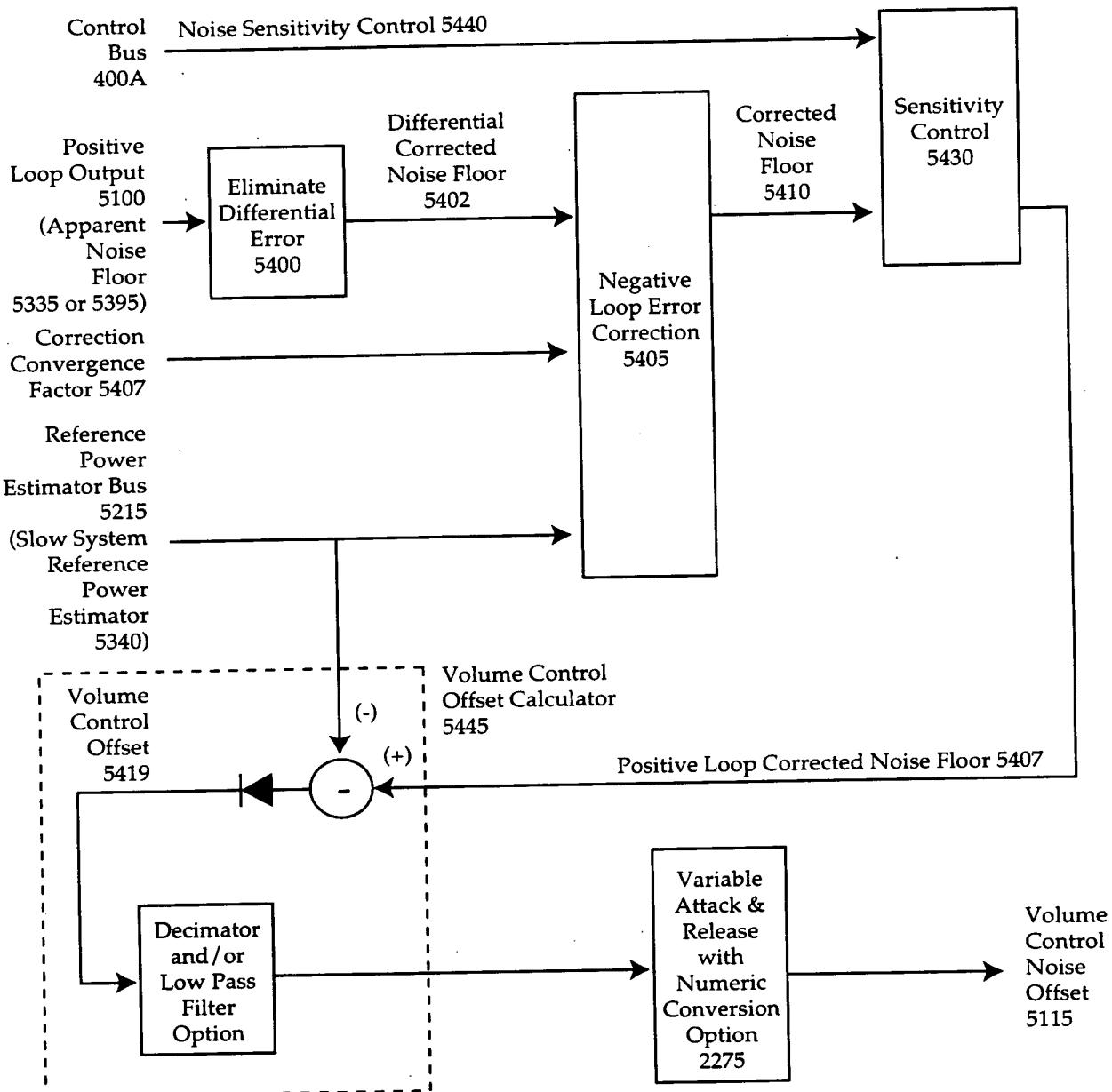


FIG. 54B

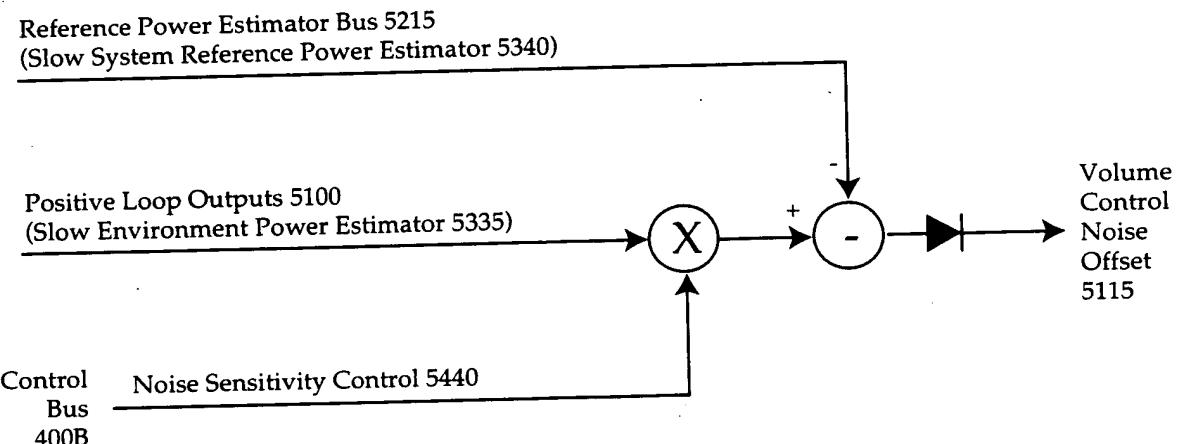


FIG. 54C

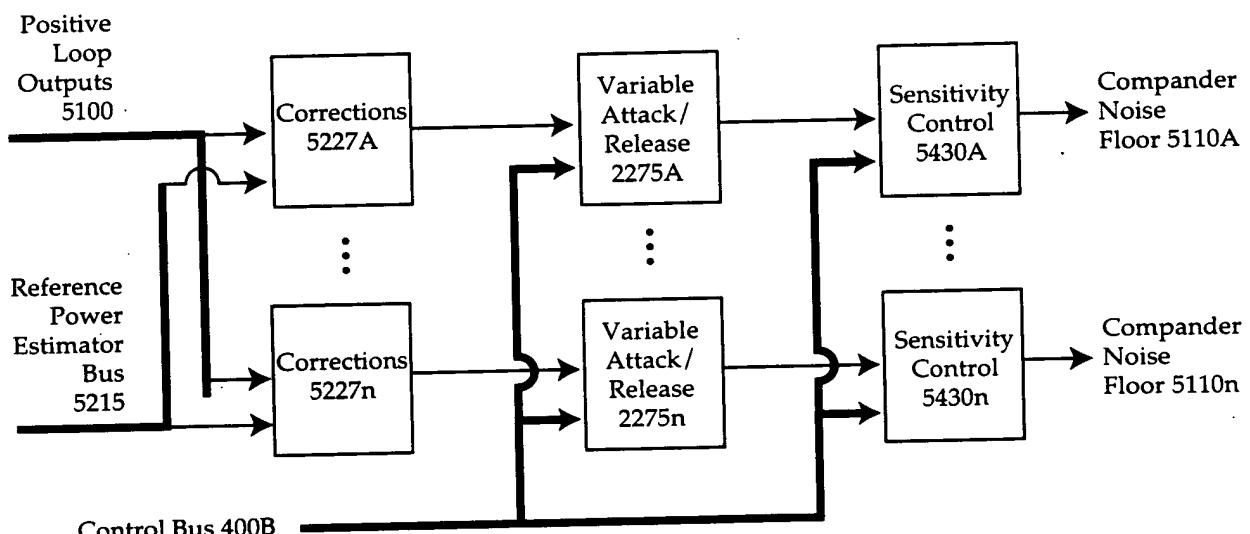


FIG. 54D

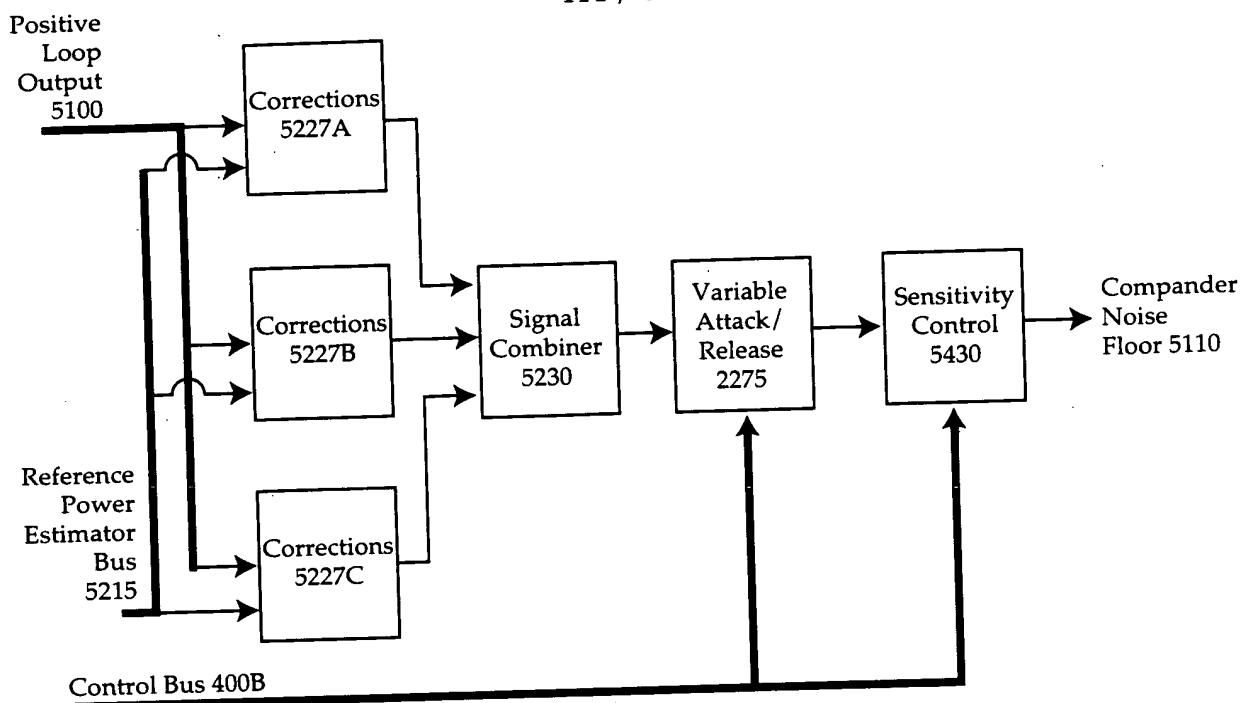


FIG. 54E

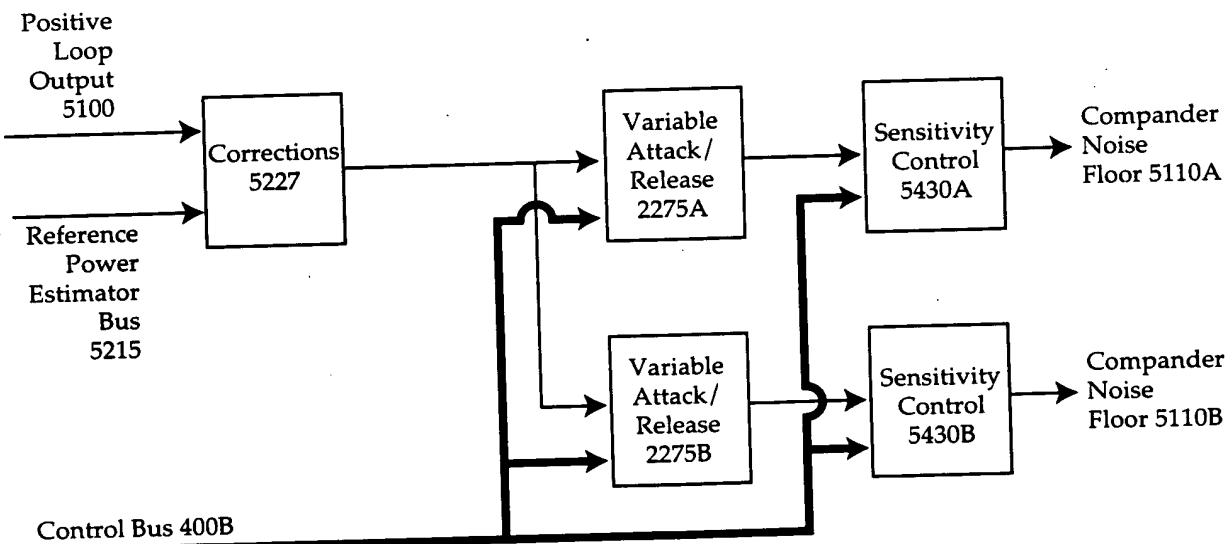


FIG. 54F

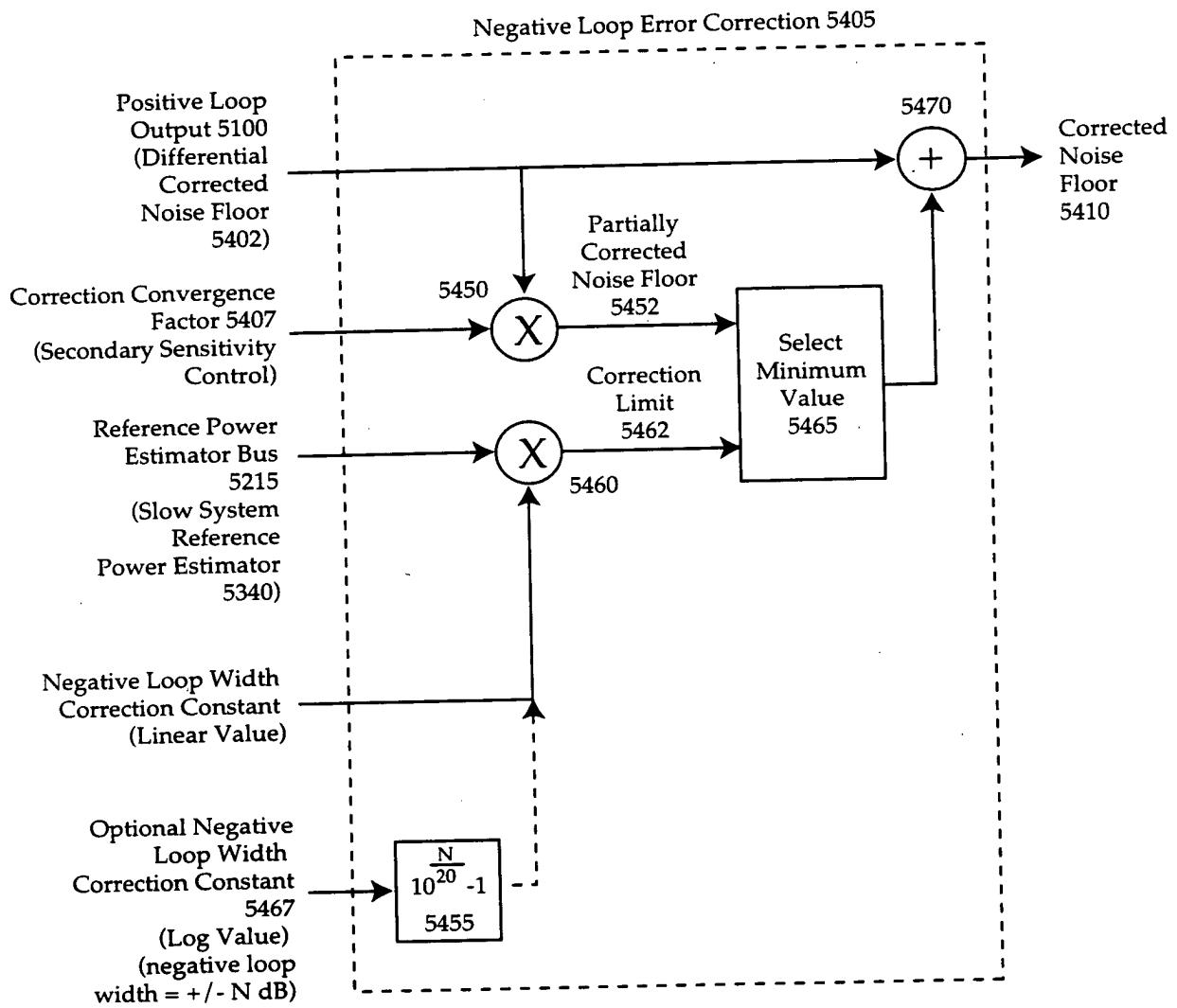


FIG. 54G

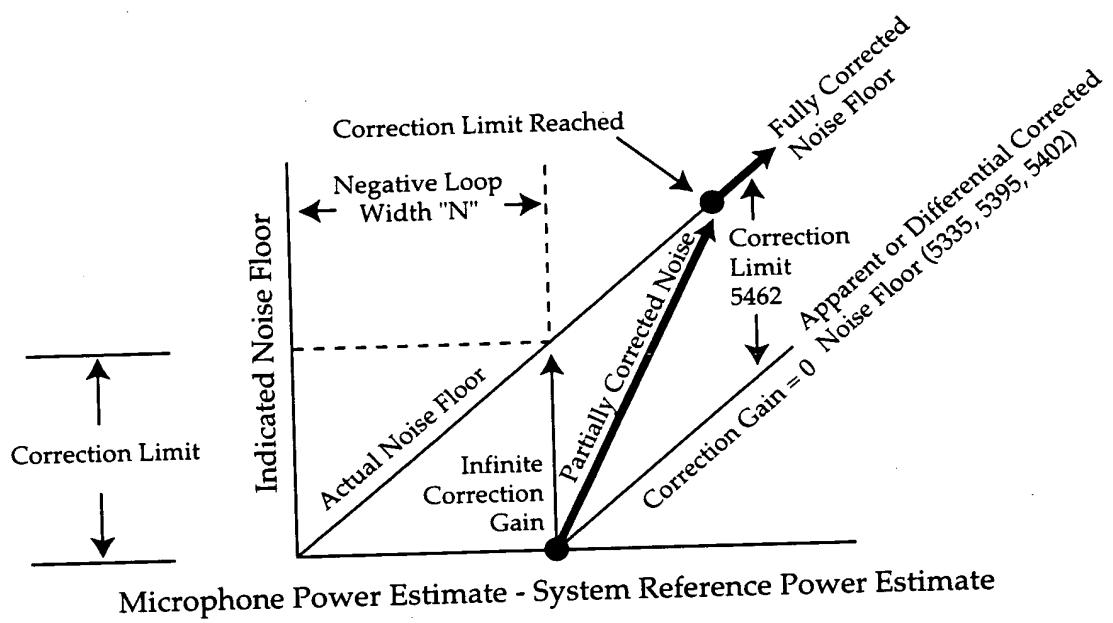


FIG. 54H

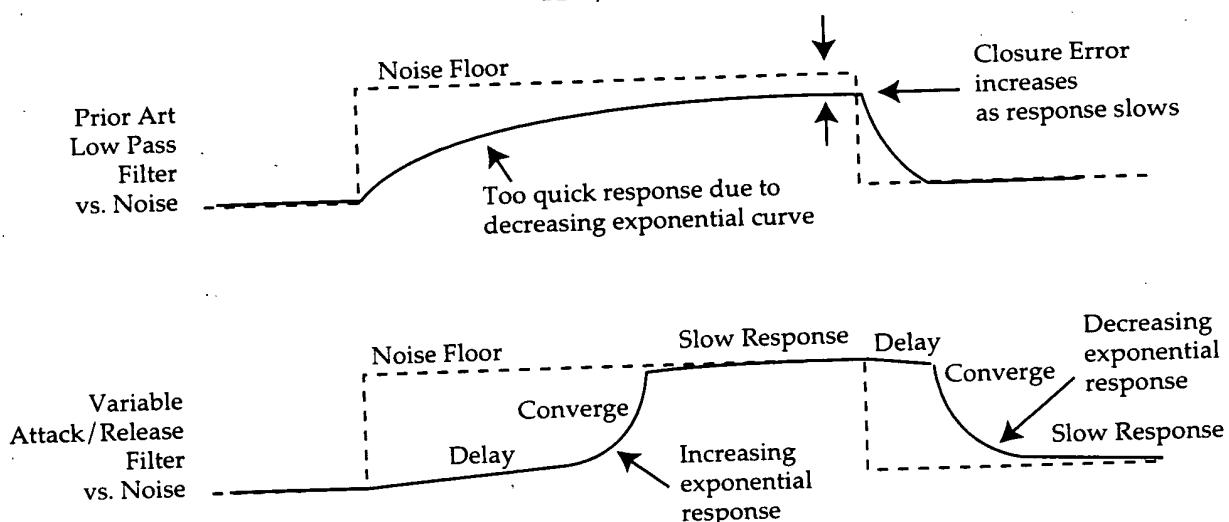


FIG. 55A

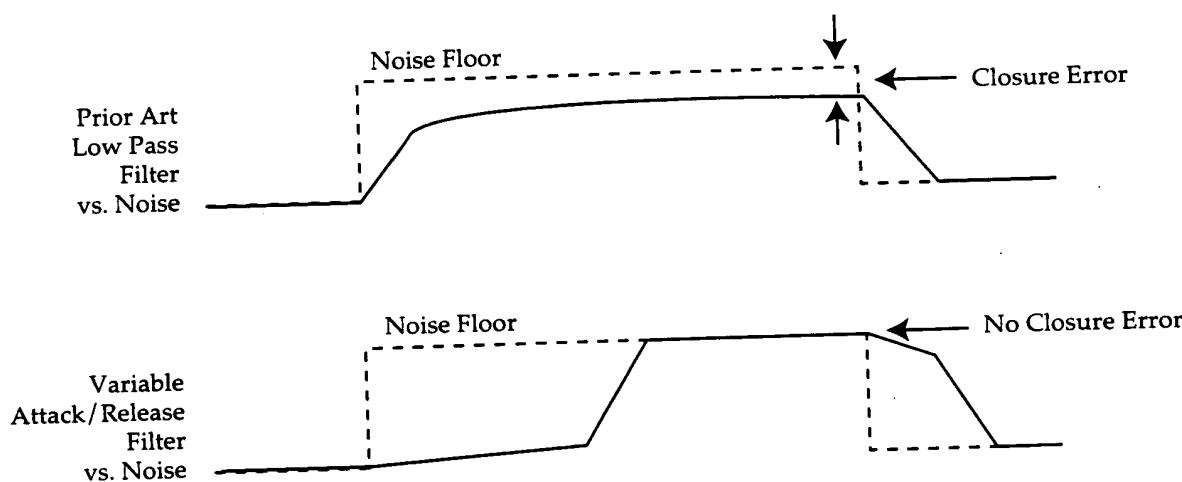


FIG. 55B

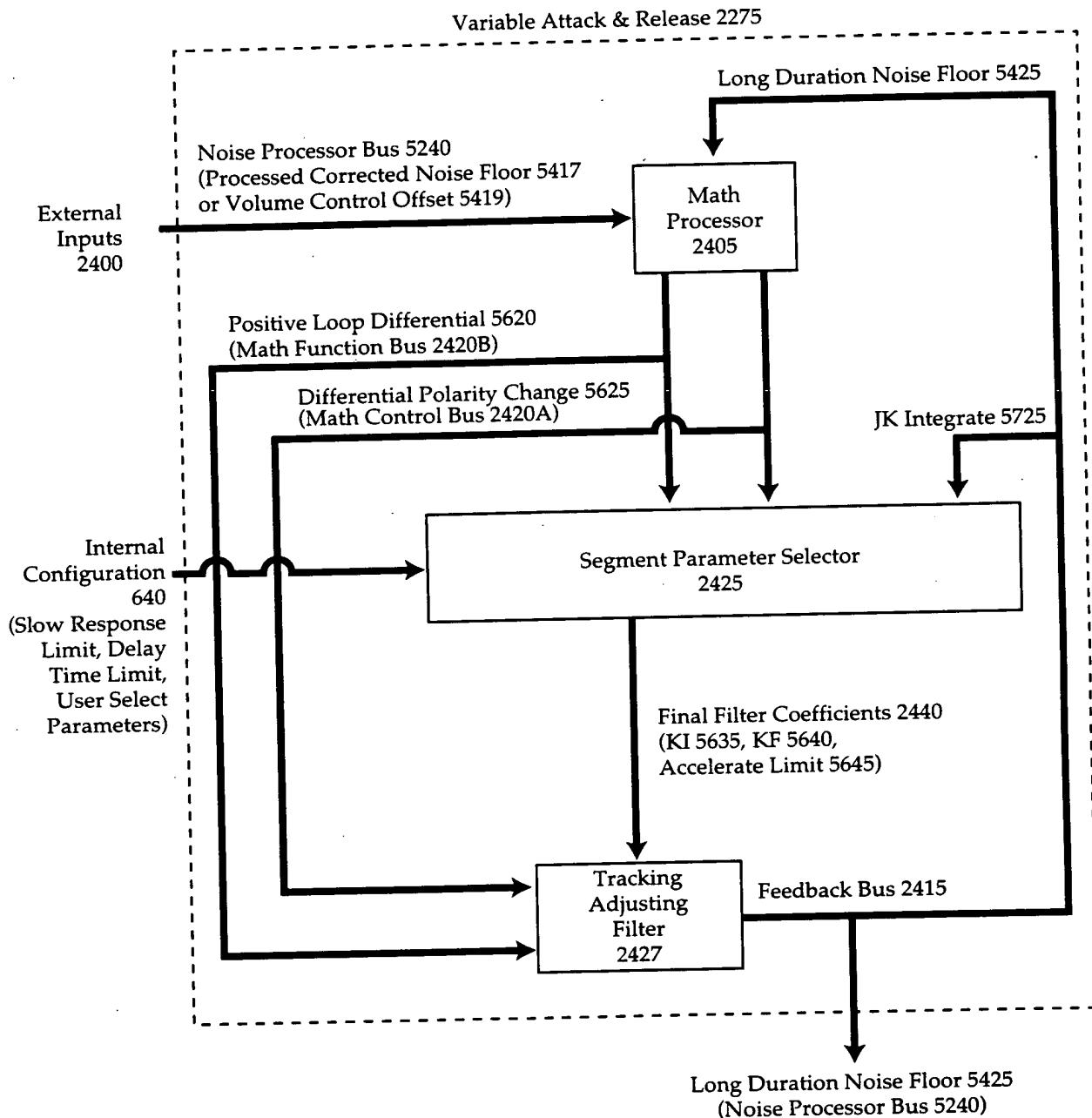


FIG. 55C

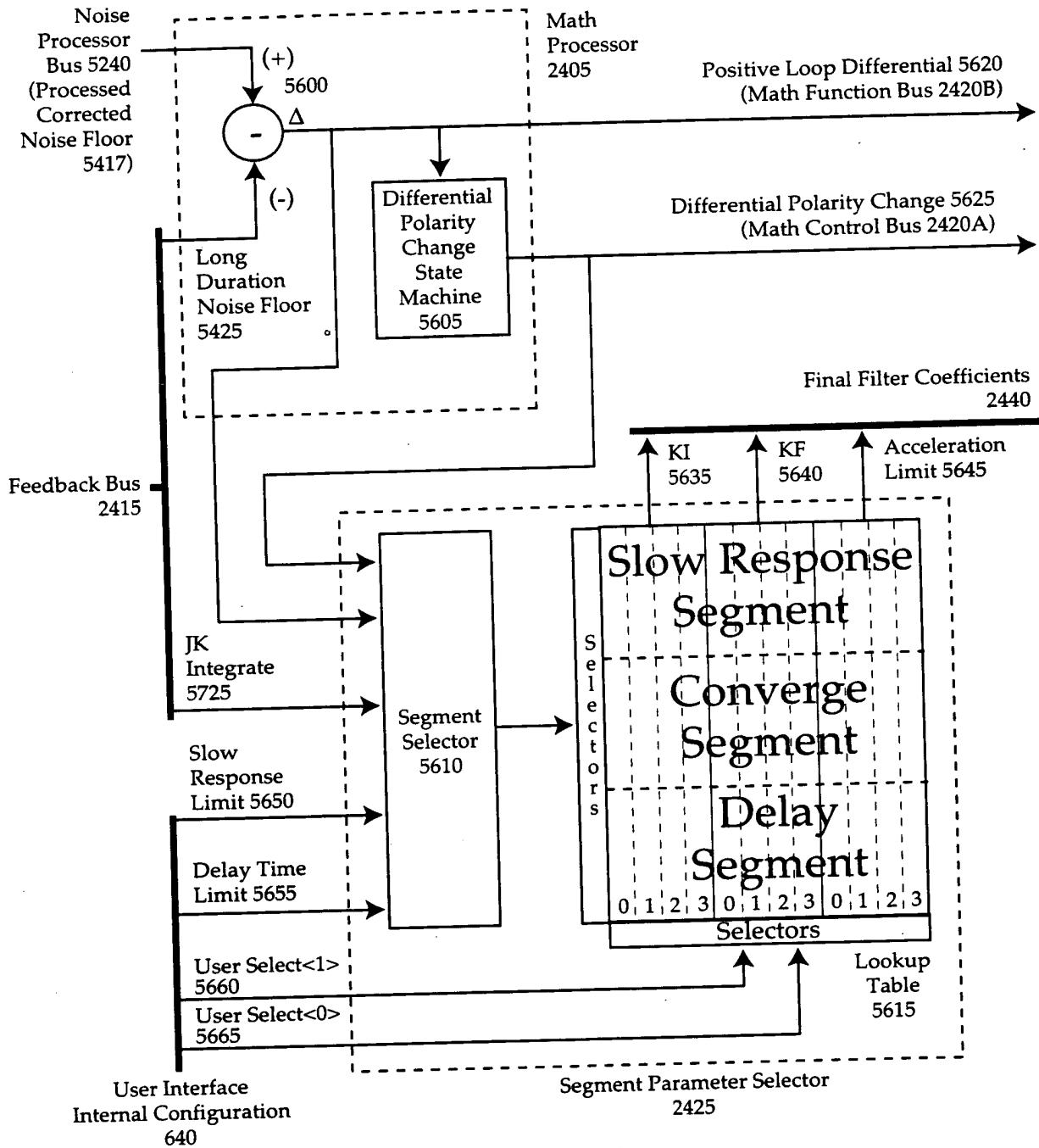


FIG. 56

Differential Polarity Change 5625
(Math Control Bus 2420A)

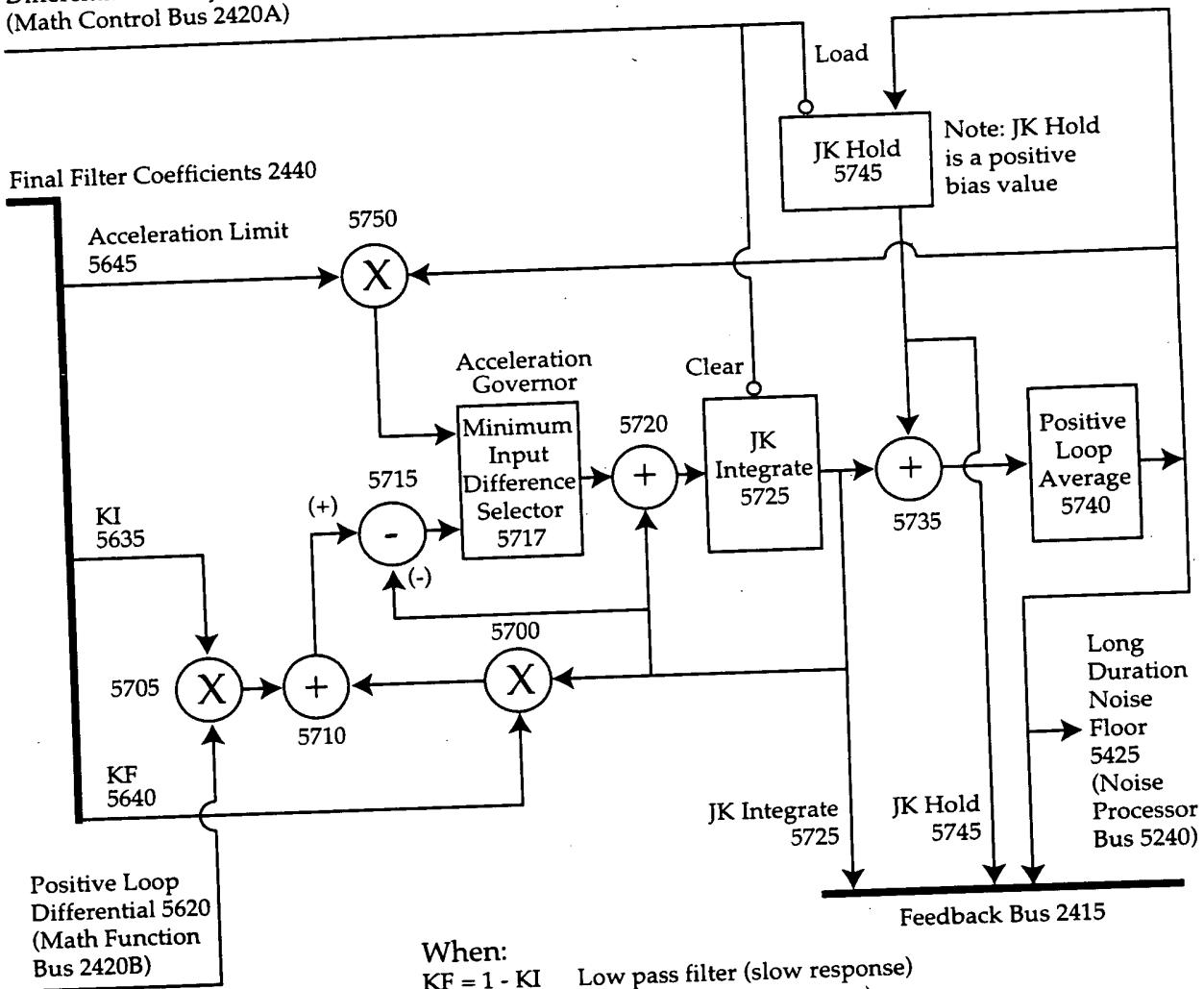


FIG. 57A

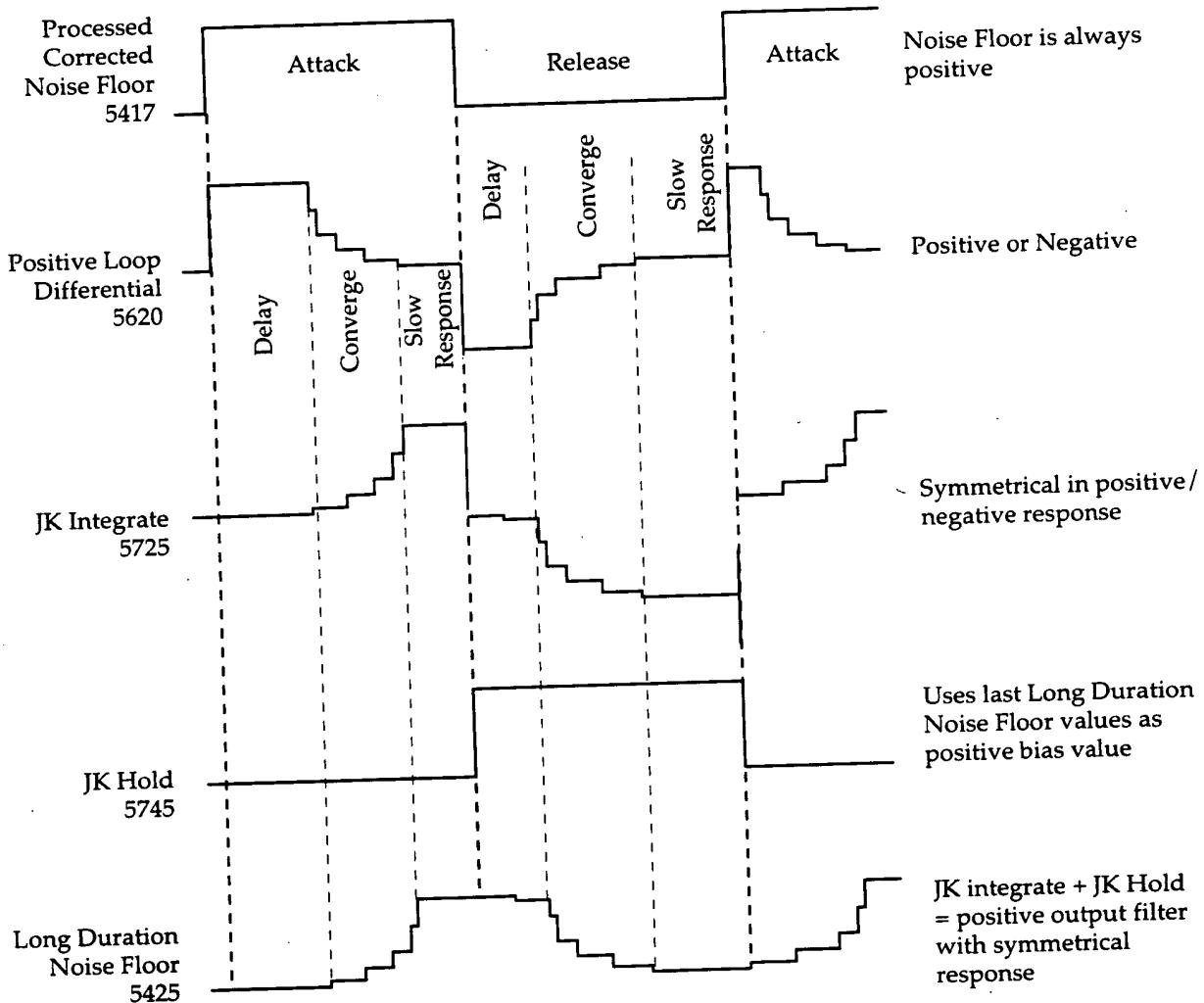


FIG. 57B

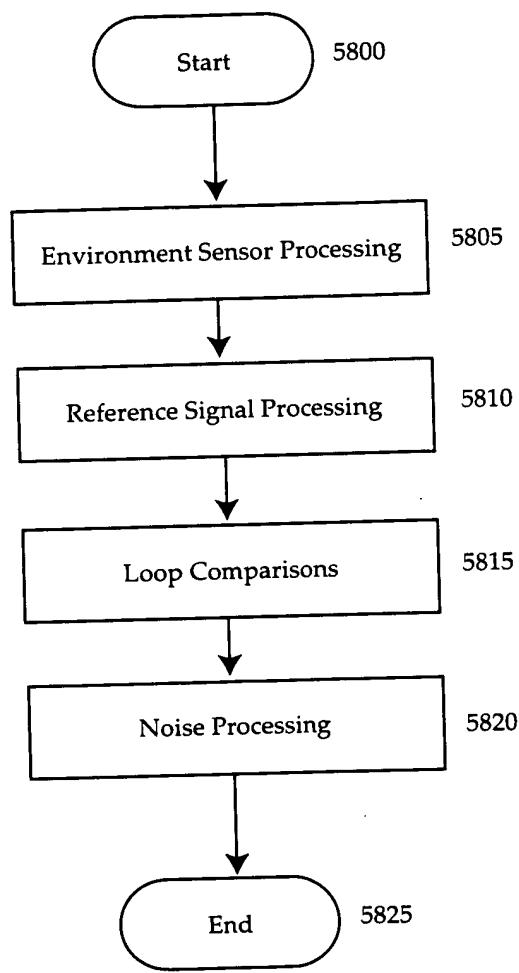


FIG. 58

00000000000000000000000000000000

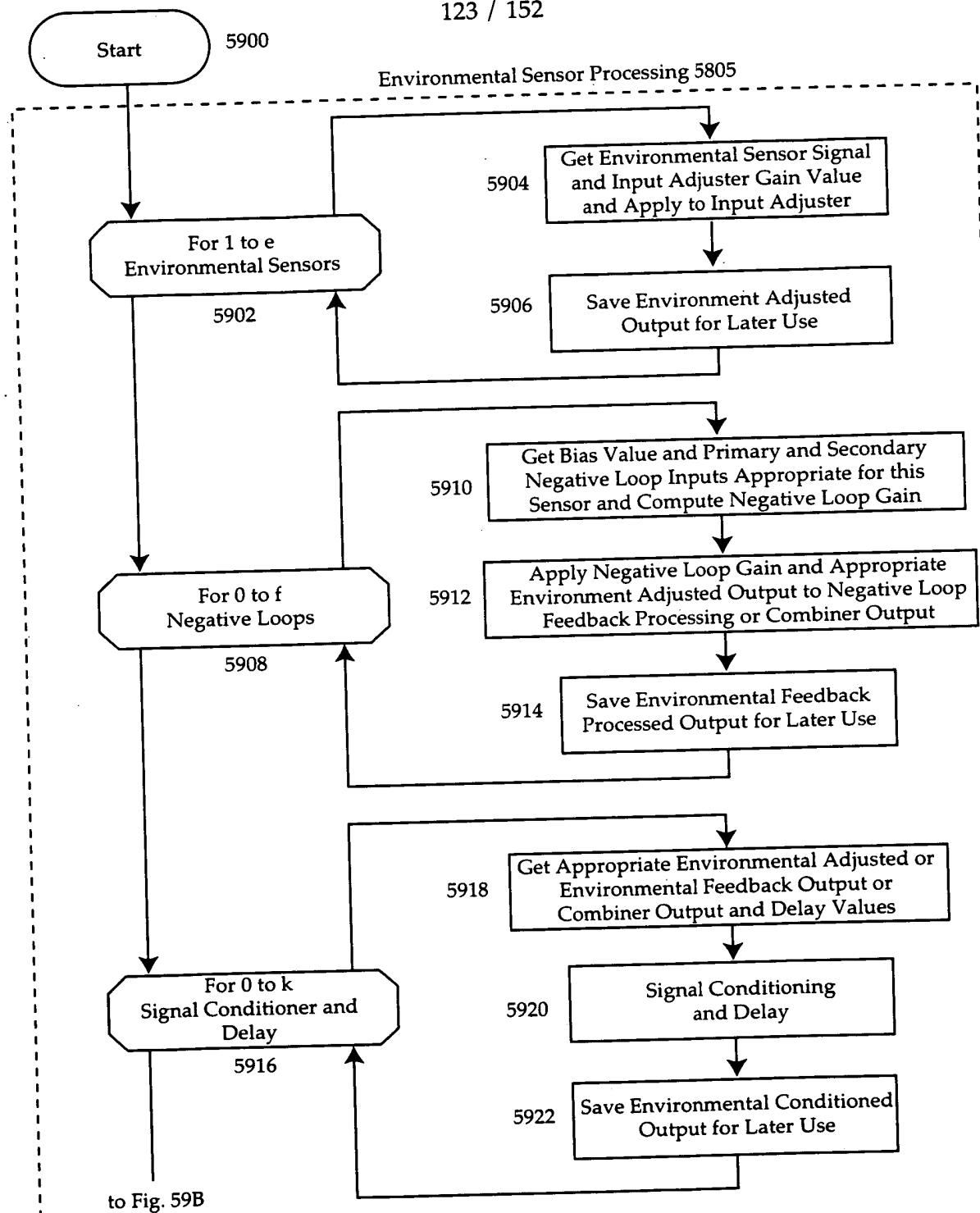


FIG. 59A

From
Fig. 59A

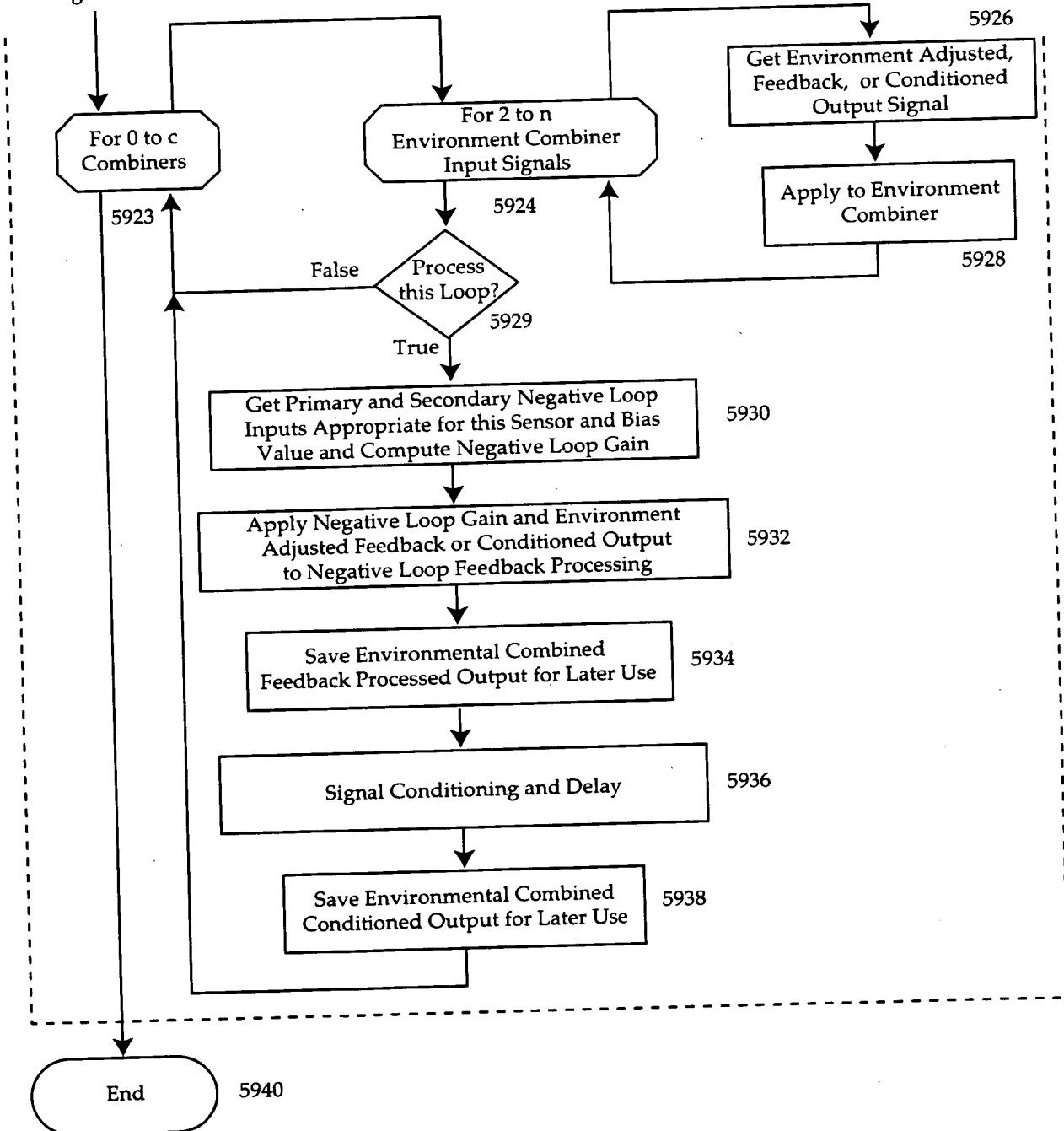
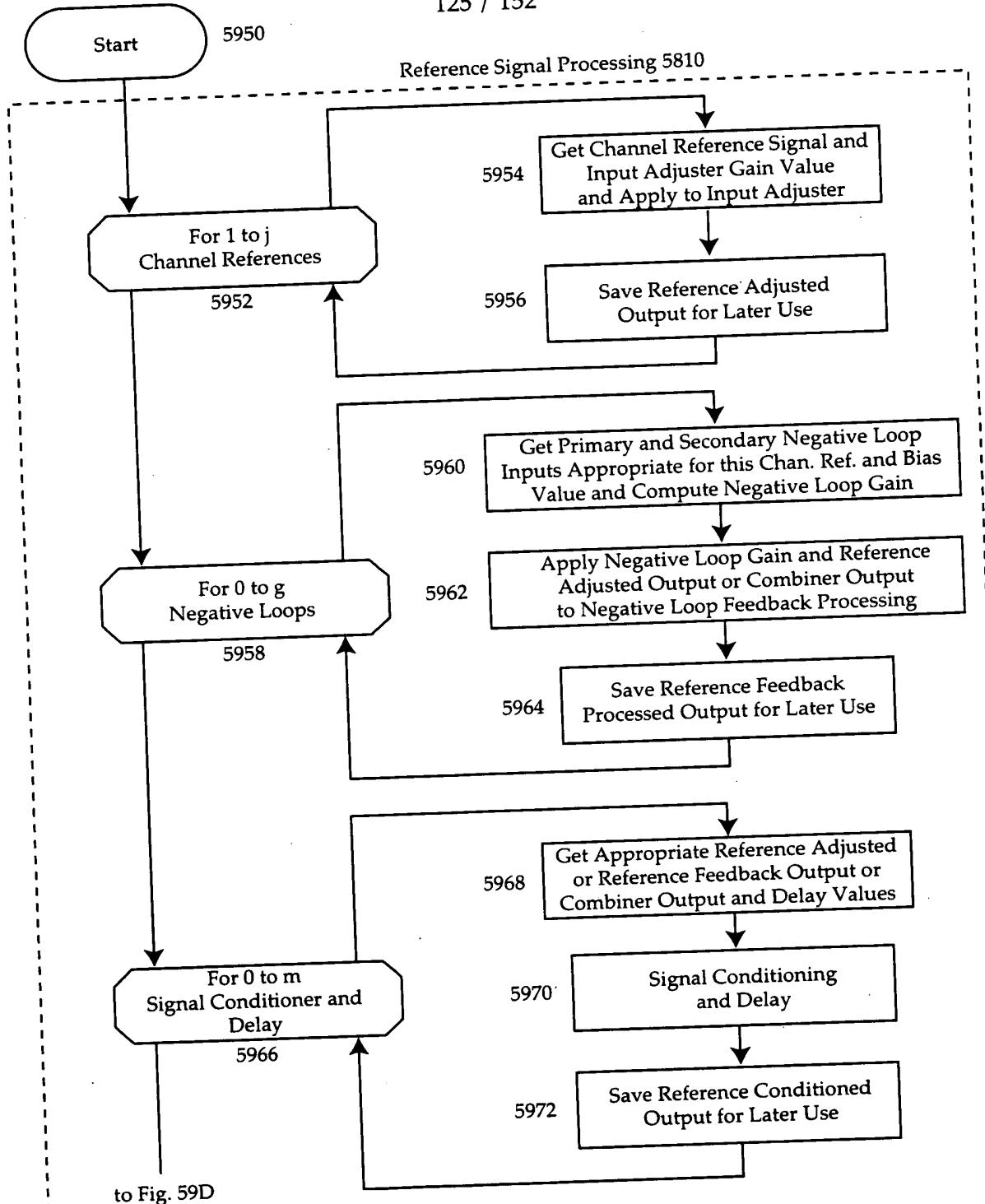


FIG. 59B

Start

5950

Reference Signal Processing 5810



to Fig. 59D

FIG. 59C

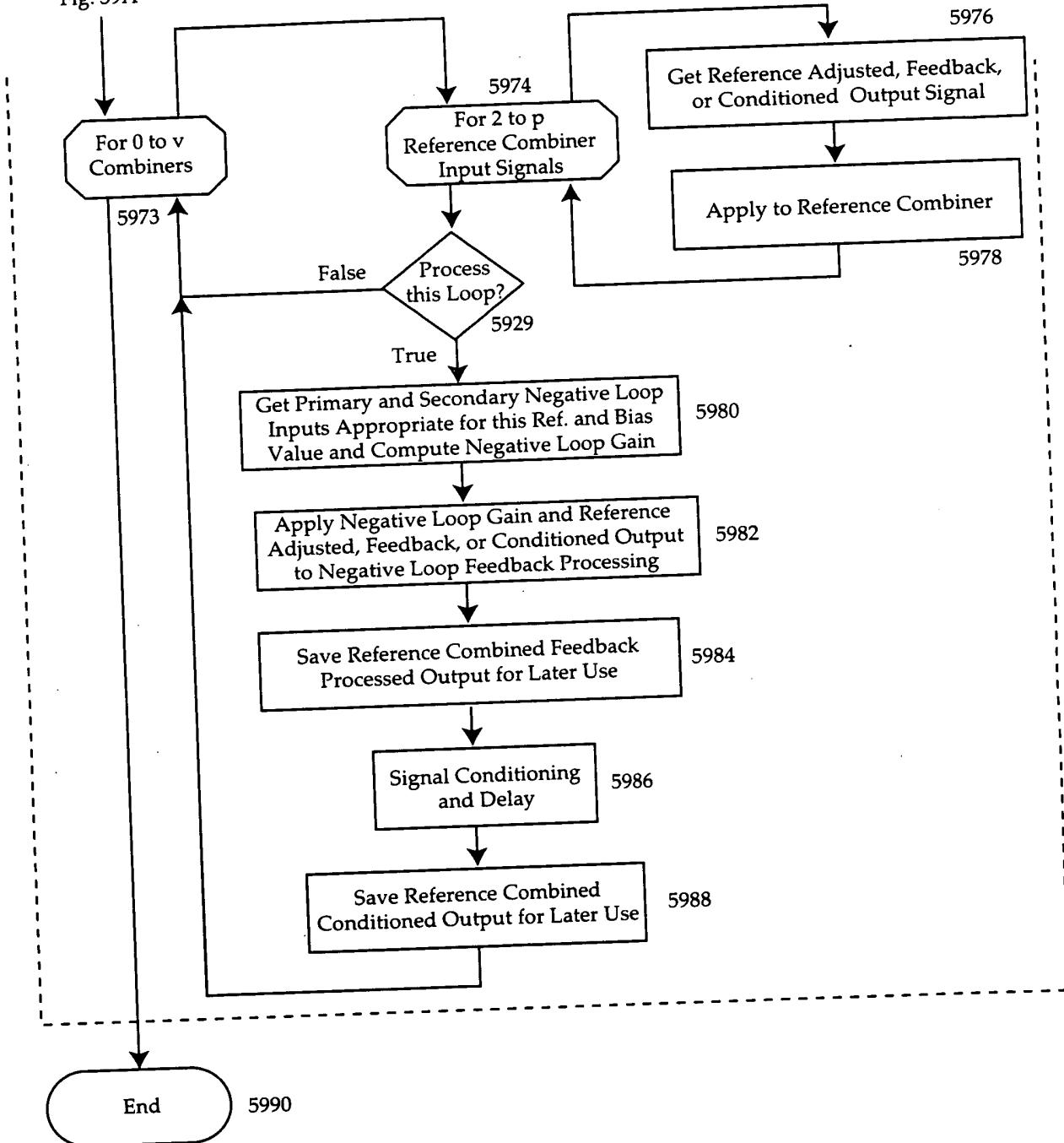
From
Fig. 59A

FIG. 59D

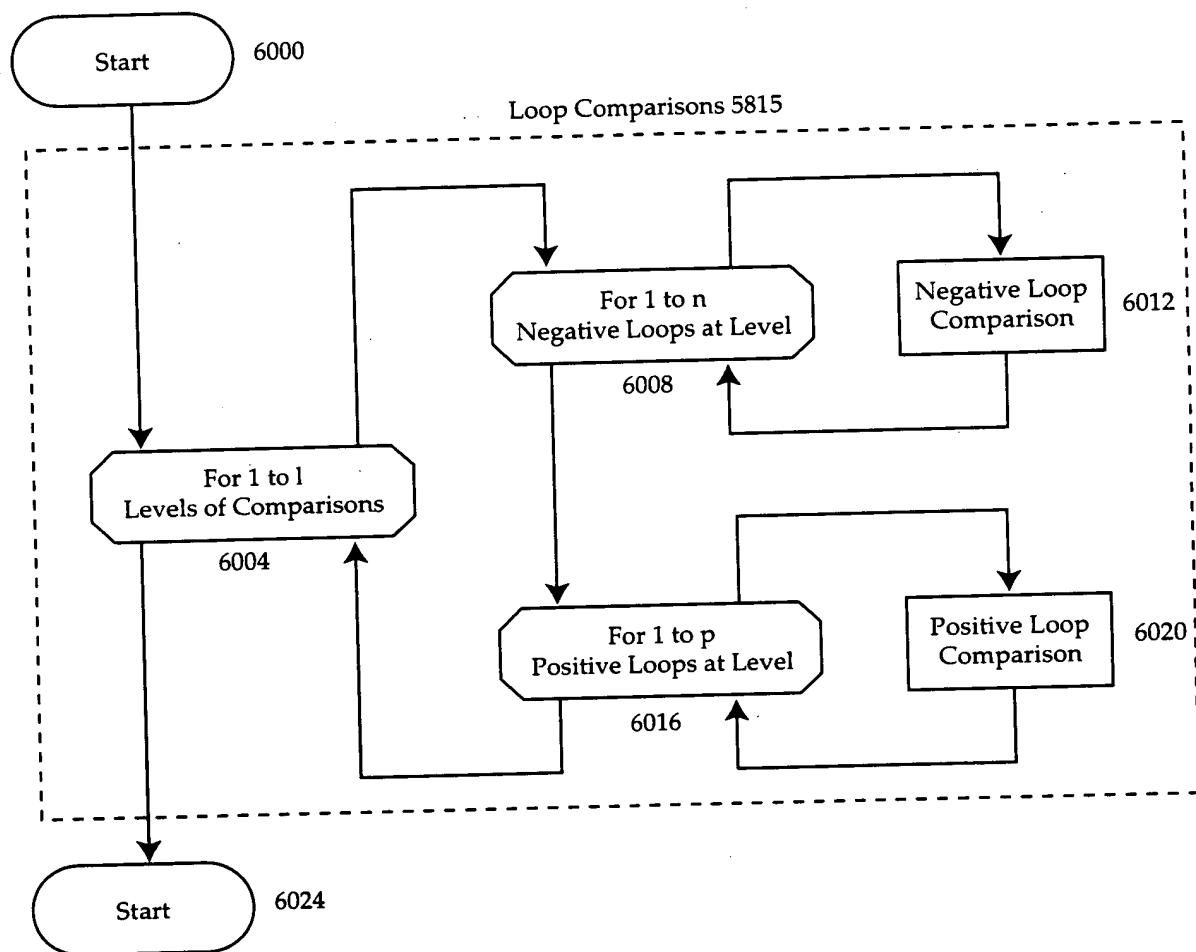


FIG. 60A

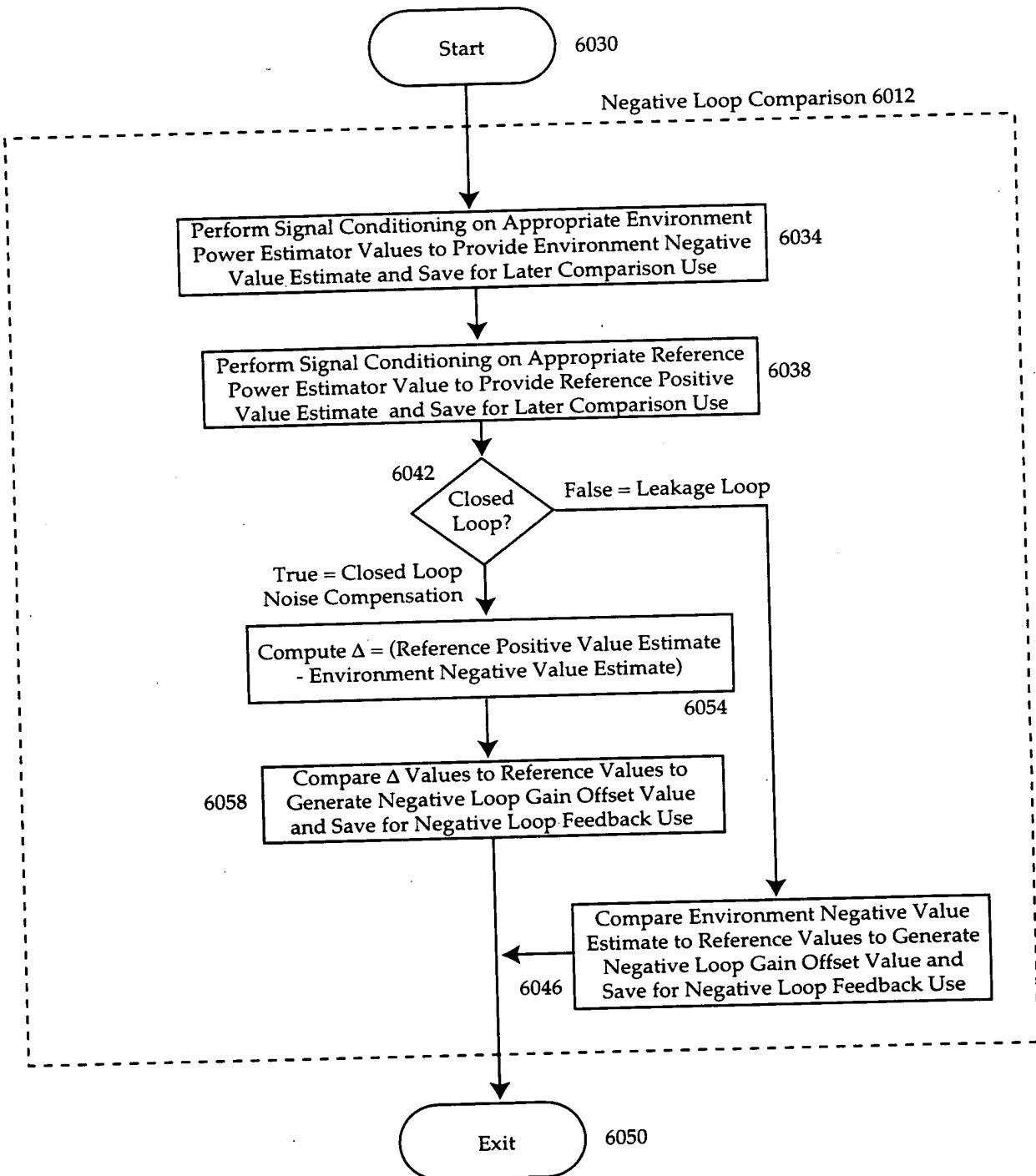


FIG. 60B

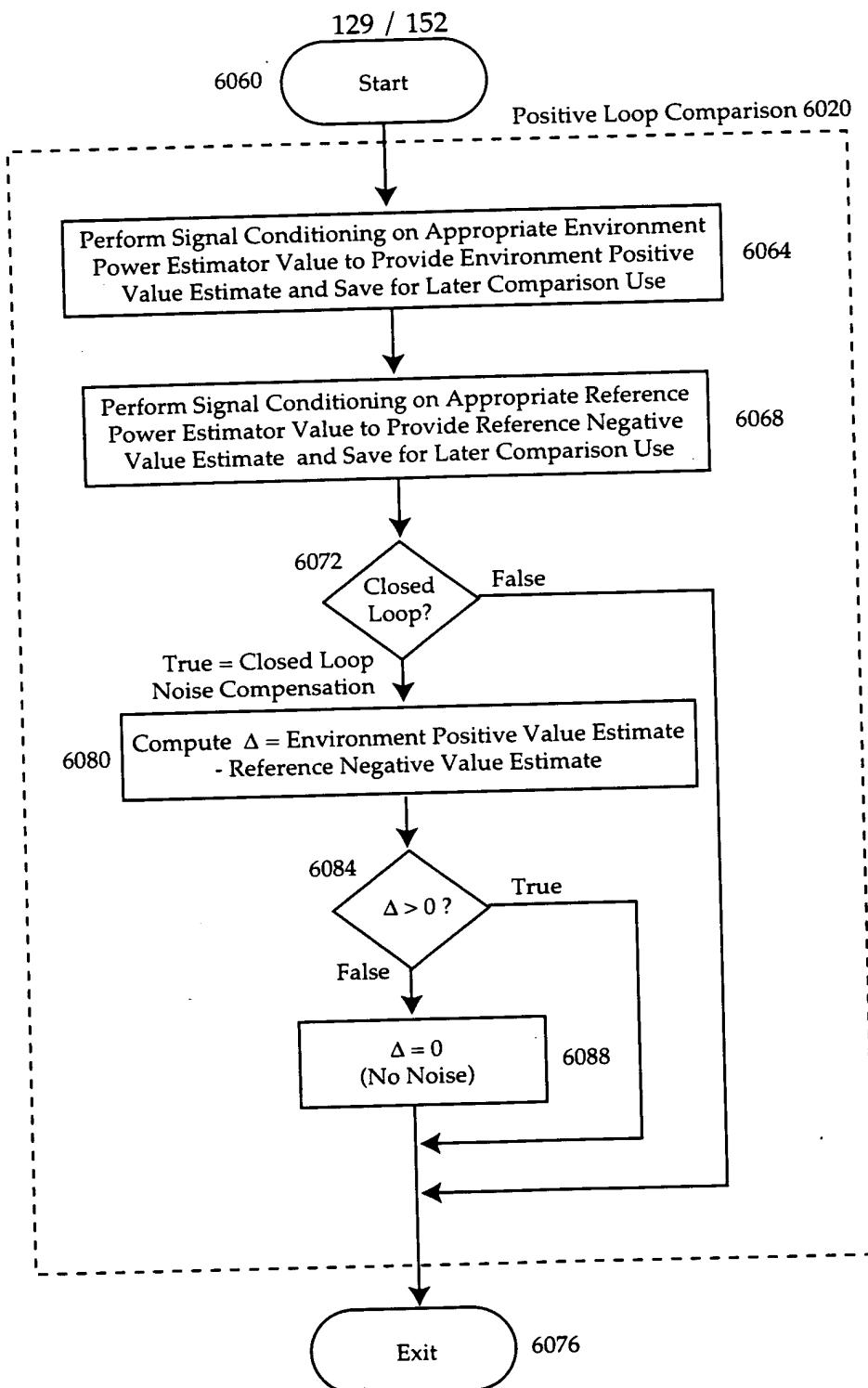


FIG. 60C

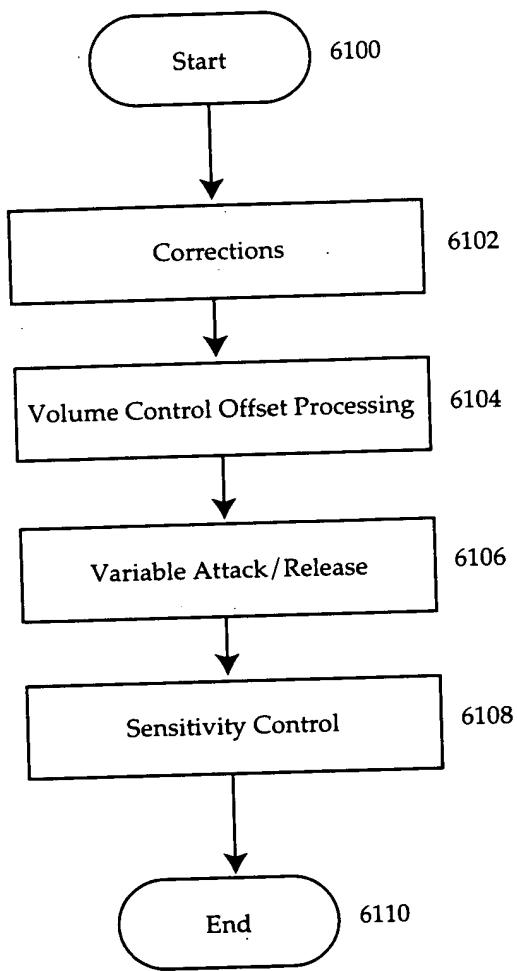


FIG. 61A

DRAFT - PENDING APPROVAL

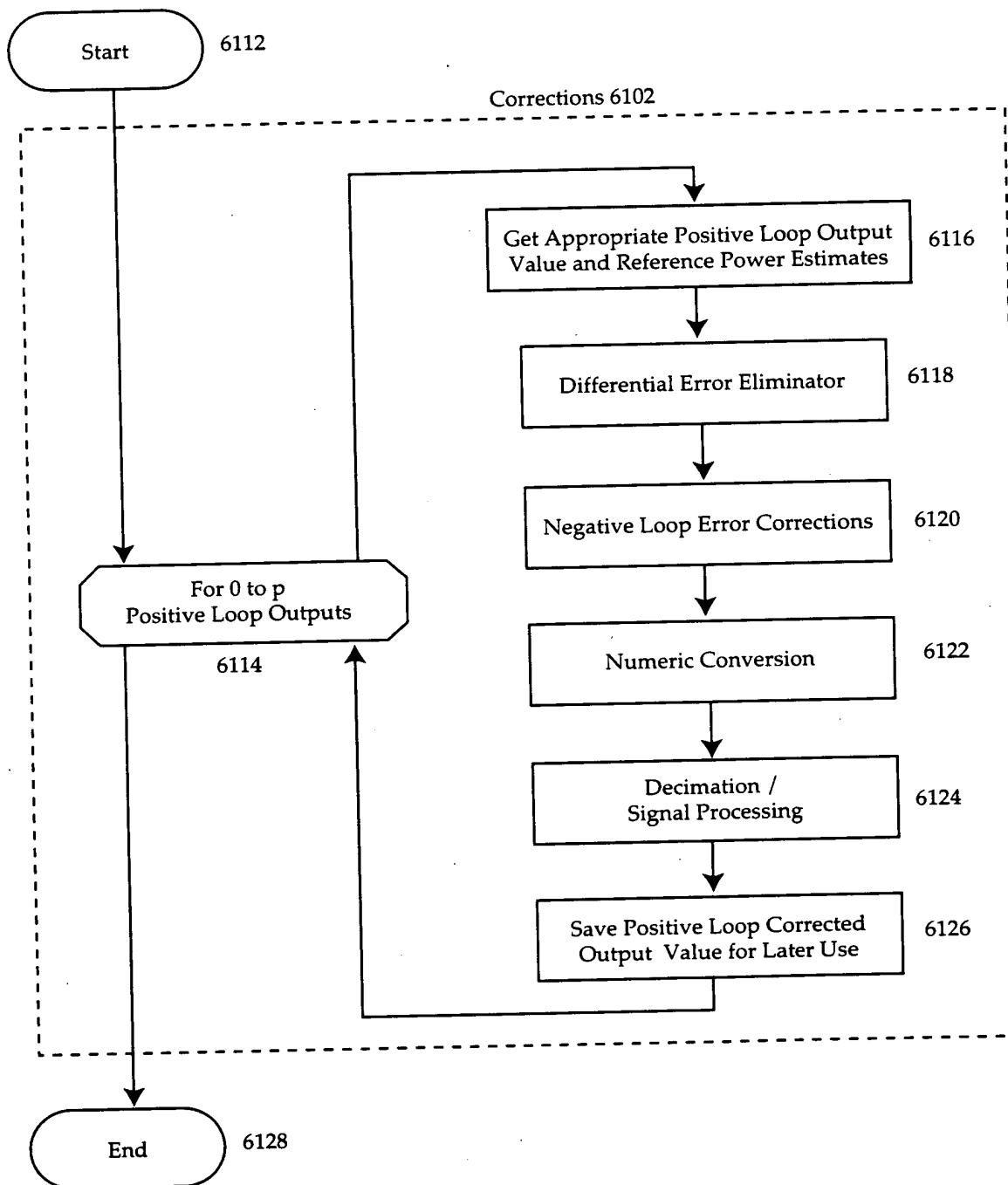


FIG. 61B

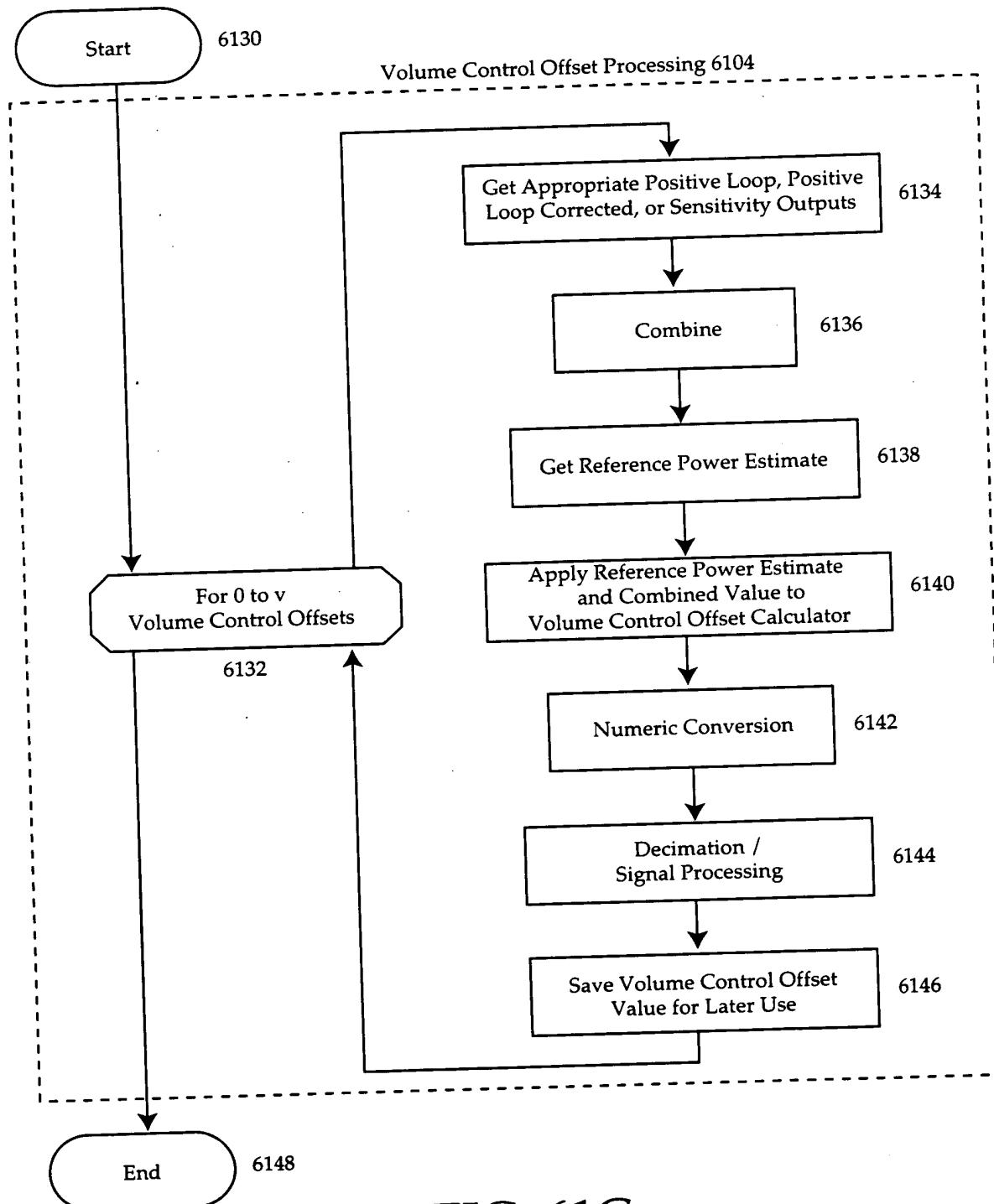


FIG. 61C

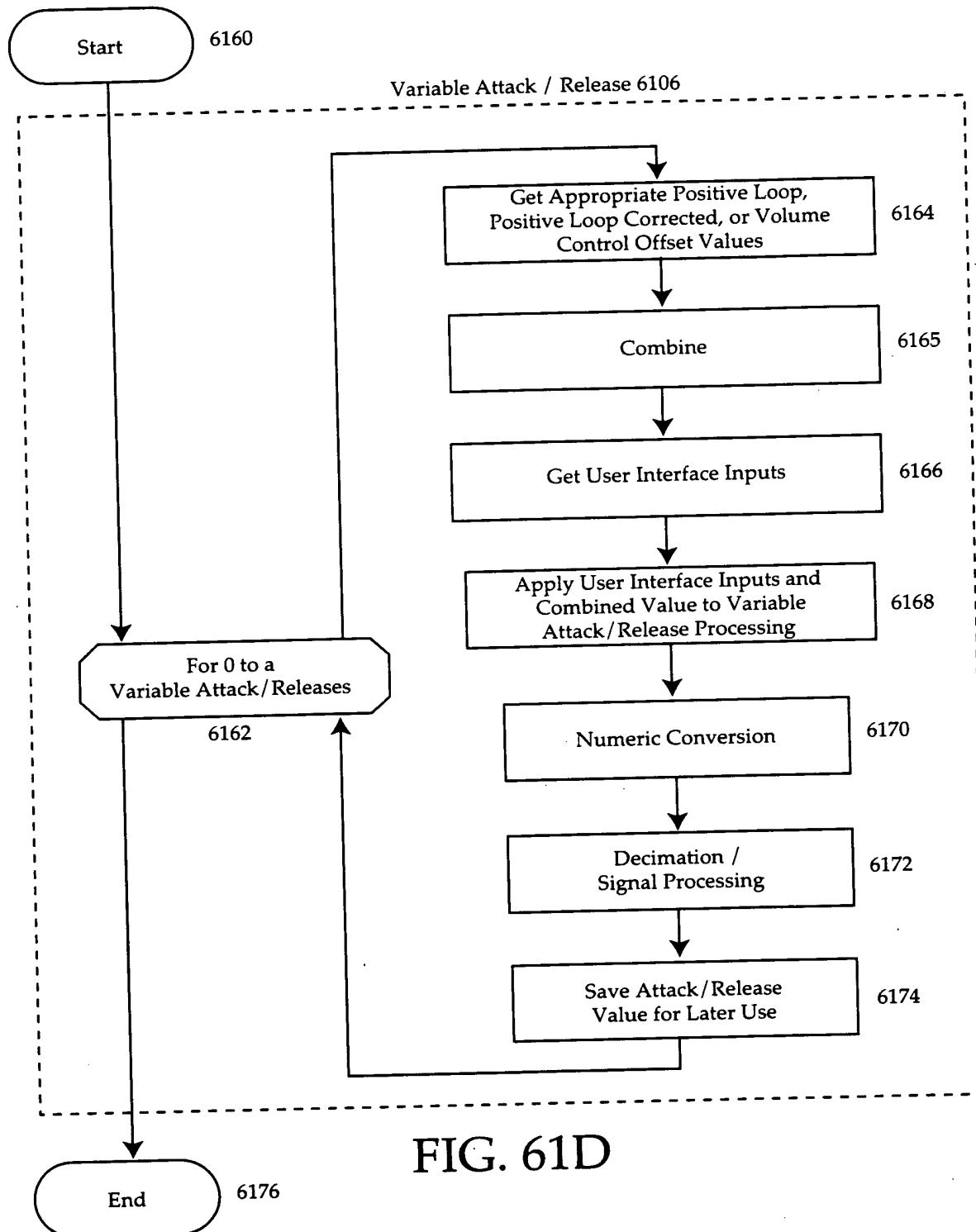


FIG. 61D

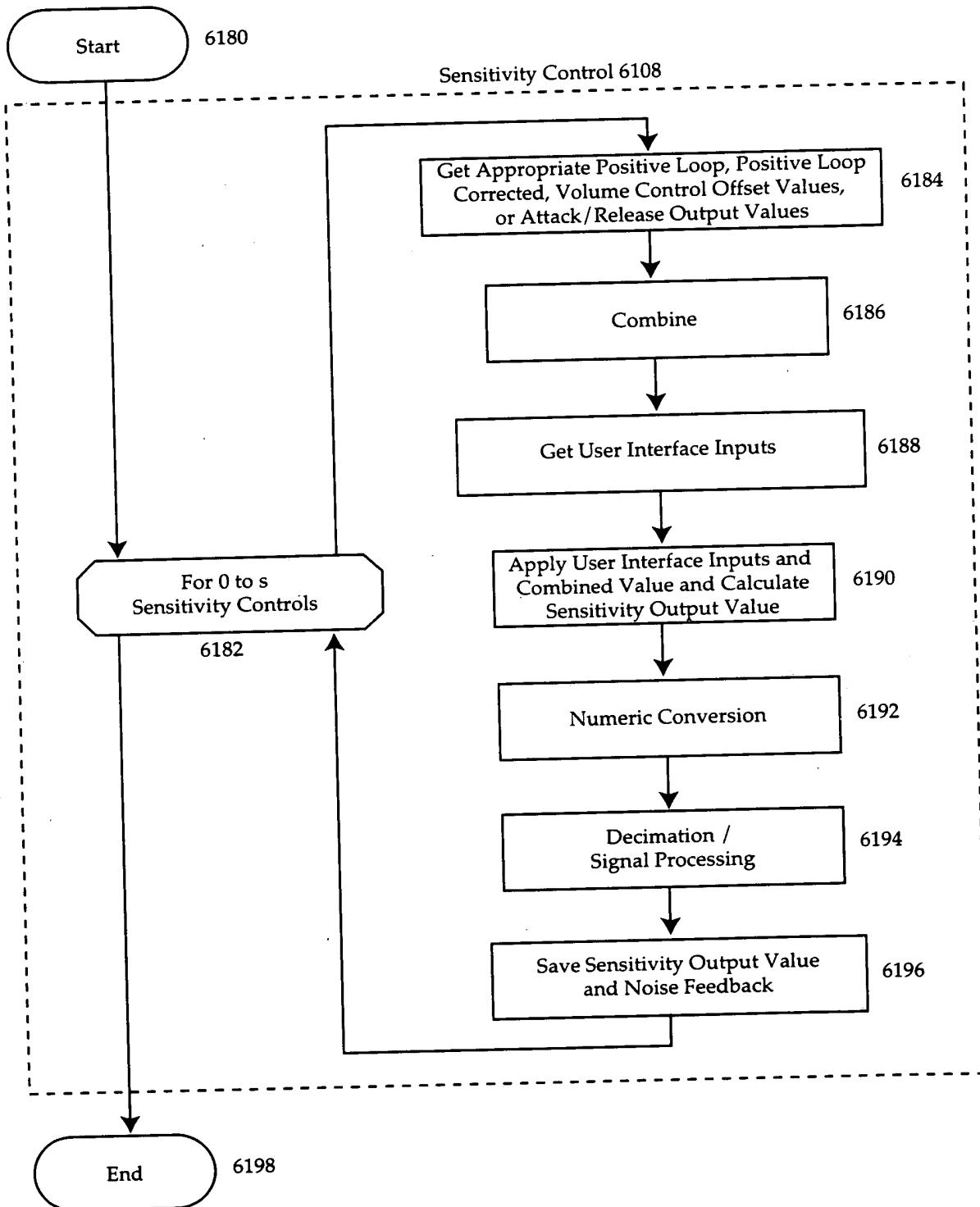


FIG. 61E

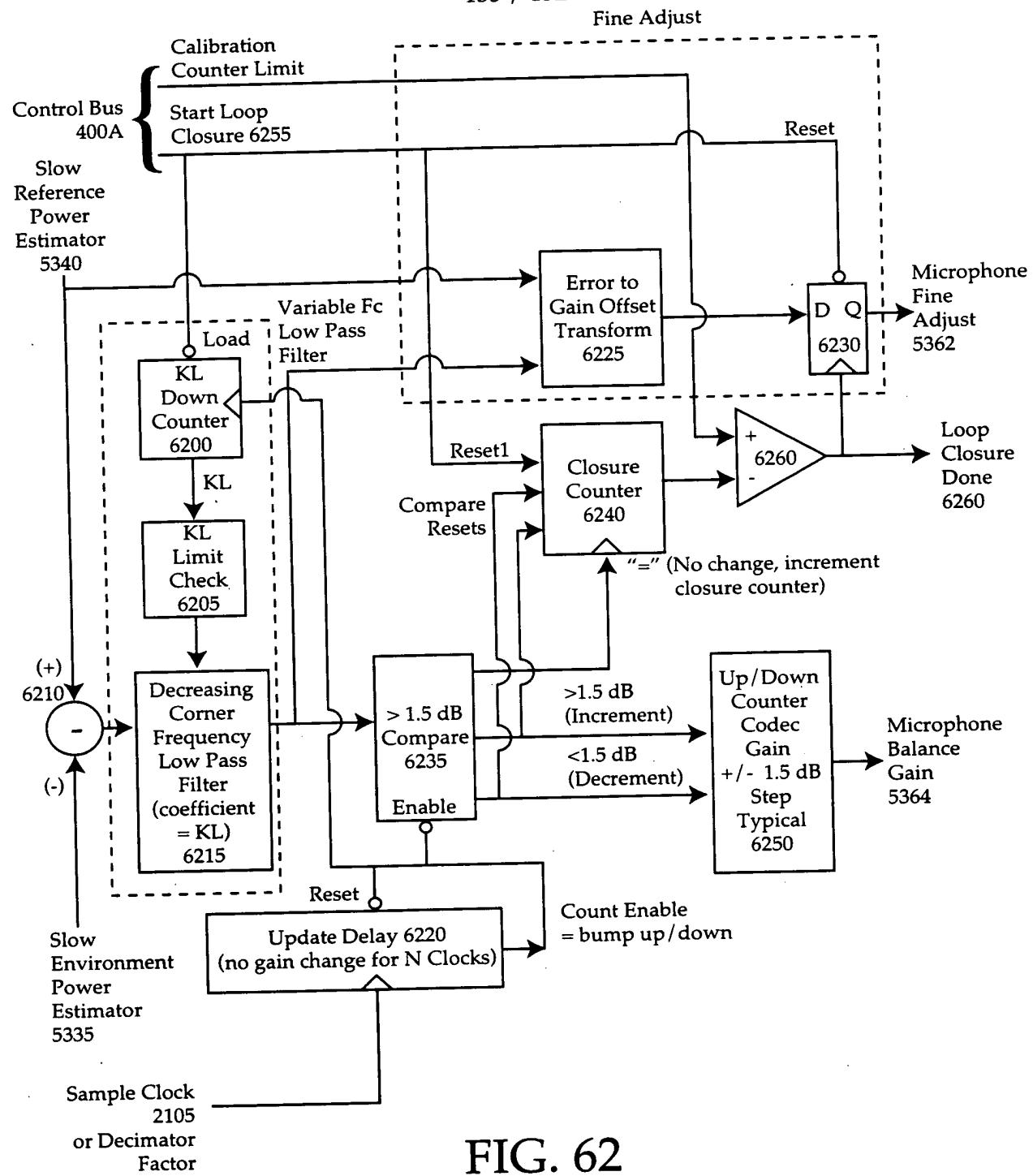


FIG. 62

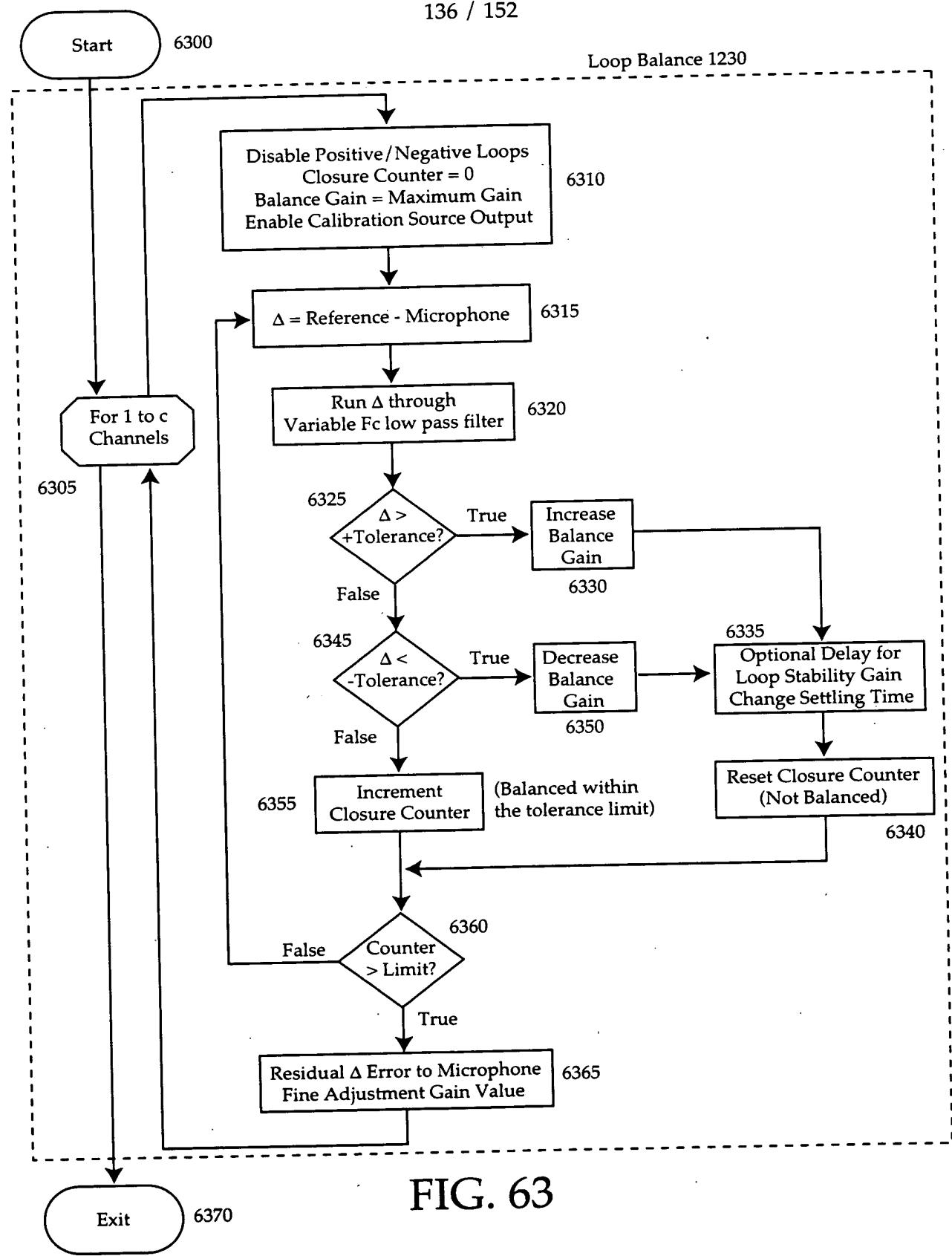


FIG. 63

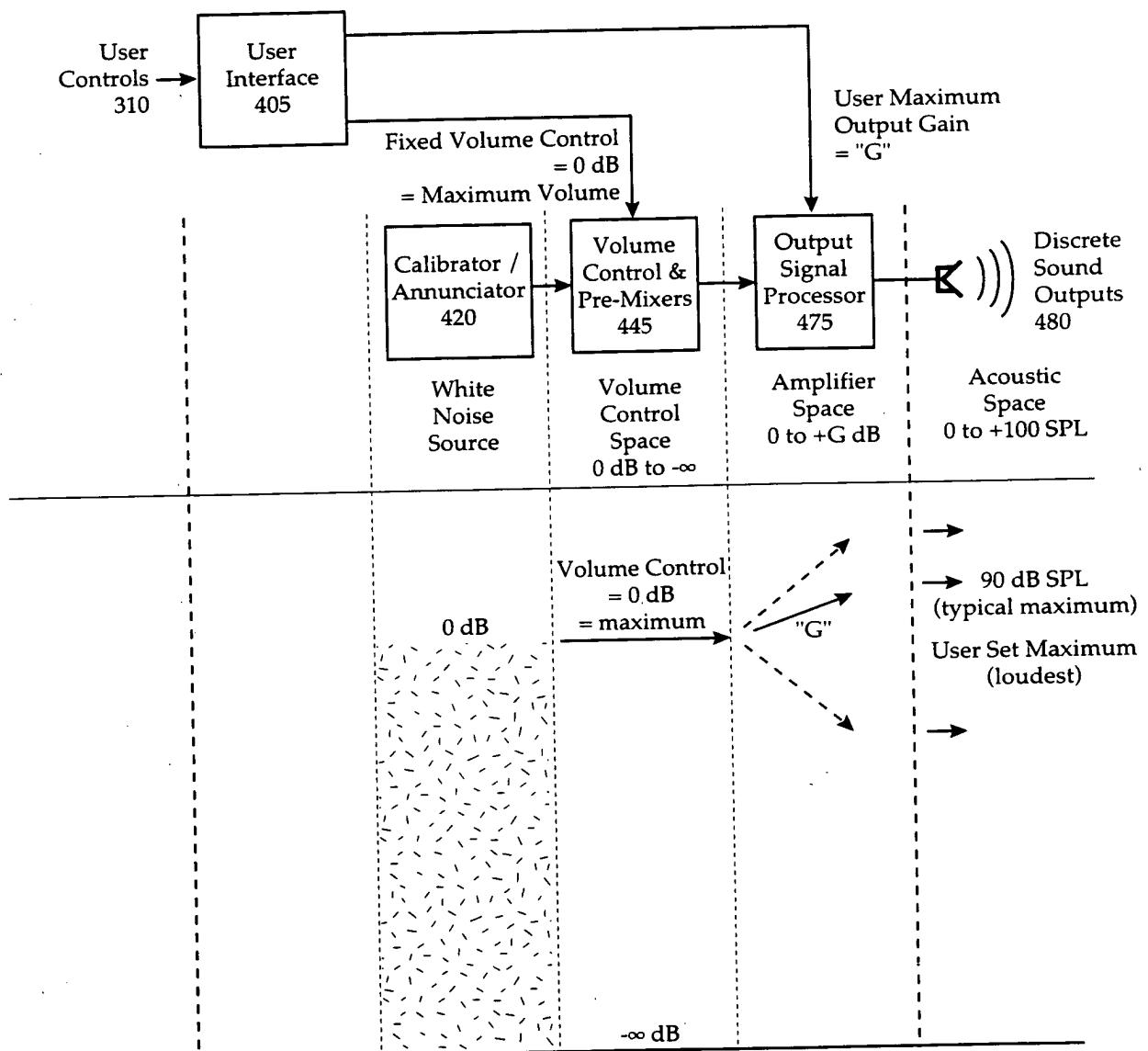


FIG. 64

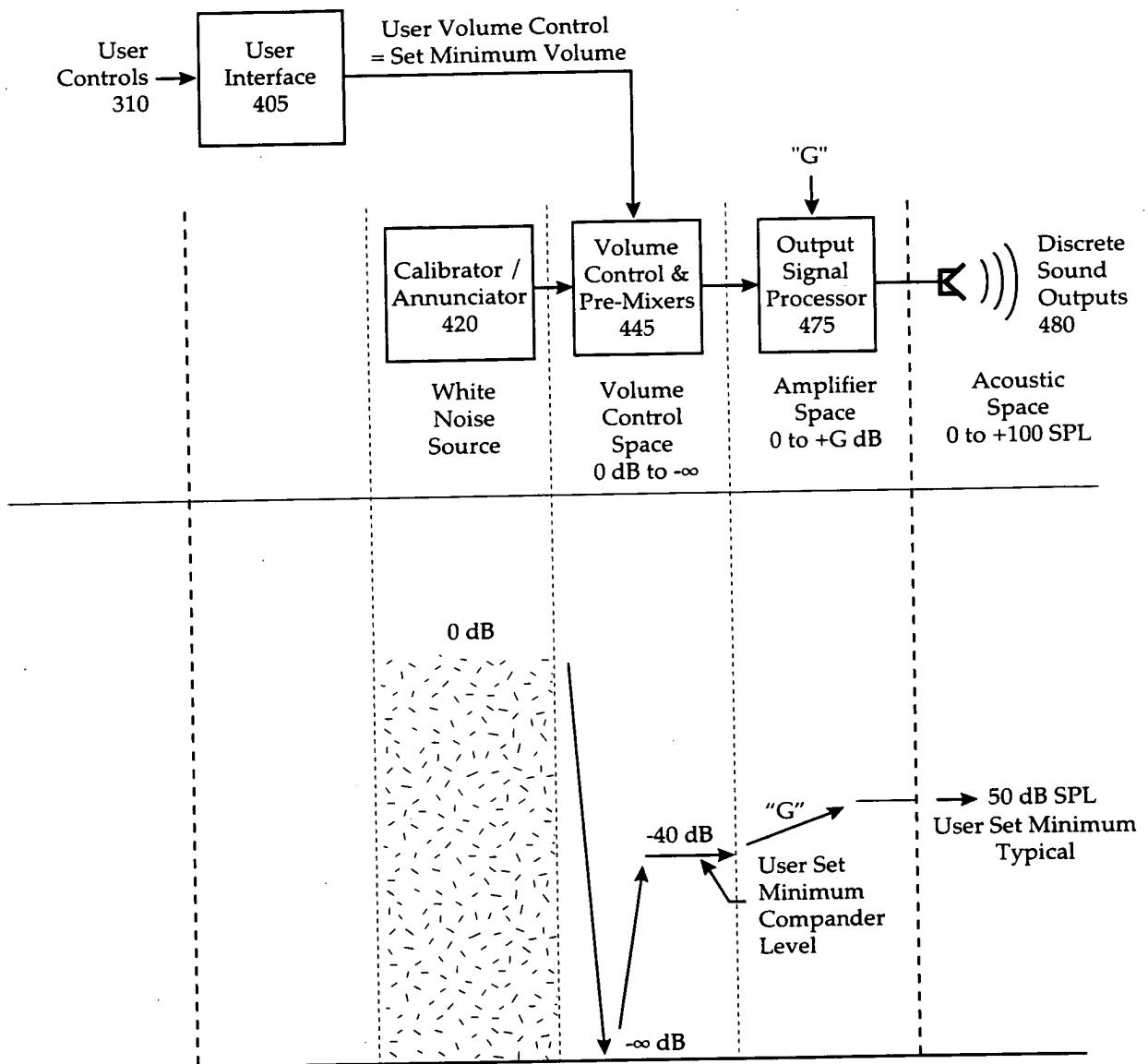


FIG. 65

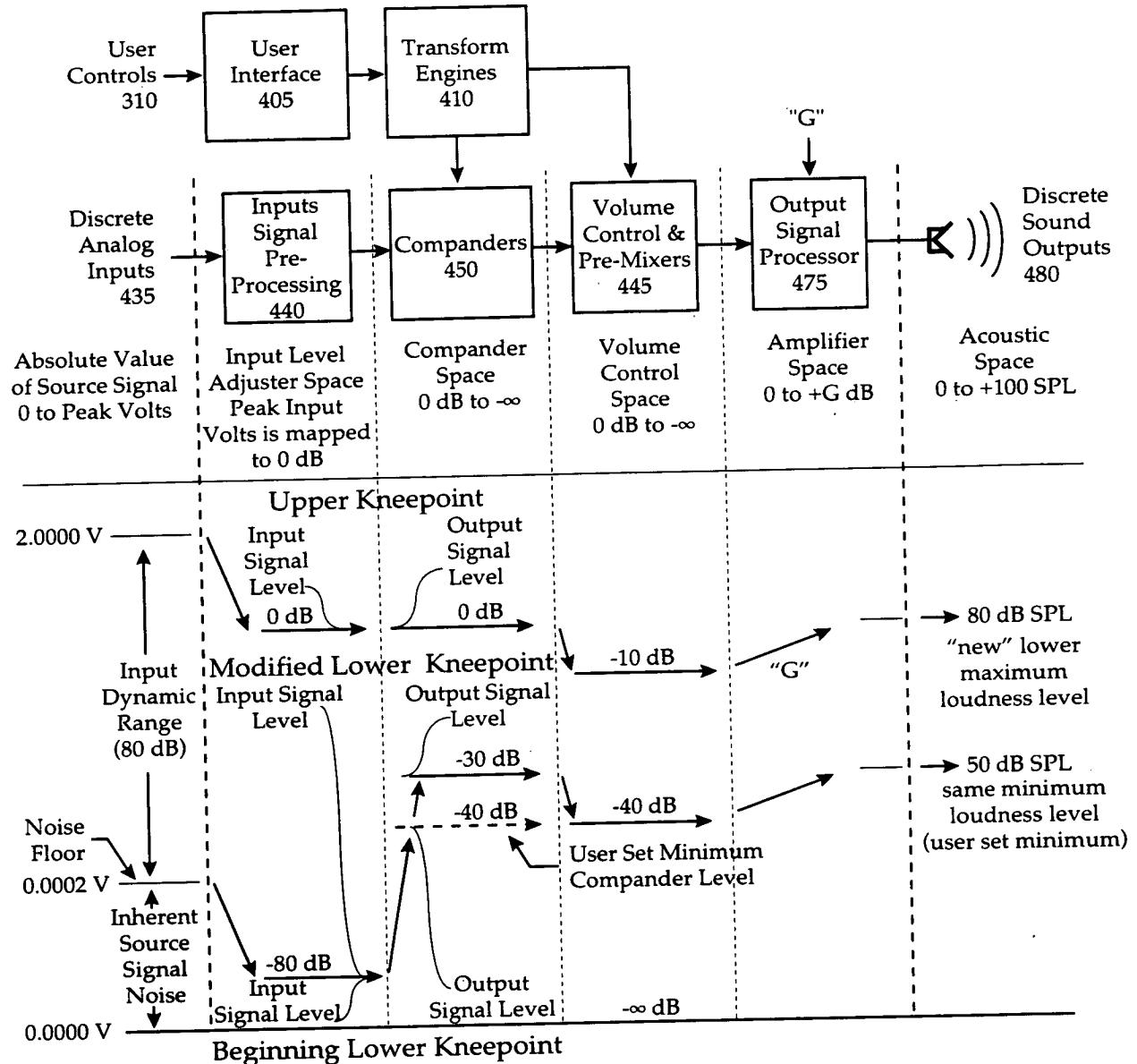


FIG. 66

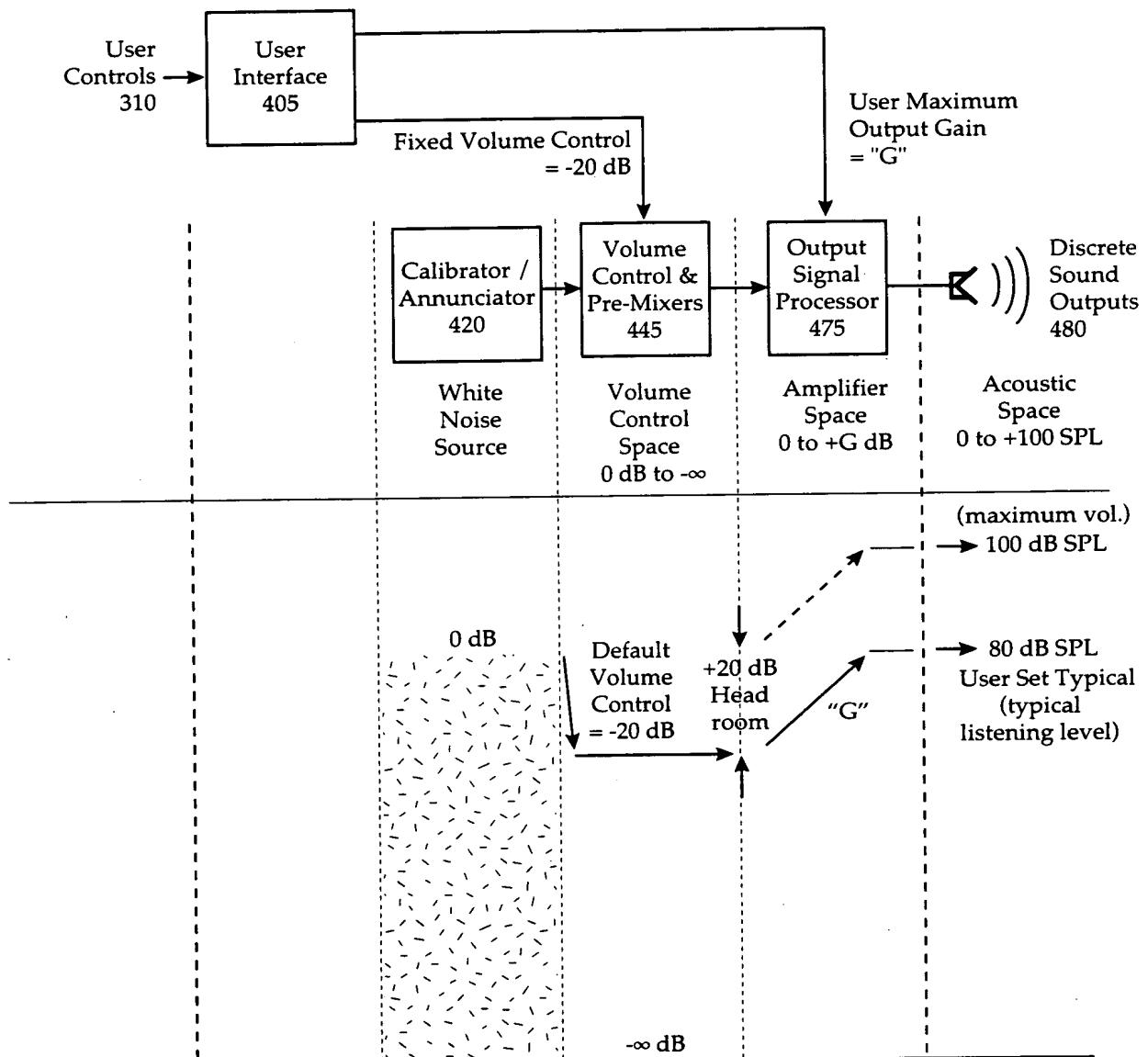


FIG. 67

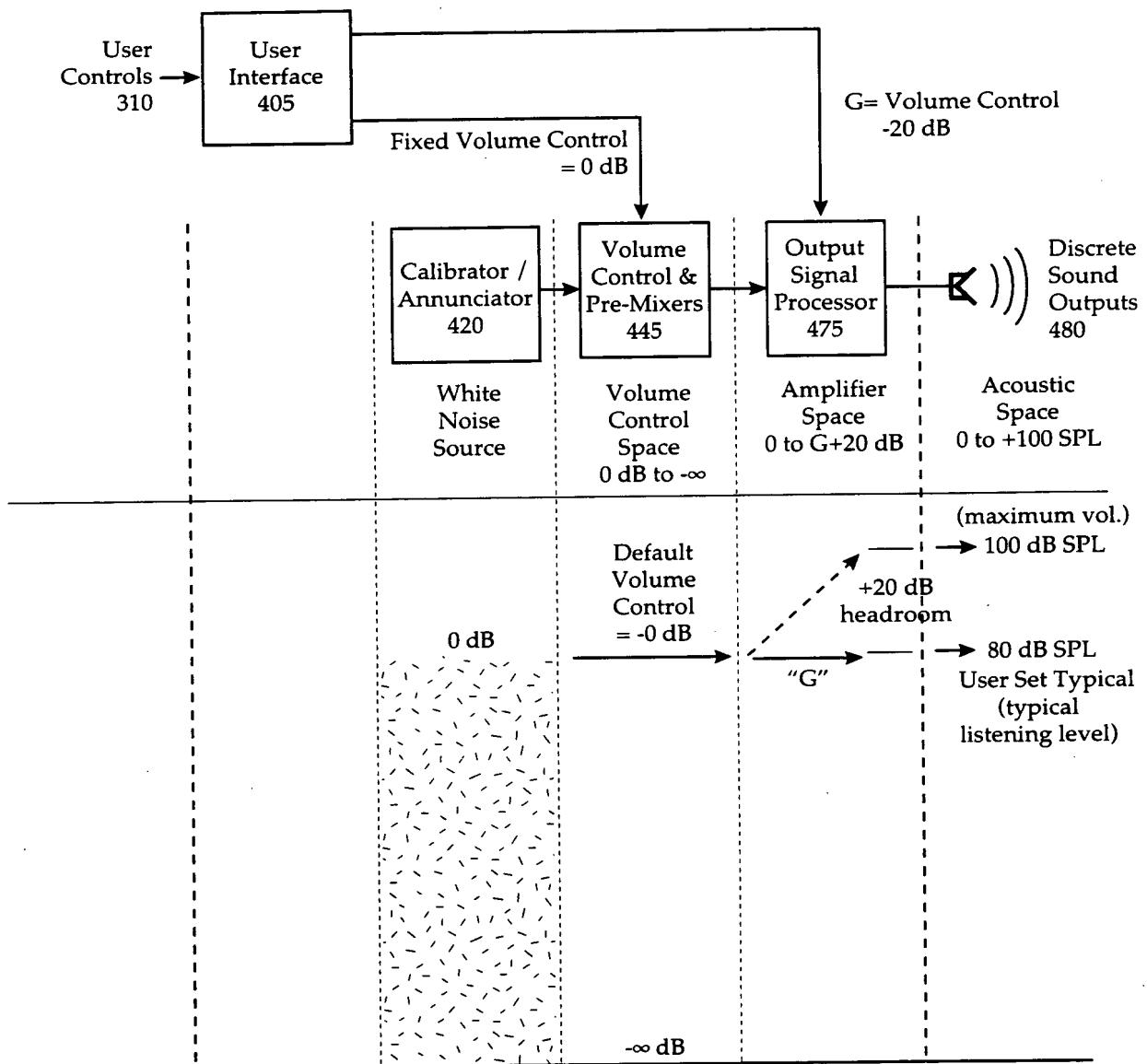


FIG. 68

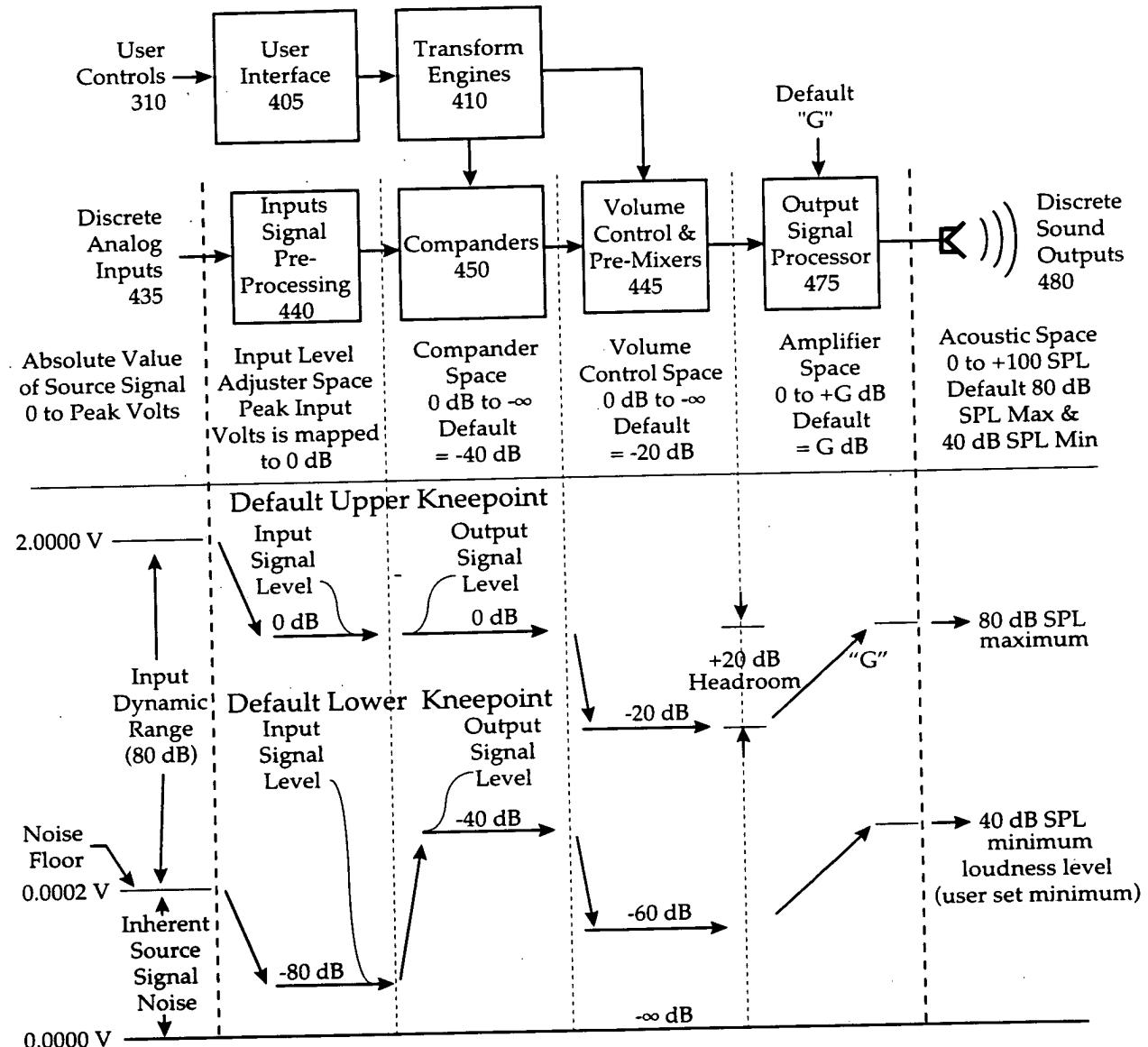


FIG. 69

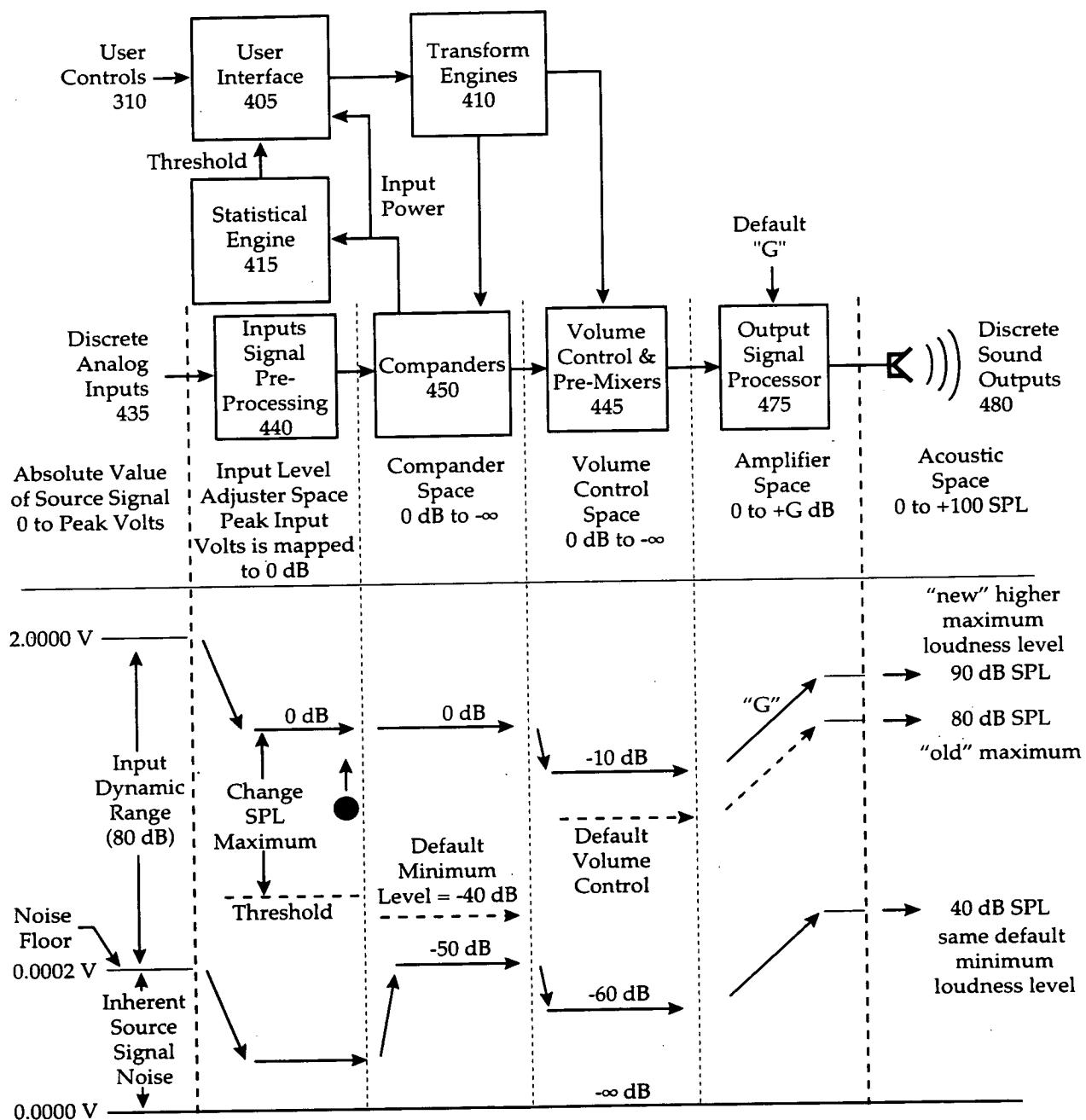


FIG. 70

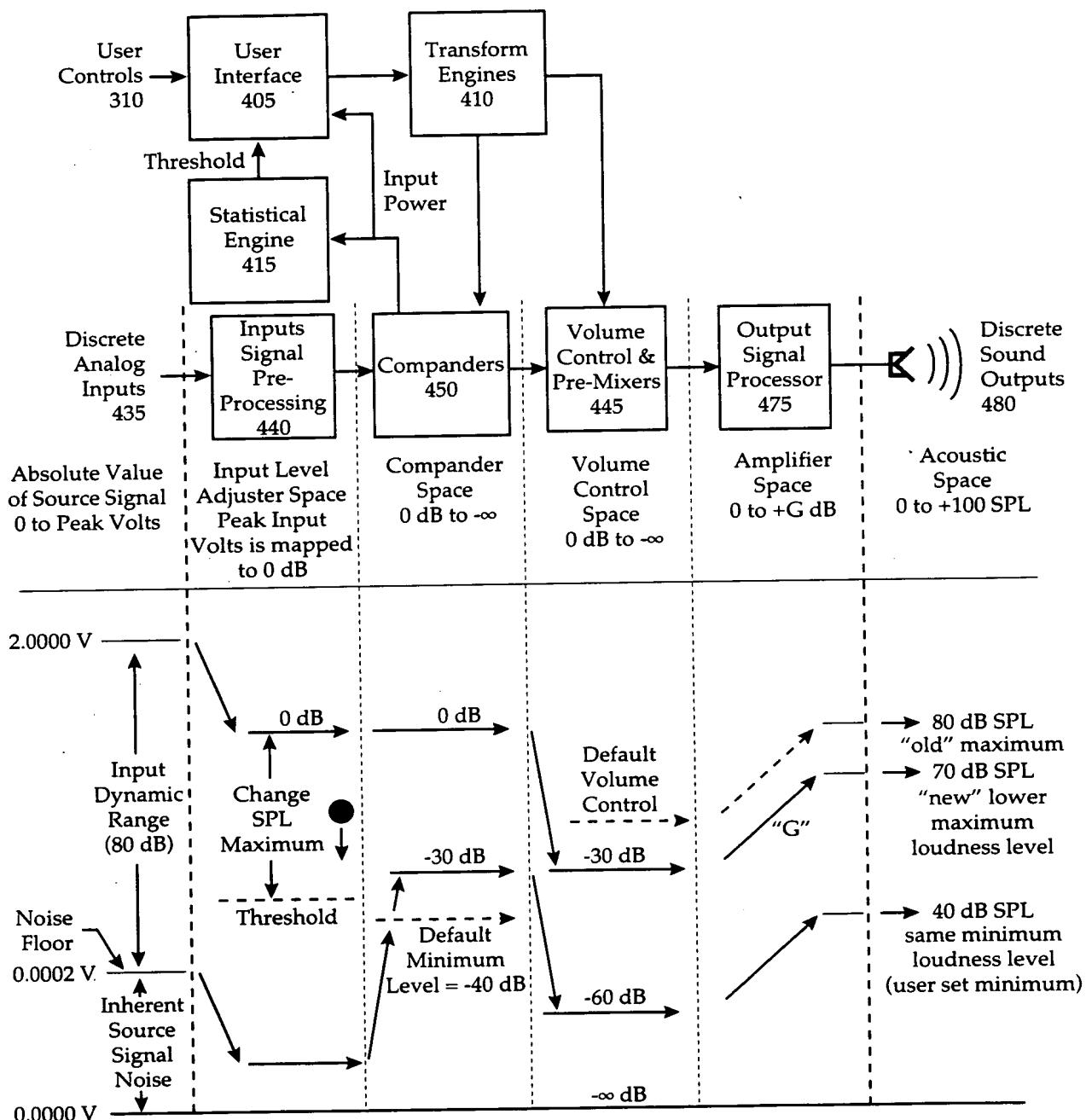


FIG. 71

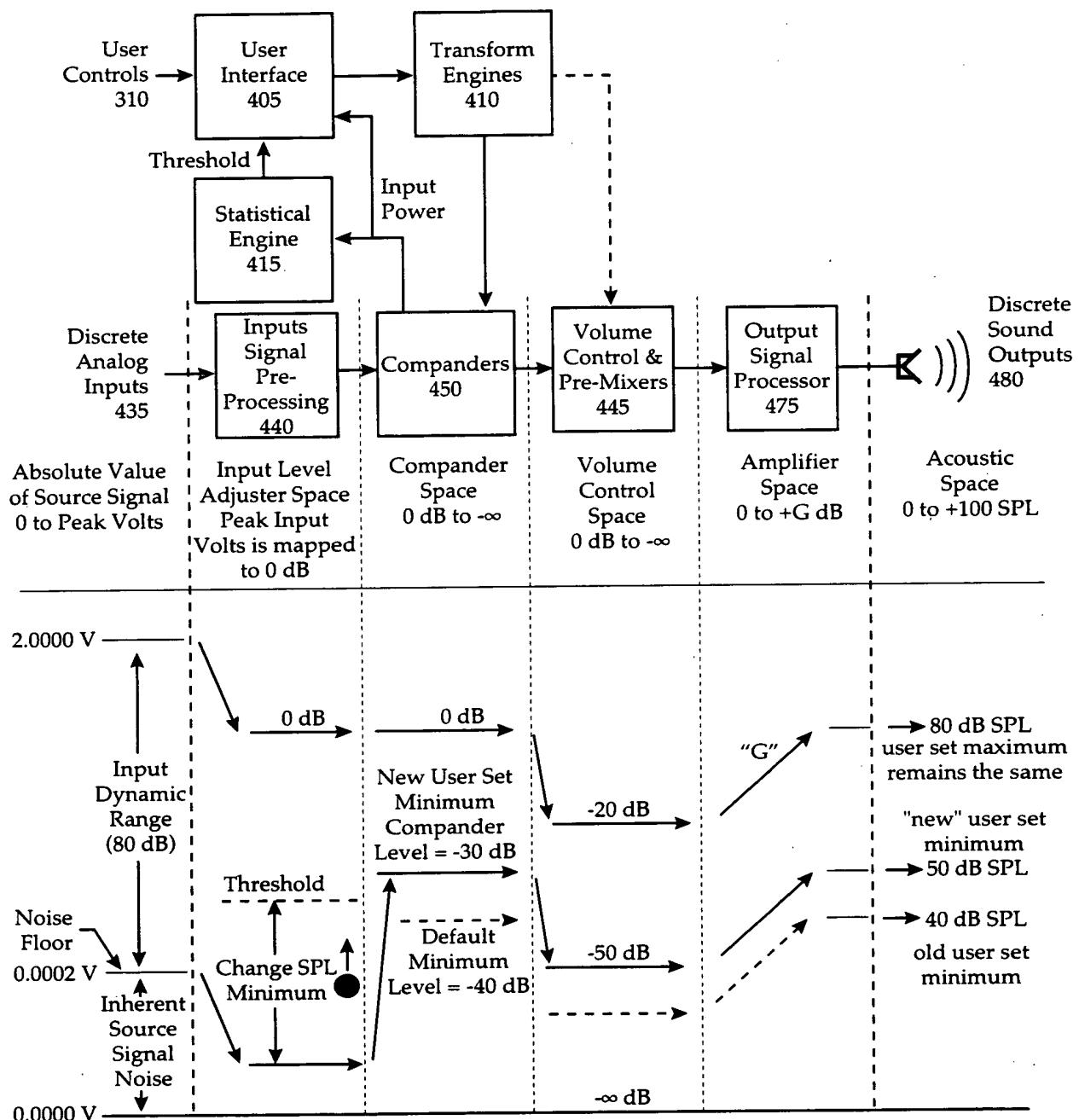


FIG. 72

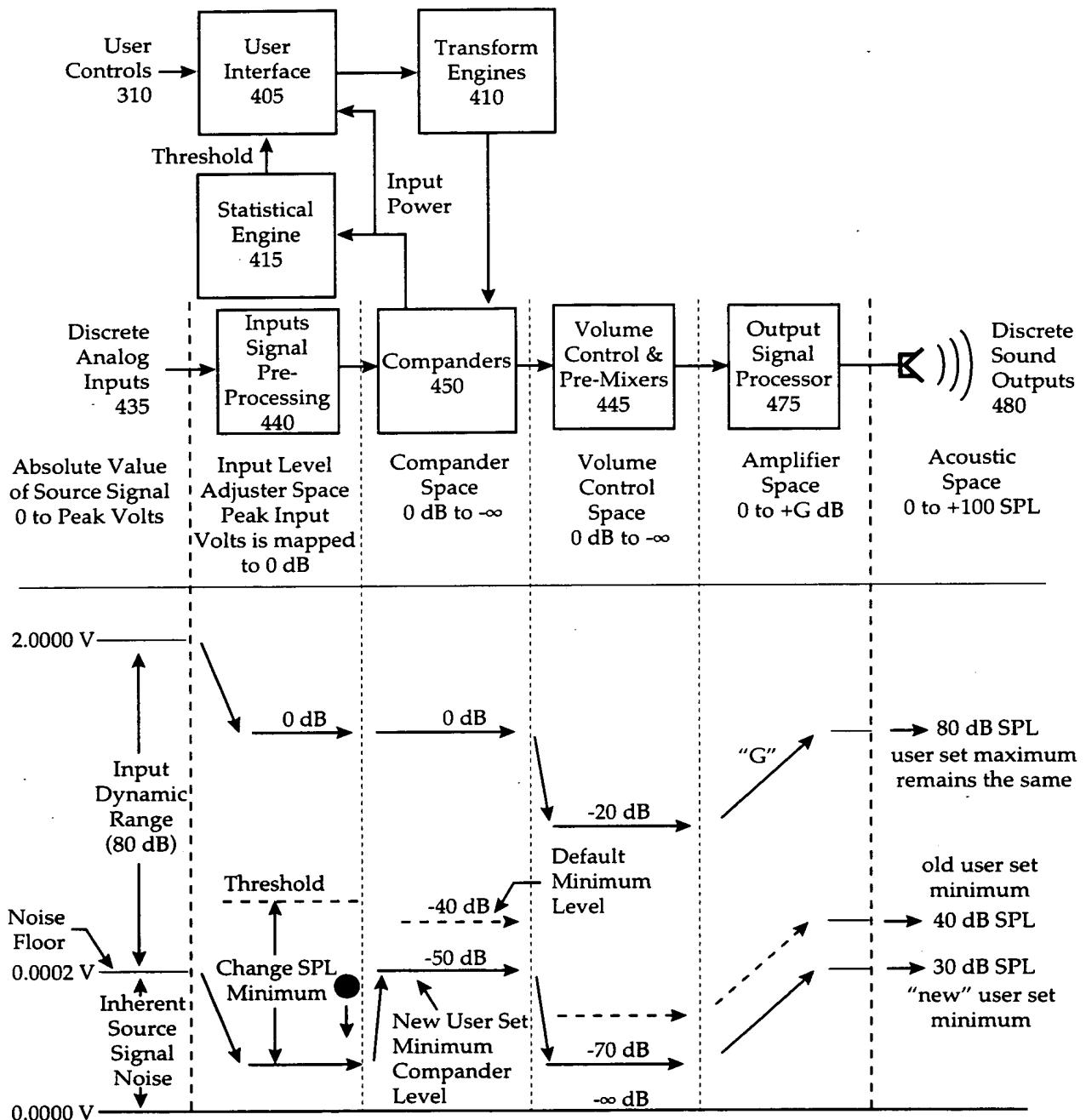


FIG. 73

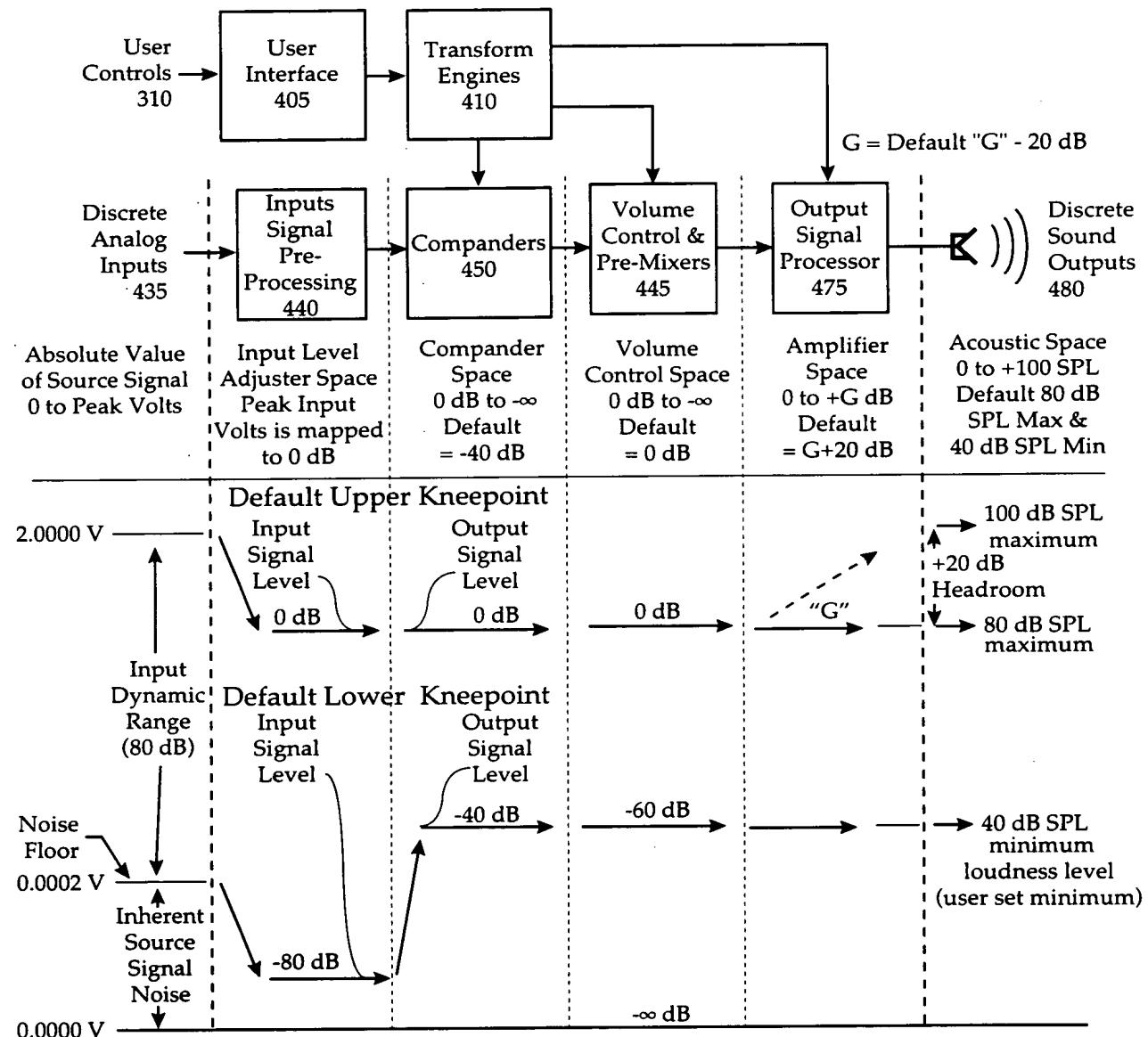


FIG. 74

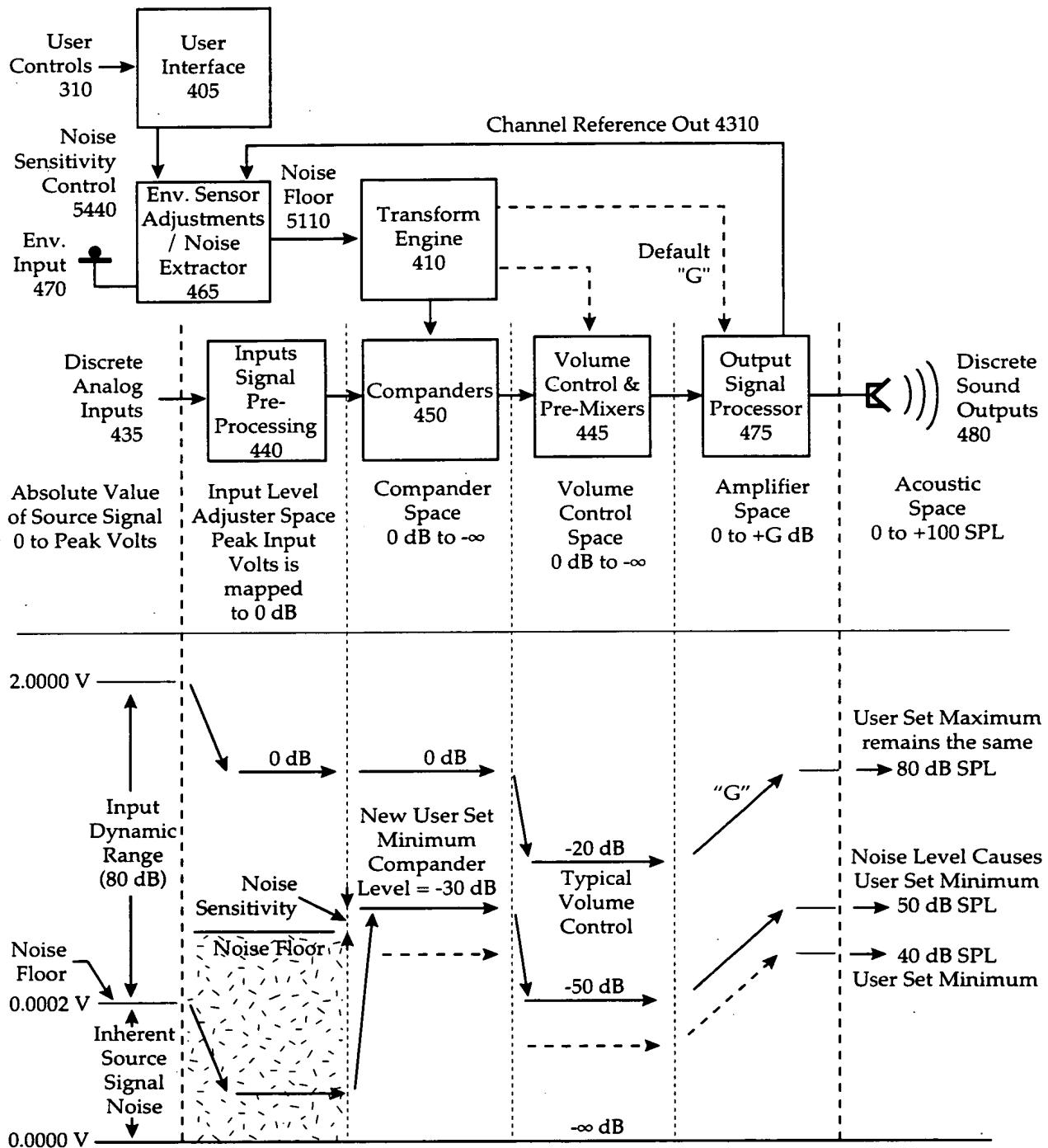


FIG. 75

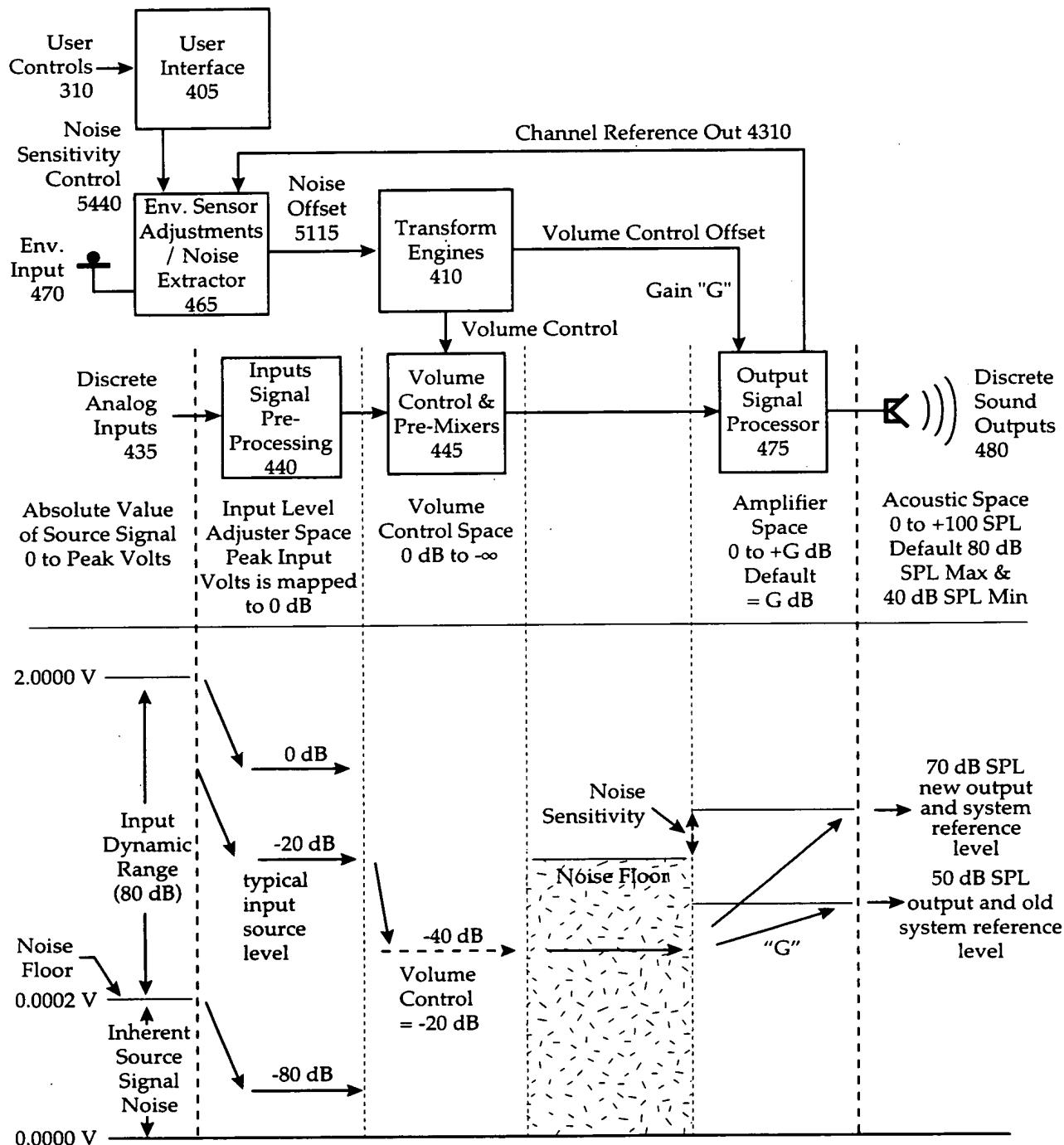


FIG. 76

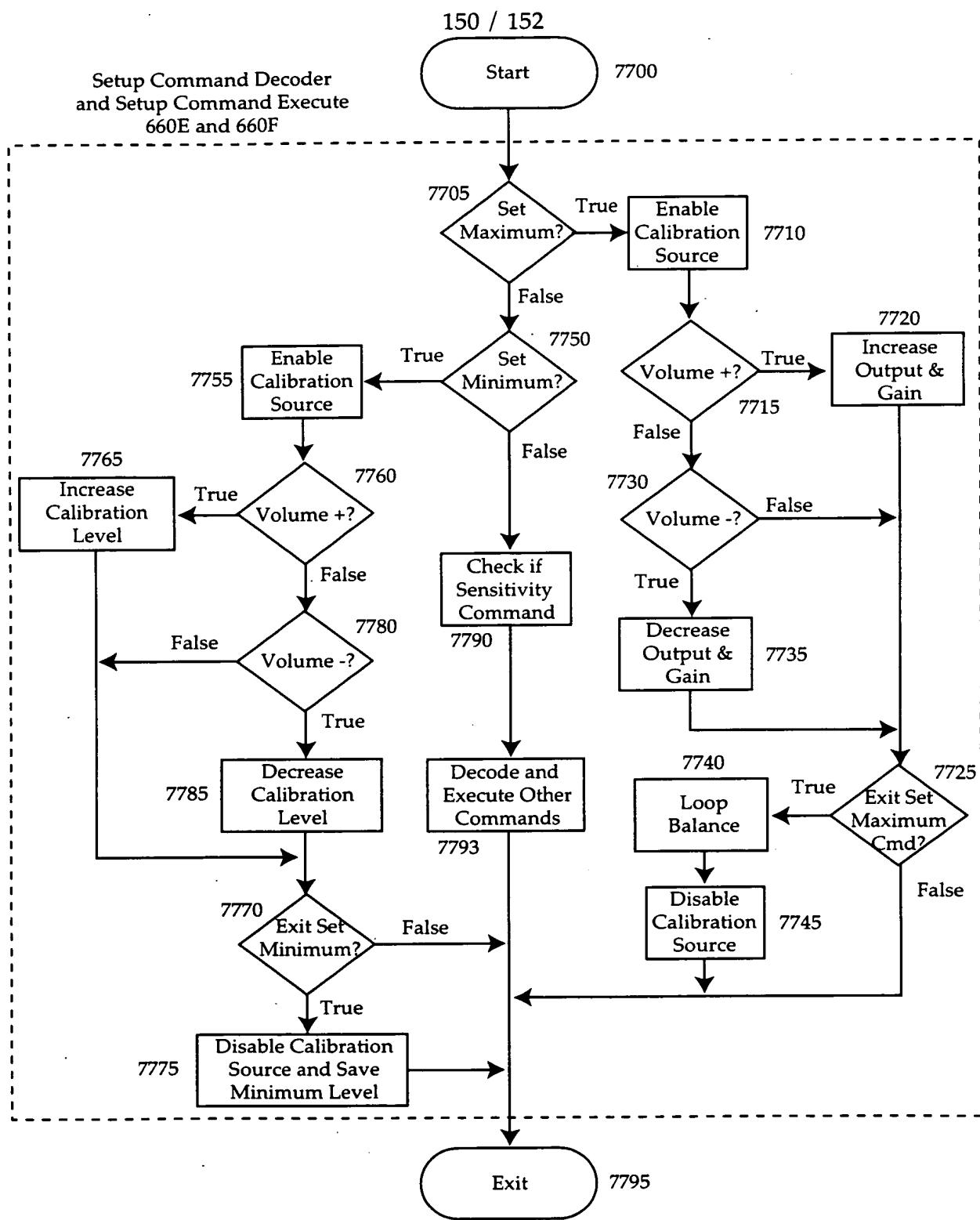


FIG. 77

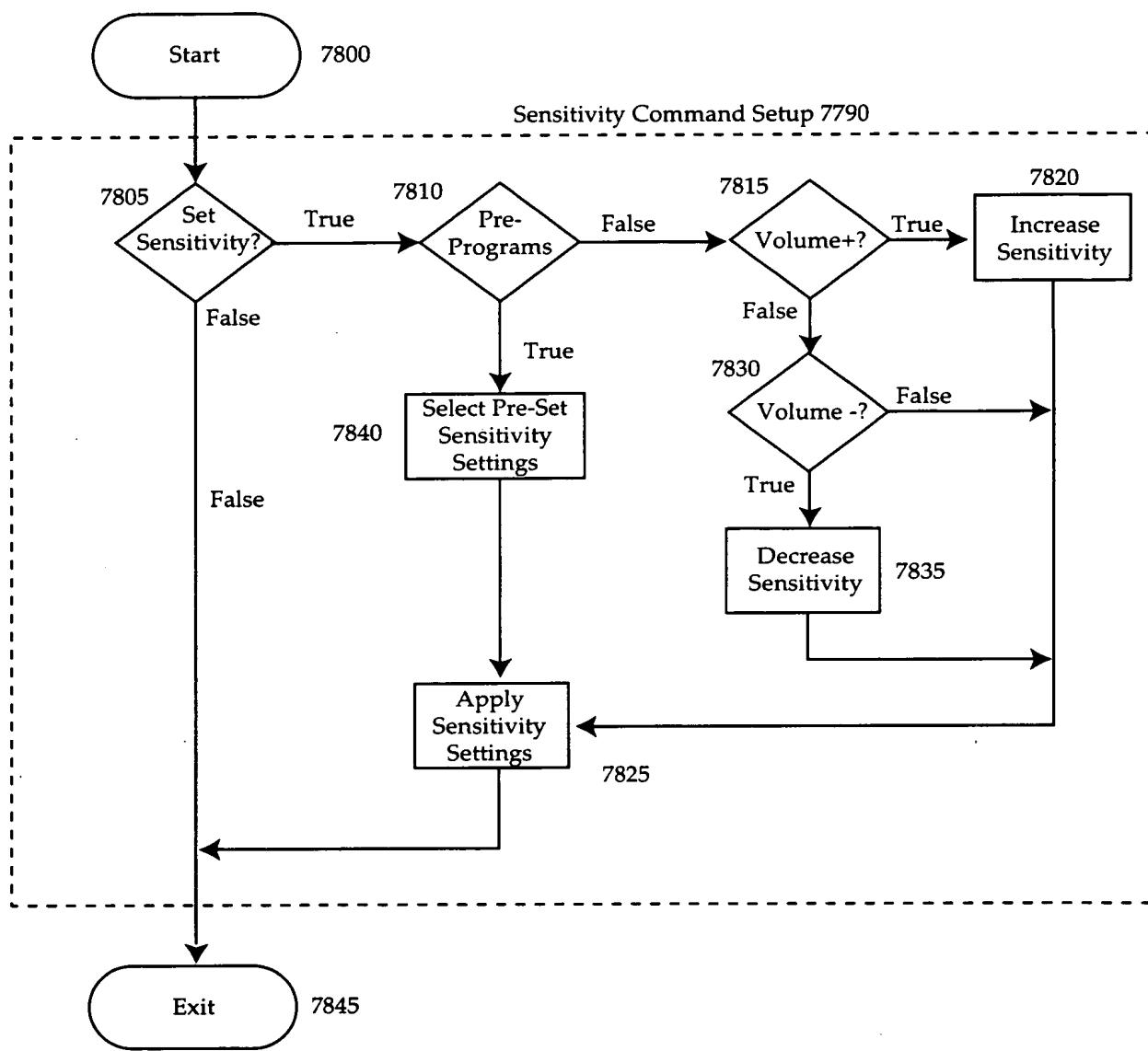


FIG. 78

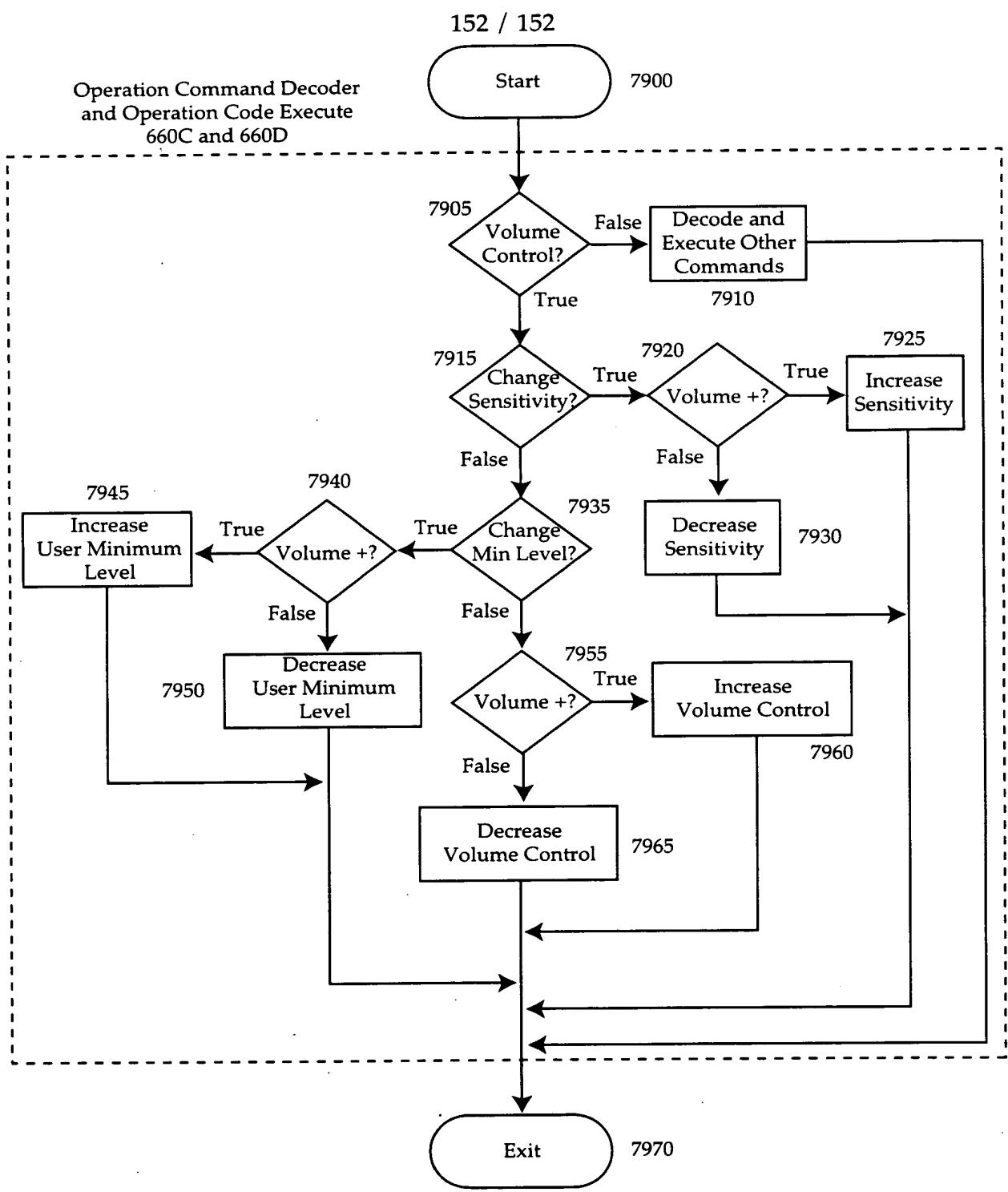


FIG. 79